

DATA SHEET

ANTI-SULFURATED ARRAY CHIP RESISTORS AUTOMOTIVE GRADE

AF122 (4Pin/2R) / AF124 (8Pin/4R) / AF162 (4Pin/2R)/ AF164 (8Pin/4R)

5%, 1%

sizes 2 × 0402, 4 × 0402, 2 × 0603, 4 × 0603 RoHS compliant



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SCOPE

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This specification describes AFI22/AFI24/AFI62/AFI64 (convex)series chip resistor arrays with lead-free terminations made by thick film process.

<u>APPLICATIONS</u>

- Terminal for SDRAM and **DDRAM**
- High-end Computer & Multimedia Electronics in high sulfur environment
- Consume electronic equipments: PDAs, PNDs
- Mobile phone, telecom...

FEATURES

- AEC-Q200 qualified
- RoHS compliant
- Reducing environmentally hazardous wastes
- High component and equipment reliability
- Saving of PCB space
- None forbidden-materials used in products/production
- Halogen Free Epoxy
- Moisture sensitivity level: MSL I

ORDERING INFORMATION - GLOBAL PART NUMBER & 12NC

Both part numbers are identified by the series, size, tolerance, packing type, temperature coefficient, taping reel and resistance value.

YAGEO BRAND ordering code

GLOBAL PART NUMBER (PREFERRED)

AFXXX - X X X XX XXXX L

(1) (2) (3) (4) (5) (6)

(I) SIZE

 $12 = 0402 \times 2 (0404)$

 $12 = 0402 \times 4 (0408)$

 $16 = 0603 \times 2 (0606)$

 $16 = 0603 \times 4 (0612)$

(2) NUMBER OF RESISTORS

2 = 2 resistors

4 = 4 resistors

(3) TOLERANCE

 $F = \pm 1\%$

 $| = \pm 5\%$ (for jumper ordering, use code of j)

(4) PACKAGING TYPE

R = Paper taping reel

(5) TEMPERATURE COEFFICIENT OF RESISTANCE

- = Base on spec

(6) TAPING REEL

07 = 7 inch dia. Reel

13 = 13 inch dia. Reel

(7) RESISTANCE VALUE

There are 2~4 digits indicated the resistor value. Letter R/K/M is decimal point, no need to mention the last zero after R/K/M, e.g. I K2, not I K20.

Detailed resistance rules show in table of "Resistance rule of global part number".

Resistance rule of global part number

| Resistance code rule | Example | | |
|--------------------------------|--|--|--|
| 0R | 0R = Jumper | | |
| XRXX (I to 9.76 Ω) | IR = I Ω IR5 = I.5 Ω 9R76 = 9.76 Ω | | |
| XXRX (10 to 97.6 Ω) | IOR = IO Ω 97R6 = 97.6 Ω | | |
| XXXR (100 to 976 Ω) | 100R = 100 Ω | | |
| XKXX (1 to 9.76 K Ω) | IK = 1,000 Ω 9K76 = 9760 Ω | | |
| | IM = 1,000,000 Ω | | |

ORDERING EXAMPLE

The ordering code of a AFI22 convex chip resistor array, value $1,000\Omega$ with ±5% tolerance, supplied in 7-inch tape reel is: AF122-JR-071KL.

NOTE

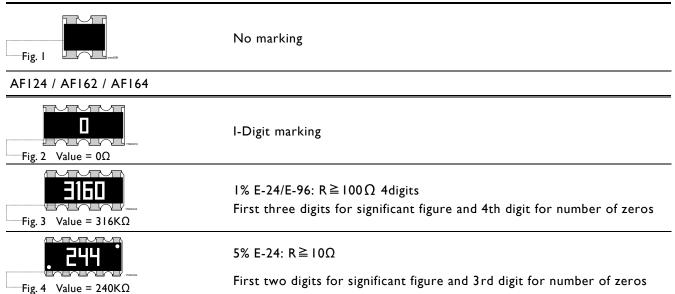
- I. All our R-Chip products meet RoHS compliant. "LFP" of the internal 2D reel label mentions "Lead Free Process"
- 2. On customized label, "LFP" or specific symbol printed and the optional "L" at the end of GLOBAL PART NUMBER



<u>MARKING</u>

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AFI22

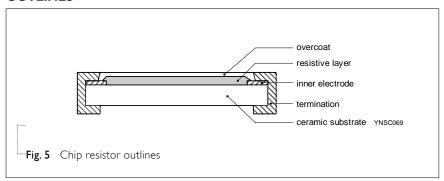


For further marking information, please refer to data sheet "Chip resistors marking".

CONSTRUCTION

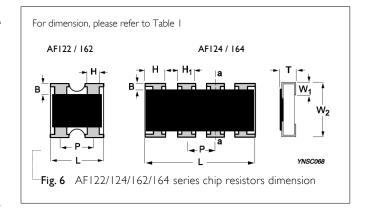
The resistor is constructed on top of a high-grade ceramic body. Internal metal electrodes are added on each end to make the contacts to the thick film resistive element. The composition of the resistive element is a noble metal embedded into a glass and covered by a glass. The resistor is laser trimmed to the rated resistance value. The resistor is covered with a protective epoxy coat, finally the external terminations (matte tin on Nibarrier) are added as shown in Fig.5.

OUTLINES



DIMENSIONS

| Table I | | | | |
|---------------------|-----------------|-----------|-----------|-----------|
| TYPE | AFI22 | AFI24 | AF162 | AFI64 |
| B (mm) | 0.24±0.10 | 0.25±0.15 | 0.35±0.10 | 0.35±0.15 |
| H (mm) | 0.30+0.10/-0.05 | 0.45±0.05 | 0.30±0.10 | 0.65±0.05 |
| H_1 (mm) | | 0.30±0.05 | | 0.50±0.15 |
| P (mm) | 0.67±0.05 | 0.50±0.05 | 0.80±0.05 | 0.80±0.05 |
| L (mm) | 1.00±0.10 | 2.00±0.10 | 1.60±0.10 | 3.20±0.15 |
| T (mm) | 0.30±0.10 | 0.45±0.10 | 0.40±0.10 | 0.60±0.10 |
| W _I (mm) | 0.25±0.10 | 0.30±0.15 | 0.30±0.10 | 0.30±0.15 |
| W ₂ (mm) | 1.00±0.10 | 1.00±0.10 | 1.60±0.10 | 1.60±0.15 |





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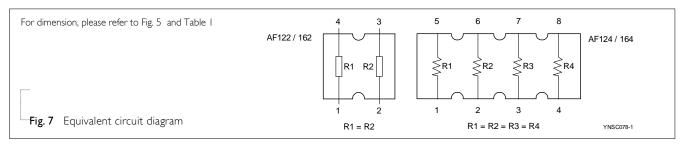
Chip Resistor Surface Mount

SERIES

122/124/162/164 (RoHS Compliant)

SCHEMATIC

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ELECTRICAL CHARACTERISTICS

Table 2

| CHARACTERISTICS | | AFI22 | | AFI24 | | AFI62 | Al | F164 |
|---|---|---------|--|-----------------|-----------------|---------|---|-------|
| Operating Temperature | −55 °C to - | +155 °C | −55 °C to - | +155 °C | −55 °C to | +155 °C | −55 °C to +15 | 55 °C |
| Rated Power | | 1/16 W | | 1/16 W | | 1/16W | 1/ | /16W |
| Maximum Working Voltage | | 50 V | | 50 V | | 50V | | 50V |
| Maximum Overload Voltage | | 100 V | | 100 V | | 100V | | 100V |
| Dielectric Withstanding | | 100 V | | 100 V | | 100V | | 100V |
| Resistance Range | 5% (E24) I Ω to I M Ω I% (E24/E96) I0 Ω to I M Ω Jumper < 50 m Ω | | 5% (E24) Ω t % (E24/E96) Ω t Jumper < | :ο I M Ω | ` , | | 5% (E24) Ω to 1% (E24/E96) Ω to Jumper < 50 | ΙΜΩ |
| Temperature Coefficient $\begin{array}{ccc} & \text{I } \Omega \leq R \leq \text{I 0 } \Omega & \pm 250 \text{ ppm/}^{\circ}\text{C} \\ & \text{I 0 } \Omega < R \leq \text{I } M\Omega & \pm 200 \text{ ppm/}^{\circ}\text{C} \end{array}$ | | | | | | | | |
| Jumper Criteria | Rated Current | 0.5 A | Rated Current | 1.0 A | Rated Current | 1.0 A | Rated Current | A0,1 |
| jumper Criteria | Maximum Current | 1.0 A | Maximum Current | 2.0 A | Maximum Current | 2.0 A | Maximum Current | 2.0A |

FOOTPRINT AND SOLDERING PROFILES

For recommended footprint and soldering profiles, please refer to data sheet "Chip resistors mounting".

PACKING STYLE AND PACKAGING QUANTITY

Table 3 Packing style and packaging quantity

| PACKING STYLE | REEL DIMENSION | AFI22 | AFI24 | AF162 | AFI64 |
|-----------------------|----------------|--------------|--------------|-------------|--------------|
| Paper Taping Reel (R) | 7" (178 mm) | 10,000 units | 10,000 units | 5,000 units | 5,000 units |
| | 13" (330 mm) | 50,000 units | 40,000 units | | 20,000 units |

NOTE

1. For paper tape and reel specification/dimensions, please refer to data sheet "Chip resistors packing".



SERIES

FUNCTIONAL DESCRIPTION

POWER RATING

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AFI22 / AFI24 / AFI62 / AFI64 rated power at 70 °C is I/16 W

RATED VOLTAGE

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

$$V = \sqrt{(P \times R)}$$

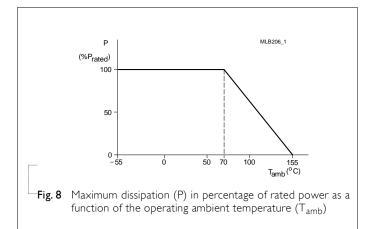
or max. working voltage whichever is less

Where

V=Continuous rated DC or AC (rms) working voltage (V)

P=Rated power (W)

R=Resistance value (Ω)





Chip Resistor Surface Mount

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TESTS AND REQUIREMENTS

Table 4 Test condition, procedure and requirements

| TEST | TEST METHOD | PROCEDURE | REQUIREMENTS |
|---------------------------------|------------------------|--|---|
| High Temperature | AEC-Q200 Test 3 | 1,000 hours at T_A = 155 °C, unpowered | $\pm (2.0\% + 0.05\Omega)$ |
| Exposure | MIL-STD-202 Method 108 | | $<$ 50 m Ω for Jumper |
| Moisture | MIL-STD-202 Method 106 | Each temperature / humidity cycle is defined at | ±(2.0%+0.05Ω) |
| Resistance | | 8 hours, 3 cycles / 24 hours for 10d. with 25 °C / 65 °C 95% R.H, without steps 7a & 7b, unpowered | <100 m Ω for Jumper |
| Biased | AEC-Q200 Test 7 | I,000 hours; 85 °C / 85% RH | ±(3.0%+0.05Ω) |
| Humidity | MIL-STD-202 Method 103 | I 0% of operating power Measurement at 24±4 hours after test conclusion | $<$ 100 m Ω for Jumper |
| Operational Life | AEC-Q200 Test 8 | 1,000 hours at 125 °C, derated voltage applied for | ±(3.0%+0.05Ω) |
| · | MIL-STD-202 Method 108 | 1.5 hours on, 0.5 hour off, still-air required | $<$ 100 m Ω for Jumper |
| Resistance to Soldering Heat | AEC-Q200 Test 15 | Condition B, no pre-heat of samples | ±(1.0%+0.05Ω) |
| | MIL-STD-202 Method 210 | Lead-free solder, 260±5 °C, 10±1 seconds immersion time | <50 m Ω for Jumper No visible damage |
| | | Procedure 2 for SMD: devices fluxed and cleaned with isopropanol | |
| Thermal Shock | MIL-STD-202 Method 107 | -55/+125 °C | ±(1.0%+0.05Ω) |
| | | Number of cycles is 300. Devices mounted | $<$ 50 m Ω for Jumper |
| | | Maximum transfer time is 20 seconds. Dwell time is 15 minutes. Air – Air | |
| ESD | AEC-Q200 Test 17 | Human Body Model, | ±(3.0%+0.05 Ω) |
| | AEC-Q200-002 | $I_{pos.} + I_{neg.}$ discharges | $<$ 50 m Ω for Jumper |
| | | 122/124: 500V | |
| | | 162/164: IKV | |



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| TEST | TEST METHOD | PROCEDURE | REQUIREMENTS |
|--|---|--|--------------------------------|
| Solderability | AEC-Q200 Test 18 Electrical Test not required Magnification 50X | | Well tinned (≥95% covered) |
| - Wetting | J-STD-002 | SMD conditions: | No visible damage |
| | | (a) Method B, aging 4 hours at 155 °C dry heat, dipping at 235±3 °C for 5±0.5 seconds. | TWO VISIBLE GUITINGE |
| | | (b) Method B, steam aging 8 hours, dipping at 215 ± 3 °C for 5 ± 0.5 seconds. | |
| | | (c) Method D, steam aging 8 hours, dipping at 260±3 °C for 30±0.5 seconds. | |
| Board Flex | AEC-Q200 Test 21 | Chips mounted on a 100mm × 40mm glass | ±(1.0%+0.05Ω) |
| | AEC-Q200-005 | epoxy resin PCB (FR4) 3mm | <50 m $Ω$ for Jumper |
| | | Holding time: minimum 60 seconds | |
| Temperature Coefficient of Resistance (T.C.R.) | MIL-STD-202 Method 304 | At +25/–55 °C and +25/+125 °C | Refer to table 2 |
| Resistance (1.C.R.) | | Formula: | |
| | | T.C.R= $\frac{R_2-R_1}{R_1(t_2-t_1)} \times 10^6 \text{ (ppm/°C)}$ | |
| | | Where t_1 =+25 °C or specified room temperature | |
| | | t_2 =–55 °C or +125 °C test temperature | |
| | | R_1 =resistance at reference temperature in ohms | |
| | | R ₂ =resistance at test temperature in ohms | |
| Short Time | IEC60115-1 8.1 | 2.5 times of rated voltage or maximum | ±(2.0%+0.05Ω) |
| Overload | | overload voltage whichever is less for 5 sec at room temperature | $<$ 50 m Ω for Jumper |
| FOS | ASTM-B-809-95* | Sulfur 750 hours, 105°C, unpowered | ±(4.0%+0.05Ω) |
| | *Modified | | $<$ $100 m\Omega$ for $Jumper$ |





Chip Resistor Surface Mount AF SERIES 122/124/162/164 (RoHS Compliant)



REVISION HISTORY

| REVISION | DATE | CHANGE NOTIFICATION | DESCRIPTION |
|-----------|---------------|---------------------|---|
| Version 6 | Apr. 21, 2021 | - | - Upgrade to Automotive Grade and voltage of AF124 updated, TCR of AF164 updated. |
| Version 5 | Mar. 20, 2017 | - | - Modify AF124/164 Equivalent Circuit Diagram |
| Version 4 | Jun. 23, 2016 | - | - AEC-Q200 qualified |
| Version 3 | Nov. 17, 2015 | - | - Add in AF162 |
| Version 2 | May 29,2015 | - | - Add in AF164 |
| Version I | Aug. 15, 2014 | - | - Update AFI24 dimensions |
| Version 0 | Oct. 02, 2013 | - | - First issue of this specification |



Chip Resistor Surface Mount

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