

Innovative Service Around the Globe

# DATA SHEET SURFACE MOUNT MULTILAYER

General purpose & High capacitance Class 2, X5R

4 V TO 50 V

100 pF to 220 μF RoHS compliant & Halogen free



YAGEO Phícomp

Surface Mount Multilayer Ceramic Capacitors General Purpose & High Cap. X5R 4 V to 50 V

#### <u>SCOPE</u>

This specification describes X5R series chip capacitors with lead-free terminations.

#### **APPLICATIONS**

PCs, Hard disk, Game PCs Power supplies DVD players Mobile phones Data processing

#### **FEATURES**

Supplied in tape on reel Nickel-barrier end termination RoHS compliant Halogen free compliant

#### ORDERING INFORMATION - GLOBAL PART NUMBER, PHYCOMP

(5)

#### <u>CTC & 12NC</u>

All part numbers are identified by the series, size, tolerance, TC material, packing style, voltage, process code, termination and capacitance value. **YAGEO BRAND ordering code** 

### GLOBAL PART NUMBER (PREFERRED)

#### CC <u>xxxx x x</u> X5R <u>x</u> BB <u>xxx</u>

(1) (2) (3) (4)

#### (I) SIZE – INCH BASED (METRIC)

0201	(0603)
0402	(1005)
0603	(1608)
0805	(2012)
1206	(3216)
1210	(3225)

#### (2) TOLERANCE

 $K = \pm 10\%$ 

 $M = \pm 20\%$ 

#### (3) PACKING STYLE

- R = Paper/PE taping reel; Reel 7 inch
- K = Blister taping reel; Reel 7 inch
- P = Paper/PE taping reel; Reel 13 inch
- F = Blister taping reel; Reel 13 inch
- C = Bulk case

#### (4) RATED VOLTAGE

4	=	4 V
5	=	6.3 V
6	=	10 V
7	=	16 V

- 8 = 25 V
- 9 = 50 V

#### (5) CAPACITANCE VALUE

2 significant digits+number of zeros

The 3rd digit signifies the multiplying factor, and letter R is decimal point

Example:  $103 = 10 \times 10^3 = 10,000 \text{ pF} = 10 \text{ nF}$ 

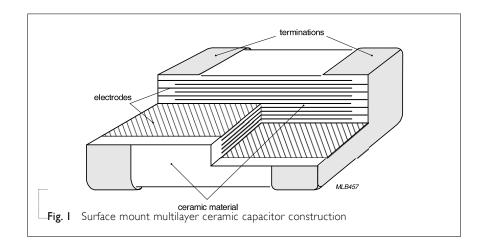


Surface Mount Multilayer Ceramic Capacitors General Purpose & High Cap. XSR 4 V to 50 V

#### CONSTRUCTION

The capacitor consists of a rectangular block of ceramic dielectric in which a number of interleaved metal electrodes are contained. This structure gives rise to a high capacitance per unit volume.

The inner electrodes are connected to the two end terminations and finally covered with a layer of plated tin (NiSn). The terminations are lead-free. A cross section of the structure is shown in Fig. I.

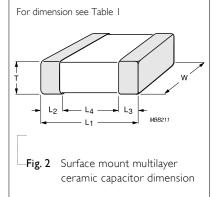


#### **DIMENSION**

**Table I** For outlines see fig. 2

ТҮРЕ	L <sub>I</sub> (mm)	W (mm)	T (MM)	L <sub>2</sub> / L <sub>3</sub>	, (mm)	L <sub>4</sub> (mm)	
		· · /	. ,	min.	max.	min.	CODE
	0.6 ±0.03	0.3 ±0.03	0.3 ±0.03	0.1	0.2	0.2	BA
0201	0.6 ±0.05	0.3 ±0.05	0.3 ±0.05	0.1	0.2	0.2	BB
0201	0.6 ±0.09	0.3 ±0.09	0.3 ±0.09	0.1	0.25	0.2	BC
	0.6 ±0.15	0.3 ±0.15	0.3 ±0.15	0.1	0.25	0.2	BD
	1.0 ±0.05	0.5 ±0.05	0.5 ±0.05	0.15	0.35	0.4	CA
0402	1.0 ±0.10	0.5 ±0.10	0.5 ±0.10	0.15	0.35	0.4	СВ
0402	1.0 ±0.15	0.5 ±0.15	0.5 ±0.15	0.15	0.35	0.4	CC
	1.0 ±0.20	0.5 ±0.20	0.5 ±0.20	0.15	0.35	0.4	CD
	1.6 ±0.10	0.8 ±0.10	0.8 ±0.10	0.2	0.6	0.4	DA
0603	1.6 ±0.15	0.8 ±0.15	0.8 ±0.15	0.2	0.6	0.4	DB
	1.6 ±0.20	0.8 ±0.20	0.8 ±0.20	0.2	0.6	0.4	DC
0805	2.0 ±0.20	1.25 ±0.20	0.85 ±0.10	0.25	0.75	0.7	EA
0005	2.0 ±0.20	1.25 ±0.20	1.25 ±0.20	0.25	0.75	0.7	EB
	3.2 ±0.15	1.6 ±0.15	1.15 ±0.10	0.25	0.75	1.4	FA
1206	3.2 ±0.30	1.6 ±0.20	1.25 ±0.20	0.25	0.75	1.4	FB
1200	3.2 ±0.30	1.6 ±0.30	1.60 ±0.20	0.25	0.80	1.4	FC
	3.2 ±0.30	1.6 ±0.30	1.60 ±0.30	0.30	0.90	1.4	FD
	3.2 ±0.40	2.5 ±0.30	1.25 ±0.20	0.25	0.75	1.4	GA
1210	3.2 ±0.40	2.5 ±0.30	1.90 ±0.20	0.25	0.75	1.4	GB
1210	3.2 ±0.40	2.5 ±0.30	2.5 ±0.20	0.25	0.75	1.0	GC
	3.2 ±0.40	2.5 ±0.30	2.5 ±0.30	0.25	0.75	0.1	GD

#### OUTLINES





 $\frac{\text{Product specification}}{18}$ 

Surface Mount Multilayer Ceramic Capacitors General Purpose & High Cap. XSR 4 V to 50 V

#### CAPACITANCE RANGE & THICKNESS FOR X5R

Table 2 CAP.	Sizes fro 0201	m 0201 to	0402				0402					
	4 V	6.3 V	10 V	16 V	25 V	50 V	4 V	6.3 V	10 V	16 V	25 V	50 V
100 pF		BA	BA	BA	BA	BA						
150 pF		BA	BA	BA	BA	BA						
220 pF		BA	BA	BA	BA	BA						
330 pF		BA	BA	BA	BA	BA						
470 pF		BA	BA	BA	BA	BA						
680 pF		BA	BA	BA	BA	BA						
I.0 nF		BA	BA	BA	BA	BA						
I.5 nF		BA	BA	BA	BA							
2.2 nF		BA	BA	BA	BA							
3.3 nF		BA	BA	BA	BA							
4.7 nF		BA	BA	BA	BA							
6.8 nF		BA	BA	BA	BA							
10 nF		BA	BA	BA	BA							
15 nF		BA	BA	BA								
22 nF		BA	BA	BA	BA			CA	CA	CA	CA	CA
33 nF		BA	BA	BA				CA	CA	CA	CA	CA
47 nF		BA	BA	BA				CA	CA	CA	CA	CA
68 nF		BA	BA	BA				CA	CA	CA	CA	CA
100 nF		BA	BA	BA	BB			CA	CA	CA	CA	CA
150 nF								CA	CA	CA	CA	CA
220 nF	BA	BA	BA	BA				CA	CA	CA	CA	CA
330 nF								CA	CA			
470 nF	BA	BA	BA	BA				CA	CA	СВ	СВ	СВ
680 nF								CA	CA			
Ι.Ο μF	BB	BB	BB					CA	CA	CA	CA	
2.2 µF	BC	BC	BC					CA	CA	CC	CD	
4.7 µF	BD						CC	CC	CC	CC		
IO μF							CD	CD	CD			
22 µF							CD	CD				

#### NOTE

I. Values in shaded cells indicate thickness class in mm

2. Capacitance value of non E-6 series is available on request

YAGEO	Phicomp		F	Product specification	5
	Surface Mount Multilayer Ceramic Capacitors	General Purpose & High Cap.	X5R	4 V to 50 V	18

CAPACITANCE	DANCE 2	THICKNESS	EOD VED
	NANGECK	1 111/2/11/299	LOW WOW

I		m 0603 to (	0805		<u>191011</u>		0805					
CAP.	0603 4∨	6.3 V	10 V	16 V	25 V	50V	4V	6.3 V	10 V	16 V	25 V	50V
10 nF												
15 nF												
22 nF												
33 nF												
47nF												
68 nF												
100 nF												
150 nF												
220 nF		DA	DA	DA	DA	DA						
330 nF		DA	DA	DA	DA	DA						
470 nF		DA	DA	DA	DA	DA		EA EB	EA EB	EA EB	EB	EB
680 nF		DA	DA	DA	DA	DA		EA EB	EA EB	EA EB	EB	EB
Ι.0 μF		DA	DA	DA	DA	DA		EA EB	EA EB	EA EB	EB	EB
2.2 µF		DA	DA	DA	DB	DC		EA EB	EA EB	EA EB	EA EB	EB
4.7 µF		DA	DA	DB	DB			EA EB	EA EB	EB	EB	EB
IO μF		DB	DC	DC	DC			EA EB	EA EB	EB	EB	EB
22 µF		DC	DC					EB	EB	EB	EB	
47 µF	DC	DC						EB	EB			
100 µF							EB	EB				

#### ΝΟΤΕ

- ${\rm I.} \quad {\rm Values \ in \ shaded \ cells \ indicate \ thickness \ class \ in \ mm}$
- 2. Capacitance value of non E-6 series is available on request

<b>YAGEO</b>	Phíco	отр								Product specificat	tion 6
	Surfac	e Mount Mu	Itilaye	er Ceramic	Capacit	ors	General Purp	ose & High (	Cap. X5R	4 V to 50 V	18
Table 4 CAP.	Sizes from <b>I 206</b>	1206 to 1210					1210				
CAr.	4 V	6.3 V	10 V	16 V	25 V	50V		10 V	16 V	25 V	50V
10 nF											
15 nF											
22 nF											
33 nF											
47nF											
68 nF											
100 nF											
150 nF											
220 nF											
330 nF											
470 nF											
680 nF											
Ι.0 μF		FA	FA	FA	FA	FC	GA	GA	GA	GA	GA
2.2 µF		FA	FA	FA	FA	FC	GB	GB	GB	GB	GB
4.7 µF		FC	FC	FC	FC	FC	GB	GB	GB	GB	GC
ΙΟ μF		FC	FC	FC	FC	FD	GB	GB	GB	GB	GC
22 µF		FC	FC	FC	FD		GC	GC	GC	GD	
47 µF		FC	FC	FD			GC	GC	GC		
100 µF		FD					GD	GD	GD		
220 µF	FD						GD				

#### ΝΟΤΕ

1. Values in shaded cells indicate thickness class in mm

2. Capacitance value of non E-6 series is available on request



7

Surface Mount Multilayer Ceramic Capacitors General Purpose & High Cap. XSR 4 V to 50 V

#### THICKNESS CLASSES AND PACKING QUANTITY

Table 5							
SIZE	THICKNESS	TAPE WIDTH -	Ø180 MM	/ 7 INCH	Ø330 MM	/ 13 INCH	QUANTITY
CODE	CLASSIFICATION	QUANTITY PER REEL	Paper	Blister	Paper	Blister	PER BULK CASE
0201	0.3 ±0.03 mm	8 mm	15,000		50,000		
0402	0.5 ±0.05 / 0.1 mm	8 mm	10,000		50,000		50,000
0402	0.5 ±0.15 / 0.2 mm	8 mm	10,000		40,000		
0603	0.8 ±0.1 mm	8 mm	4,000		15,000		15,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		10,000
0805	0.85 ±0.1 mm	8 mm	4,000		15,000		8,000
	1.25 ±0.2 mm	8 mm		3,000		10,000	5,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		
	0.85 ±0.1 mm	8 mm	4,000		15,000		
1206	1.00 / 1.15 ±0.1 mm	8 mm		3,000		10,000	
1200	1.25 ±0.2 mm	8 mm		3,000		10,000	
	1.6 ±0.15 mm	8 mm		2,500		10,000	
	1.6 ±0.2 mm	8 mm		2,000		8,000	
	0.6 / 0.7 ±0.1 mm	8 mm		4,000		15,000	
	0.85 ±0.1 mm	8 mm		4,000		10,000	
	1.15 ±0.1 mm	8 mm		3,000		10,000	
	1.15 ±0.15 mm	8 mm		3,000		10,000	
	1.25 ±0.2 mm	8 mm		3,000			
1210	1.5 ±0.1 mm	8 mm		2,000			
	1.6 / 1.9 ±0.2 mm	8 mm		2,000			
	2.0 ±0.2 mm	8 mm		2,000 1,000			
	2.5 ±0.2 mm	8 mm		1,000 500			



Surface Mount Multilayer Ceramic Capacitors General Purpose & High Cap. XSR 4 V to 50 V

#### ELECTRICAL CHARACTERISTICS

#### X5R DIELECTRIC CAPACITORS; NISN TERMINATIONS

Unless otherwise specified, all tests and measurements shall be made under standard atmospheric conditions for testing as given in 5.3 of IEC 60068-1:

- Temperature: 15 °C to 35 °C
- Relative humidity: 25% to 75%
- Air pressure: 86 kPa to 106 kPa

Before the measurements are made, the capacitor shall be stored at the measuring temperature for a time sufficient to allow the entire capacitor to reach this temperature.

The period as prescribed for recovery at the end of a test is normally sufficient for this purpose.

Capacitan	ce range					100 - 5	- 220 J.F
· ·	ce tolerance					•	to 220 µF
	n factor (D.F.)					±10/6 a	and ±20%
X5R	0201	0402	0603	0805	1206	1210	D.F
≤ 6.3V	100pF to 10nF	22nFto 100nF	220nF to IuF	470nF to 680nF	luF to 10uF	luF to 10uF	≤ 5%
		I 20nF to 220nF				22uF	≤ 7%
	l2nF to luF	330nF to 10uF	2.2uF to 47uF	luF to 100uF	22uF to 47uF	47uF to 220uF	≤ 10%
	2.2uF				100uF, 220uF		≤  5%
	4.7uF	22uF					≤ 20%
10V	100pF to 10nF	22nF to 100nF	220nF to 470nF	470nF to 680nF	luF to 4.7uF	l uF to 4.7uF	≤ 5%
		I 20nF to 220nF	680nF	IuF			≤ 7%
	I 2nF to 220nF, I uF	330nF to 10uF	l uF to 22uF	2.2uF to 47uF	10uF to 47uF	10uF to 100uF	≤ 10%
	470nF						≤  5%
	2.2uF						≤ 20%
16V	100pF to 10nF	22nF to 100nF	220nF to 470nF	470nF to 680nF	luF to 4.7uF	l uF to 4.7uF	≤ 5%
		I 20nF to 220nF	680nF to IuF	I uF to 2.2uF			≤ 7%
	I 2nF to 220nF	470nF to 4.7uF	2.2uF to 10uF	4.7uF to 22uF	10uF to 47uF	10uF to 100uF	≤ 10%
	470nF						≤ 20%
25V	100pF to 10nF	22nF		470nF to IuF	luF to 2.2uF	l uF to 4.7uF	≤ 3.5%
		27nFto 100nF	220nF to 470nF	2.2uF	4.7uF	l OuF	≤ 5%
		I 20nF to 220nF	680nFtoluF				≤ 7%
	22nF, 100nF	470nF to 2.2uF	2.2uF to 10uF	4.7uF to 22uF	10uF to 22uF	22uF	≤ 10%
50∨	100pF to 1nF	22nF					≤ 3.5%
		27nF to 120nF					≤ 5%
		I 50nF to 220nF					≤ 7%
		470nF	220nF to 2.2uF	470nF to 10uF	IuF to IOuF	luF to 10uF	$\leq 10\%$
	resistance after 1 min			ns ≥ 10 GΩ or Rir	ns × Cr ≥ 50/100/5	500* seconds which	ever is less
	capacitance change as a		erature				±15%
<u> </u>	cure characteristic/coeffic g temperature range:	cient):					o +85 °C



Product specification 9 18

Surface Mount Multilayer Ceramic Capacitors General Purpose & High Cap. XSR 4 V to 50 V

#### NOTE

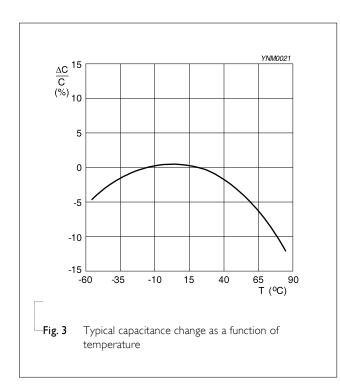
\_

* Rins $\geq$ 10 G $\Omega$ or Rins × Cr $\geq$ 500 $\Omega$ .F:
0201 : 100pF to 47nF
0402 : 22nF to 470nF
0603 : 220nF to IuF
0805 : 470nF to 2.2uF, 4.7uF/6.3V to 10V
1206 : IuF to 2.2uF, 4.7uF/6.3V to IOV
1210 : IuF to 2.2uF, 4.7uF/6.3V to 16V

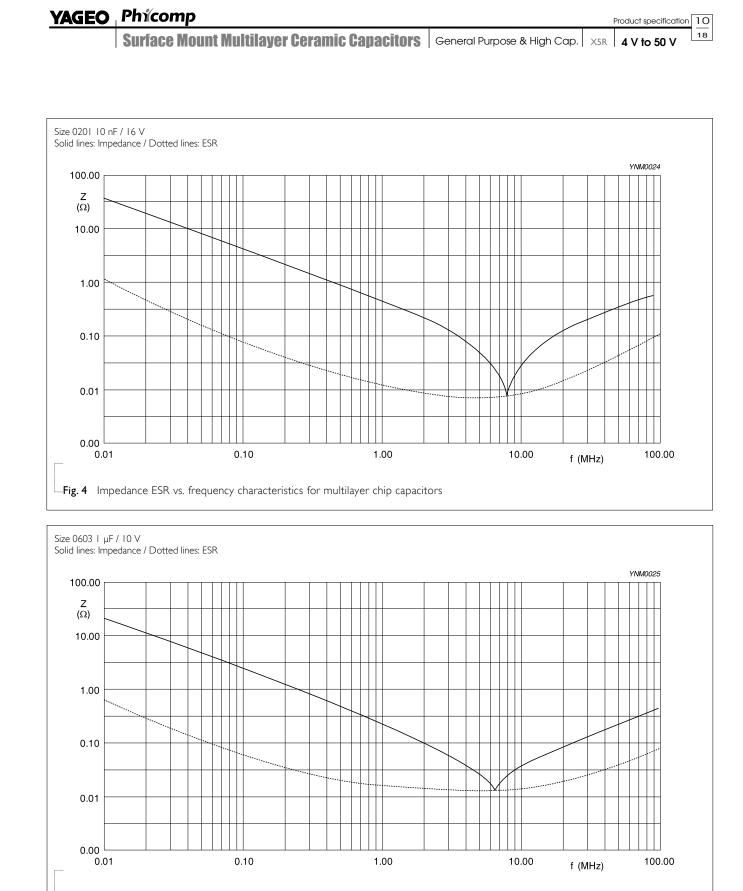
* Rins × Cr ≥ 50Ω.F:
0201 : luF
0402 : IOuF
0603 : 10uF to 22uF
0805 : I0uF/50V, 47uF to I00uF
1206 : 100uF, 220uF

\* Rins × Cr ≥ 100Ω.F: 0201 : 100nF to 470nF 0402 : 1uF to 4.7uF 0603 : 2.2uF to 4.7uF 0805 : 4.7uF/16V to 50V, 10uF to 22uF/4V to 25V 1206 : 4.7uF/16V to 50V, 10uF to 47uF 1210 : 4.7uF/25V to 50V, 10uF to 220uF

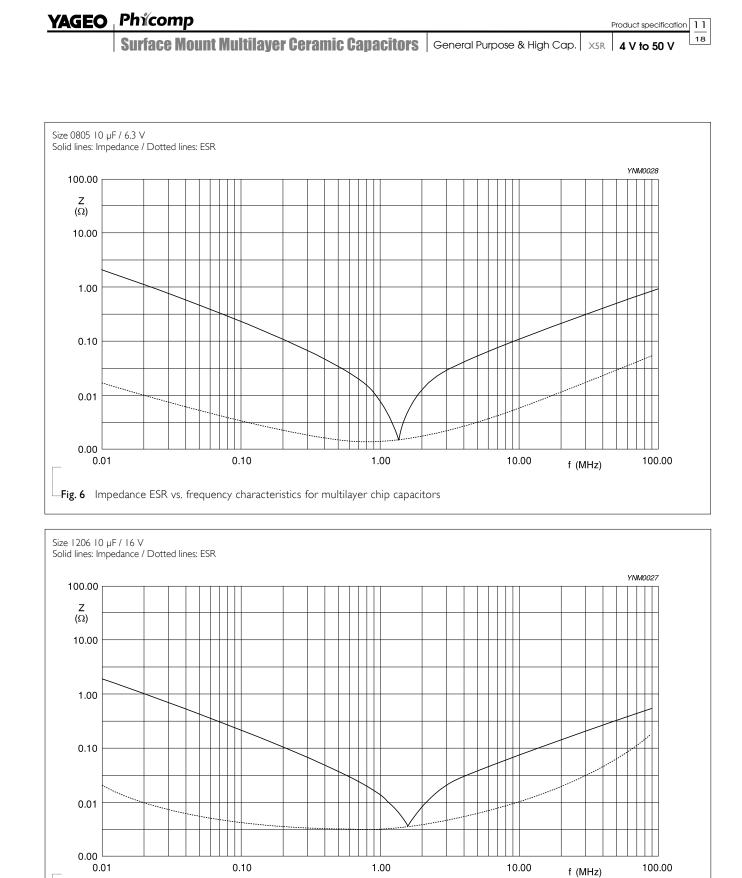
\* Rins × Cr ≥ 20Ω.F: 0201 : 2.2uF to 4.7uF 0402 : 22uF 0603 : 47uF





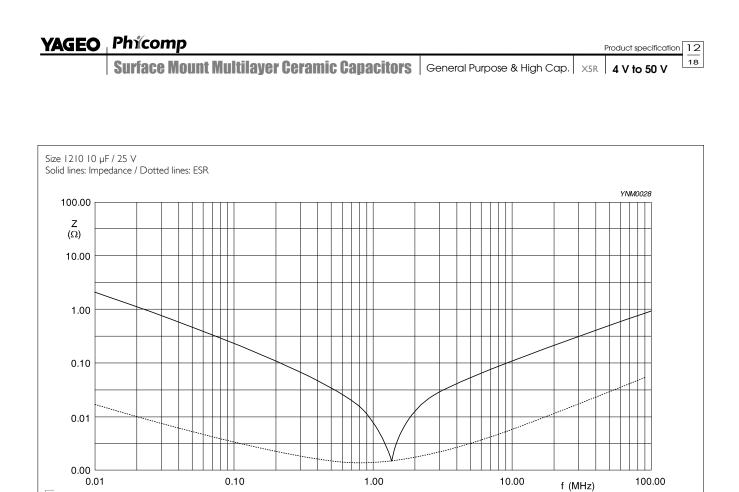


**Fig. 5** Impedance ESR vs. frequency characteristics for multilayer chip capacitors



**Fig. 7** Impedance ESR vs. frequency characteristics for multilayer chip capacitors





#### Fig. 8 Impedance ESR vs. frequency characteristics for multilayer chip capacitors

#### SOLDERING RECOMMENDATION

Table 7						
SOLDERING METHOD	SIZE 0201	0402	0603	0805	1206	≥  2 0
Reflow	Reflow only	> 100 nF	> IµF	> 2.2 µF	> 2.2 µF	Reflow only
Reflow/Wave		≤ 100 nF	≤IµF	≤ 2.2 µF	≤ 2.2 µF	

Product specification 13

Surface Mount Multilayer Ceramic Capacitors General Purpose & High Cap. XSR 4 V to 50 V

#### TESTS AND REQUIREMENTS

Table 8 Test procedures and requirements				
TEST	TEST MET	HOD	PROCEDURE	REQUIREMENTS
Mounting	IEC 60384- 21/22	4.3	The capacitors may be mounted on printed-circuit boards or ceramic substrates	No visible damage
Visual Inspection and Dimension Check		4.4	Any applicable method using × 10 magnification	In accordance with specification
Capacitance <sup>(I)</sup>		4.5.I	Class 2:	Within specified tolerance
Dissipation Factor (D.F.) (I)4.5.2At 20 °C, 24 hrs Cap $\leq I \mu$ F, f = Cap $> I u$ F, f = I measuring at volt f = I KHz, for C voltage 0.5 Vrms		4.5.2	At 20 °C, 24 hrs after annealing Cap $\leq$ I $\mu$ F, f = I KHz, measuring at voltage I Vrms at 20 °C Cap > IuF, f = I KHz for C $\leq$ 10 $\mu$ F, rated voltage > 6.3 V, measuring at voltage I Vrms at 20 °C f = I KHz, for C $\leq$ 10 $\mu$ F, rated voltage $\leq$ 6.3 V, measuring at voltage 0.5 Vrms at 20 °C f = I 20 Hz for C > 10 $\mu$ F, measuring at voltage 0.5 Vrms at 20 °C	
Insulation Resistance		4.5.3	5.3 At U <sub>r</sub> (DC) for I minute In accordance with	

#### NOTE

I. The figure indicates typical inspection. Please refer to individual specifications.



Product specification  $\frac{14}{18}$ 

Surface Mount Multilayer Ceramic Capacitors General Purpose & High Cap. X5R 4 V to 50 V

TEST	TEST METH	HOD	PROCEDURE	REQUIREMENTS			
Temperature Characteristic		4.6	Capacitance shall be measured by the steps shown in the following table. The capacitance change should be measured after 5 min at	<general purpose="" series=""> Class I : Δ C/C: ±30ppm</general>			
			each specified temperature stage.	Class2: X7R: Δ C/C: ±15%			
			Step Temperature(°C)	Y5V: Δ C/C: 22~-82%			
			a 25±2	<high capacitance="" series=""></high>			
			b Lower temperature±3°C	Class2:			
			c 25±2	X7R/X5R: Δ C/C: ±15% Y5V: Δ C/C: 22~-82%			
			d Upper Temperature±2° <b>C</b>	130. 2 C/C. 22 -02/6			
			e 25±2				
			(I) Class I				
			Temperature Coefficient shall be calculated from the formula as below				
			Temp, Coefficient = $\frac{C2 - CI}{CI \times \Delta T} \times 10^6 \text{ [ppm/°C]}$				
			CI: Capacitance at step c				
			C2: Capacitance at 125°C				
			$\Delta T: 100^{\circ}C(=125^{\circ}C-25^{\circ}C)$				
			(2) Class II				
	Capacitance Change shall be calculated from the for as below		Capacitance Change shall be calculated from the formula as below				
		$\Delta C = \frac{C2 - C1}{C1} \times 100\%$					
			CI: Capacitance at step c				
			C2: Capacitance at step b or d				
size = 0402: 2.		Force size ≥ 0603: 5N size = 0402: 2.5N size = 0201: 1N					
Bending	IEC 60384-	4.8	Mounting in accordance with IEC 60384-22 paragraph 4.3	No visible damage			
Strength	21/22						
Strength	21/22	-	Conditions: bending 1 mm at a rate of 1 mm/s, radius jig 5 mm	ΔC/C Class2: <general purpose="" series=""></general>			
Strength	21/22	-		Class2:			
Strength	21/22	-	5 mm	Class2: <general purpose="" series=""></general>			
Strength	21/22		5 mm Test Substrate:	Class2: <general purpose="" series=""> X5R: ±10%</general>			
Strength	21/22	-	5 mm Test Substrate:	Class2: <general purpose="" series=""> X5R: ±10% <high capacitance="" series=""></high></general>			
Strength	21/22		5 mm Test Substrate:	Class2: <general purpose="" series=""> X5R: ±10% <high capacitance="" series=""> X5R: ±12.5%</high></general>			
Strength	21/22		5 mm Test Substrate:	Class2: <general purpose="" series=""> X5R: ±10% <high capacitance="" series=""> X5R: ±12.5% Dimension(mm)</high></general>			
Strength	21/22		5 mm Test Substrate:	Class2: <general purpose="" series=""> X5R: ±10% <high capacitance="" series=""> X5R: ±12.5% Dimension(mm) Type a b c</high></general>			
Strength	21/22		5 mm Test Substrate: b + 04.5 VNSCH7 40 100	Class2: <general purpose="" series=""> X5R: ±10% <high capacitance="" series=""> X5R: ±12.5% Dimension(mm) Type a b c 0201 0.3 0.9 0.3</high></general>			
Strength	21/22		5 mm Test Substrate:	Class2: <general purpose="" series="">         X5R: ±10%         <high capacitance="" series="">         X5R: ±12.5%         Dimension(mm)         Type       a       b       c         0201       0.3       0.9       0.3         0402       0.4       1.5       0.5</high></general>			
Strength	21/22		5 mm Test Substrate: b + 04.5 VNSCH7 40 100	Class2: <general purpose="" series=""> X5R: ±10% <high capacitance="" series=""> X5R: ±12.5%</high></general>			

Product specification  $\frac{15}{18}$ 

Surface Mount Multilayer Ceramic Capacitors General Purpose & High Cap. XSR 4 V to 50 V

TEST	TEST METH	IOD	PROCEDURE	REQUIREMENTS
Resistance to Soldering Heat		4.9	Precondition: $150 \pm 0/-10$ °C for 1 hour, then keep for 24 ±1 hours at room temperature Preheating: for size ≤ 1206: 120 °C to 150 °C for 1	Dissolution of the end face plating shall not exceed 25% of the length of the edge concerned
			minute	<general purpose="" series=""></general>
			Preheating: for size >1206: 100 °C to 120 °C for 1 minute and 170 °C to 200 °C for 1 minute	$\Delta C/C$ Class2:
			Solder bath temperature: $260 \pm 5$ °C	×5R: ±10%
			Dipping time: 10 ±0.5 seconds	<high capacitance="" series=""></high>
			Recovery time: 24 ±2 hours	$\Delta C/C$
			,	Class2:
				X5R: ±10%
			-	D.F. within initial specified value
				R <sub>ins</sub> within initial specified value
Solderability		4.10	Preheated the temperature of 80 °C to 140 °C and maintained for 30 seconds to 60 seconds.	The solder should cover over 95% of the critical area of each termination
			I. Temperature: 235±5°C / Dipping time: 2 ±0.5 s	
			2. Temperature: 245±5°C / Dipping time: 3 ±0.5 s (lead free)	
			Depth of immersion: 10mm	
Rapid Change of	IEC 60384- 21/22	4.11	Preconditioning; 150 +0/–10 °C for 1 hour, then keep for 24 $\pm$ 1 hours at _	No visual damage
Temperature			room temperature	<general purpose="" series=""></general>
				$\Delta C/C$
			5 cycles with following detail:	Class2:
			30 minutes at lower category temperature 30 minutes at upper category temperature	X5R: ±15%
				<high capacitance="" series=""></high>
			Recovery time 24 $\pm$ 2 hours	$\Delta C/C$
				Class2:
				X5R: ±15%
			-	D.F. meet initial specified value
				R <sub>ins</sub> meet initial specified value



Product specification  $\frac{16}{18}$ 

Surface Mount Multilayer Ceramic Capacitors General Purpose & High Cap. X5R 4 V to 50 V

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Damp Heat	4.13	<ol> <li>Preconditioning, class 2 only: 150 +0/-10 °C /1 hour, then keep for 24 ±1 hour at room temp</li> </ol>	No visual damage after recovery
with U <sub>r</sub> Load			<general purpose="" series=""></general>
			$\Delta C/C$
		2. Initial measure:	Class2:
		Spec: refer to initial spec C, D, IR	X5R: ±15%
		3. Damp heat test:	D.F.
		500 ±12 hours at 40 ±2 °C; 90 to 95% R.H. 1.0 U <sub>r</sub> applied	Class2:
		4. Recovery:	X5R:
		Class 2: 24 $\pm$ 2 hours	$\leq$ 16V: $\leq$ 7% or 2 x initial value whichever
		5. Final measure: C, D, IR	is greater
			$\geq$ 25V: $\leq$ 5% or 2 × initial value whichever
		P.S. If the capacitance value is less than the minimum	is greater
		value permitted, then after the other measurements have been made the capacitor shall be preconditioned according to " $IEC 60384 4.1$ " and then the requirements shall be met.	R <sub>ins</sub>
			Class2:
			X5R: ≥ 500 MΩ or $R_{ins} \times C_r \ge 25s$
			whichever is less
		* General product:	<high capacitance="" series=""></high>
		0201 < 100nF	$\Delta C/C$
		0402 < IuF	Class2:
		0603 < 2.2uF	X5R: ±20%
		0805, 1206, 1210 < 4.7uF	D.F.
			Class2:
		* High cap product: 0201 ≥ 100nF	X5R: 2 $\times$ initial value max
		$0201 \ge 100$ Hz $0402 \ge 1$ uF	R <sub>ins</sub>
		0603 ≥ 2.2uF	Class2:
		0805, 1206, 1210 ≥ 4.7uF	Rins × Cr ≥ 5s
			whichever is less



Product specification  $\frac{17}{18}$ 

Surface Mount Multilayer Ceramic Capacitors General Purpose & High Cap. X5R 4 V to 50 V

TEST	TEST METHOD		PROCEDURE	REQUIREMENTS	
Endurance	IEC 60384- 21/22	4.14	<ol> <li>Preconditioning, class 2 only: 150 +0/-10 °C /1 hour, then keep for 24 ±1 hour at</li> </ol>	No visual damage	
			room temp	<general purpose="" series=""></general>	
			2. Initial measure:	$\Delta C/C$	
			Spec: refer to initial spec C, D, IR	Class2:	
			3. Endurance test:	X5R: ±15%	
			Temperature: X5R: 85 °C	D.F.	
			Specified stress voltage applied for 1,000 hours:	Class2:	
			Applied 2.0 × Ur for general product*.	X5R:	
			Applied 1.5 $\times$ Ur for high cap. product*.	$\leq 16V$ : $\leq 7\%$ or 2 x initial value whichever	
			Applied 1.0 $\times$ Ur for high cap. product*.	is greater	
			4. Recovery time: 24 $\pm$ 2 hours	$\geq 25V: \leq 5\%$ or 2 x initial value whichever	
			5. Final measure: C, D, IR	$\geq 25 \text{ V}$ : $\leq 5\%$ or 2 x Initial value whichever is greater	
				-	
			P.S. If the capacitance value is less than the minimum	R <sub>ins</sub>	
			value permitted, then after the other measurements	Class2:	
			have been made the capacitor shall be preconditioned	X5R: ≥ 1,000 MΩ or $R_{ins} \times C_r \ge 50s$	
			according to "IEC 60384 4.1" and then the requirements	whichever is less	
			shall be met.	<high capacitance="" series=""></high>	
			* General product (Applied 2.0 × Ur):	$\Delta C/C$	
			0201 < 100nF	Class 2:	
			0402 < IuF	X5R: ±20%	
			0603 < 2.2uF	D.F.	
			0805, 1206, 1210 < 4.7uF	Class 2:	
				X5R: $2 \times initial value max$	
			* High cap product (Applied 1.5 $\times$ Ur):	R <sub>ins</sub>	
			$0201 \ge 100 nF$	Class 2:	
			$0402 \ge I  \mathrm{uF}$	Rins × Cr ≥ 10s	
			0603 ≥ 2.2uF	whichever is less	
			0805, 1206, 1210 ≥ 4.7uF		
			* High cap product (Applied 1.0 × Ur):		
			0201: 100nF/25V, 2.2uF to 4.7uF		
			0402: 4.7uF to 22uF		
			0603: 10uF/10V to 25V 22uF to 47uF		
			0805: 10uF/ 25V, 50V, 22uF to 100uF 1206: 10uF/ 50V		
Voltage		4.6	Specified stress voltage applied for 1~5 seconds	No breakdown or flashover	
Proof			Ur ≤ 100 V: series applied 2.5 Ur		
			$100 \vee < \text{Ur} \leq 200 \vee \text{series applied}$		
			(1.5 Ur + 100)		
			$200 \vee < \text{Ur} \le 500 \vee \text{series applied}$		
			(1.3 Ur + 100)		
			Ur > 500 V: 1.3 Ur		
			$Ur \ge 1000 V: 1.2 Ur$		
			Charge/Discharge current is less than 50 mA		



Product specification 18

Surface Mount Multilayer Ceramic Capacitors General Purpose & High Cap. XSR 4 V to 50 V

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 25	Jun. 2, 2017	-	- I.R spec updated
Version 24	Mar. 6, 2017	-	- 0805 L4 spec updated
Version 23	Nov. 15, 2016	-	- Dimension updated
Version 22	Oct. 3, 2016	-	- Dimension and Soldering recommendation updated
Version 21	Jan. 28, 2016	-	- Tests and requirements updated
Version 20	Dec. 04, 2015	-	- Size updated
Version 19	Apr. 09, 2015	-	- Voltage updated
Version 18	Jul. 07, 2014	-	- Voltage updated
Version 17	Mar. 31, 2014	-	- Test condition updated
Version 16	Nov. 29, 2012	-	- Test condition updated
Version 15	Sep. 03, 2012	-	- Test condition updated
Version 14	May 16, 2012	-	- Product range updated
Version 13	May 02, 2012	-	- Product range updated
Version 12	Feb 10, 2012	-	- Product range updated
Version 11	Oct 21, 2011	-	- Product range updated
Version 10	Jun 21, 2011	-	- Product range updated
Version 9	Mar 23, 2011	-	- Product range updated
Version 8	Jan 25, 2011	-	- Rated voltage of 0201 extend to 50V
Version 7	Jan 05, 2011	-	- Product range updated
Version 6	Jul 27, 2010	-	- Dimension on 0603 and 1206 case size updated
Version 5	Apr 21, 2010	-	- The statement of "Halogen free" on the cover added
			- Dimension updated
Version 4	Jan 13, 2010	-	- Thickness updated
Version 3	Aug 17, 2009	-	- Dimension updated
Version 2	Jun 09, 2009	-	- Ordering code updated
Version I	May 15, 2009	-	- Product range updated
Version 0	Apr 15, 2009	-	<ul> <li>New datasheet for general purpose and high capacitance X5R series with RoHS compliant</li> <li>Replace the "6.3V to 50V" part of pdf files: UP-X5R_X7R_HighCaps_6.3-</li> </ul>
			<ul> <li>to-25V_II, UY-X5R_X7R_HighCaps_6.3-to-25V_II</li> <li>Combine 0201 from pdf files: UP-NP0X5RX7RY5V_0201_6.3-to-50V_2 and UY-NPOX5RX7RY5V_0201_6.3-to-50V_2</li> <li>Define global part number</li> <li>Description of "Halogen free compliant" added</li> <li>Test method and procedure updated</li> </ul>

### <u>REVISION HISTORY</u>



## **X-ON Electronics**

Largest Supplier of Electrical and Electronic Components

Click to view similar products for Multilayer Ceramic Capacitors MLCC - SMD/SMT category:

Click to view products by Yageo manufacturer:

Other Similar products are found below :

D55342E07B523DR-T/R NCA1206X7R103K50TRPF NCA1206X7R104K16TRPF NIN-FB391JTRF NIN-FC2R7JTRF NMC0402NPO220J50TRPF NMC0402X5R105K6.3TRPF NMC0402X5R224K6.3TRPF NMC0402X7R103J25TRPF NMC0402X7R153K16TRPF NMC0603NPO330G50TRPF NMC0603NPO331F50TRPF NMC0603X5R475M6.3TRPF NMC0805NPO270J50TRPF NMC0805NPO681F50TRPF NMC0805NPO820J50TRPF NMC0805X7R224K25TRPF NMC1206X7R102K50TRPF NMC1210Y5V105Z50TRPLPF NMC-H0805X7R472K250TRPF NMC-L0402NPO7R0C50TRPF NMC-L0603NPO2R2B50TRPF NMC-Q0402NPO8R2D200TRPF C1206C101J1GAC C1608C0G2A221J C1608X7R1E334K C2012C0G2A472J 2220J2K00562KXT KHC201E225M76N0T00 1812J2K00332KXT CCR06CG153FSV CDR14BP471CJUR CDR31BX103AKWR CDR33BX683AKUS CGA2B2C0G1H010C CGA2B2C0G1H040C CGA2B2C0G1H050C CGA2B2C0G1H060D CGA2B2C0G1H070D CGA2B2C0G1H120J CGA2B2C0G1H151J CGA2B2C0G1H1R5C CGA2B2C0G1H2R2C CGA2B2C0G1H390J CGA2B2C0G1H391J CGA2B2C0G1H3R3C CGA2B2C0G1H680J CGA2B2C0G1H6R8D CGA2B2C0G1H820J CGA2B2X8R1H152K