

DATA SHEET

SURFACE-MOUNT CERAMIC MULTILAYER CAPACITORS Mid-voltage

NPO/X7R 100 V TO 630 V 0.47 pF to 2.2 μF RoHS compliant & Halogen Free



vageo Phícomp

Surface-Mount Ceramic Multilayer Capacitors Mid-voltage NP0/X7R 100 V to 630 V

<u>SCOPE</u>

This specification describes Midvoltage NP0/X7R series chip capacitors with lead-free terminations.

APPLICATIONS

PCs, Hard disk, Game PCs Power supplies LCD panel ADSL, Modem

FEATURES

Supplied in tape on reel Nickel-barrier end termination RoHS compliant Halogen Free compliant

ORDERING INFORMATION - GLOBAL PART NUMBER, PHYCOMP

CTC & 12NC

All part numbers are identified by the series, size, tolerance, TC material, packing style, voltage, process code, termination and capacitance value. **YAGEO BRAND ordering code**

GLOBAL PART NUMBER (PREFERRED)

CC <u>XXXX</u> <u>X</u> <u>X</u> <u>XXX</u> <u>X</u> <u>B</u> <u>X</u> <u>XXX</u> (1) (2) (3) (4) (5) (6) (7)

(I) SIZE - INCH BASED (METRIC)

0201 (0603) / 0402 (1005) / 0603 (1608) / 0805 (2012) / 1206 (3216) / 1210 (3225) 1808 (4520) / 1812 (4532)

(2) TOLERANCE

 $C = \pm 0.25 \text{ pF}$ $D = \pm 0.5 \text{ pF}$ $F = \pm 1\%$ $G = \pm 2\%$ $J = \pm 5\%$

 $K = \pm 10\%$

M= ±20%

(3) PACKING STYLE

- R = Paper/PE taping reel; Reel 7 inch
- K = Blister taping reel; Reel 7 inch
- P = Paper/PE taping reel; Reel 13 inch
- F = Blister taping reel; Reel 13 inch
- C = Bulk case

(4) TC MATERIAL

- NPO
- X7R

(5) RATED VOLTAGE

- 0 = 100 V
- A = 200 V
- Y = 250 V
- B = 500 V
- Z = 630 V

(6) PROCESS

- N = NPO
- B = Class 2 MLCC

(7) CAPACITANCE VALUE

2 significant digits+number of zeros

The 3rd digit signifies the multiplying factor, and letter R is decimal point

Example: $|2| = |2 \times |0| = |20 \text{ pF}$



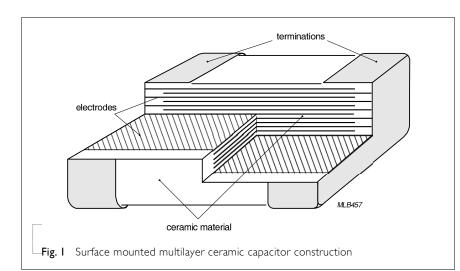
Surface-Mount Ceramic Multilayer Capacitors Mid-voltage NP0/X7R 100 V to 630 V

Product specification $\frac{3}{18}$

CONSTRUCTION

The capacitor consists of a rectangular block of ceramic dielectric in which a number of interleaved metal electrodes are contained. This structure gives rise to a high capacitance per unit volume.

The inner electrodes are connected to the two end terminations and finally covered with a layer of plated tin (NiSn). The terminations are lead-free. A cross section of the structure is shown in Fig. I.

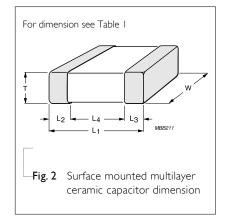


DIMENSION

Table I For outlines see fig. 2

TYPE	L _I (mm)	W (mm)	T (MM)	L ₂ / L ₃ min.	(mm) max.	L ₄ (mm) min.
0201	0.6 ±0.03	0.3±0.03		0.10	0.20	0.20
0402	1.0 ±0.05	0.5 ±0.05		0.15	0.30	0.40
0603	1.6 ±0.10	0.8 ±0.10	-	0.20	0.60	0.40
0805	2.0 ±0.20	1.25 ±0.20	Refer to	0.25	0.75	0.70
1206	3.2 ±0.30	1.6 ±0.20	table 2 to 13	0.25	0.75	1.40
1210	3.2 ±0.30	2.5 ±0.20	-	0.25	0.75	1.40
1808	4.5 ±0.40	2.0 ±0.30	-	0.25	0.75	2.20
1812	4.5 ±0.40	3.2 ±0.30	-	0.25	0.75	2.20

OUTLINES



YAGEO						1	1	1	Product specific	$\frac{4}{18}$
	Surface	-Mount Ce	ramic Mu	ltilayer Ca	apacitors	Mid-voltage	NP0/X71	R 100 V to	630 V	18
	NCE RAN	NGE & THIC	CKNESS FO	or npo						
		201 to 0805		<u></u>						
CAP.	0201	0402	0603			0805				
	100V	100V	100 V	200 V	250 V	100 V	200 V	250 V	500 V	630\
0.22 pF							_			
0.47 pF										
0.56 pF										
0.68 pF										
0.82 pF										
I.0 pF										
I.2 pF										
I.5 pF										
I.8 pF										
2.2 pF										
2.7 pF										
3.3 pF										
3.9 pF										
4.7 pF										
5.6 pF	0.3±0.03	0.5±0.05	0.8±0.1	0.8±0.1	0.8±0.1	0.6±0.1	0.6±0.1	0.6±0.1	0.6±0.1	0.6±0.1
6.8 pF	0.5±0.05	0.0 ± 0.00	0.0±0.1	0.0±0.1	0.0±0.1	0.0±0.1	0.0±0.1	0.0±0.1	0.0±0.1	0.0±0.1
8.2 pF										
10 pF										
12 pF										
15 pF										
18 pF										
22 pF										
27 pF										
33 pF										
39 pF										
47 pF										
56 pF										
68 pF										
82 pF										
100 pF										

I. Values in shaded cells indicate thickness class in mm

2. Capacitance value of non E-12 series is on request



Surface-Mount Ceramic Multilayer Capacitors Mid-voltage NP0/X7R 100 V to 630 V

CAPACITANCE RANGE & THICKNESS FOR NPO

Table 4 Sizes from 0603 to 0805 (continued)

CAP.		0603			0805				
		100 V	200 V	250 V	100 V	200 V	250 V	500 V	630 V
	120 pF								
	150 pF					0.6± 0.1	0.6± 0.1	0.6± 0.1	0.6± 0.1
	180 pF								
	220 pF								
	270 pF		0.8± 0.1	0.8± 0.1					
	330 pF	0.8± 0.1	0.01 0.1	0.01 0.1	0.6± 0.1			0.85±0.1	0.85±0.1
	390 pF	0.0±0.1			0.0± 0.1				
	470 pF					0.85±0.1	0.85±0.1		
	560 pF								
	680 pF	_							
	820 pF							1.25±0.2	1.25±0.2
	I.0 nF								
	I.2 nF					±0.1			
	I.5 nF				0.85±0.1				
	I.8 nF								
	2.2 nF								
	2.7 nF					1.25±0.2	1.25±0.2		
	3.3 nF								
	3.9 nF								
	4.7 nF				1.25±0.2				
	5.6 nF								
	6.8 nF								
	8.2 nF								
	10 nF								
	I2 nF								
	15 nF								
	18 nF								
	22 nF								

ΝΟΤΕ

I. Values in shaded cells indicate thickness class in mm

2. Capacitance value of non E-12 series is on request



Product specification 5

18

YAGEO	Phíco	omp							Product s	pecification 6
	Surfac	e-Mount (Ceramic N	Aultilayer	Capacito	Mid-v	oltage	NP0/X7R 100	V to 630 V	18
<u>CAPACIT</u>	ance ra	NGE & T	HICKNESS	FOR NPO						
		1206 to 121		10111110						
CAP.	1206					1210				
	100 V	200 V	250 V	500 V	630 V	100 V	200	✓ 250 V	500 V	630 V
0.47 pF										
0.56 pF										
0.68 pF										
0.82 pF										
I.0 pF										
I.2 pF										
1.5 pF										
1.8 pF										
2.2 pF 2.7 pF										
3.3 pF										
3.9 pF										
4.7 pF										
5.6 pF										
6.8 pF	0.6±0.1	0.6±0.1	0.6±0.1							
8.2 pF										
I0 pF										
I2 pF										
I5 pF										
18 pF										
22 pF										
27 pF				0.6±0.1	I.25±0.2					
33 pF				0.0±0.1	1.2310.2					
39 pF					_					
47 pF										1.25±0.2
56 pF						1.25±0.2	1.25±0.2	2 1.25±0.2	1.25±0.2	
68 pF										
82 pF										

- I. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-12 series is on request

		<u>ANGE & 1</u>			0					
CAP.	Sizes fron	n 1206 to 12	10 (continue	ed)		1210				
0,	100 V	200 V	250 V	500 V	630 V	100 V	200 V	250 V	500 V	630 V
100 pF							_			
120 pF										
150 pF										
180 pF										
220 pF										
270 pF		0.6±0.1	0.6±0.1	0.6±0.1						
330 pF										1.25±0.2
390 pF					1.25±0.2					
470 pF	0.6±0.1									
560 pF	0.0±0.1									
680 pF							1.25±0.2	1.25±0.2	1.25±0.2	
820 pF										
I.0 nF		0.85±0.1	0.85±0.1	0.85±0.1		1.25±0.2				
I.2 nF										
I.5 nF									i	
I.8 nF				1.25±0.2						
2.2 nF		1.25±0.2	1.25±0.2						i	
2.7 nF										
3.3 nF									i	
3.9 nF	0.05 . 0 .									
4.7 nF	0.85±0.1									
5.6 nF										
6.8 nF 8.2 nF										
10 nF	1.25±0.2									
I2 nF										
15 nF										
18 nF										
22 nF										

Surface-Mount Ceramic Multilayer Capacitors Mid-voltage

ΝΟΤΕ

- I. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-12 series is on request

YAGEO Phicomp



7 18

Product specification

NP0/X7R 100 V to 630 V

	Surfac	e-Mount Ceramic	: Multilayer Ca	apacitors 🛛 🛚	lid-voltage	NP0/X7R 100 V	to 630 V
	TANCE RAI	NGE & THICKNE	S FOR NPO				
	Sizes 1812		<u>,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,</u>				
CAP.		1812					
		100 V	200 V	2	250 V	500 V	630\
	10 pF			-	-	-	
	I2 pF						
	15 pF						
	18 pF						
	22 pF						
	27 pF						
	33 pF						
	39 pF						
	47 pF						
	56 pF						
	68 pF						
	82 pF						
	100 pF						
	120 pF						
	150 pF						
	180 pF						
	220 pF						
	270 pF						1.25±0.2
	330 pF						
	390 pF						
	470 pF 560 pF					1.25±0.2	
	680 pF 820 pF						
	l nF						
	I.2 nF			125	±0.2		
	1.5 nF		1.25±0.2	1120	_012		
	1.8 nF						
	2.2 nF						
	2.7 nF	1.25±0.2					
	3.3 nF						
	3.9 nF						
	4.7 nF						
	5.6 nF						
	6.8 nF						
	8.2 nF						
	10 nF						
	12 nF						
	15 nF						
	18 nF						
	22 nF						

I. Values in shaded cells indicate thickness class in $\ensuremath{\mathsf{mm}}$

2. Capacitance value of non E-12 series is on request



YAGEO	Phico	mp					Pro	oduct specificatio
	Surface	-Mount Cer	amic Multil	ayer Capaci	tors Mid-vo	oltage NP0/X	7R 100 V to 6	30 V
<u>CAPACIT</u>	<u>ANCE RAN</u>	<u>IGE & THIC</u>	KNESS FOR	<u>X7R</u>				
1	Sizes from (
CAP.	0402	0603	250.14	0805	200.14	250.)/	500 V	(20.)
	100 V	100 V	250 V	100 V	200 V	250 V	500 V	630 V
100 pF								
150 pF								
220 pF								
330 pF								
470 pF								
680 pF								
I.0 nF	0.5±0.05				0.85±0.1	0.85±0.1	0.85±0.1	0.85±0.1
I.5 nF				0.6±0.1				
2.2 nF			0.8±0.1					
3.3 nF		0.8±0.1						
4.7 nF				-		_		
6.8 nF						-		
I0 nF					1.25±0.2	1.25±0.2	_	1.25±0.2
I5 nF				0.85±0.1			1.25±0.2	
22 nF								
33 nF								
47 nF								
68 nF				1.25±0.2				
100 nF								
150 nF								
220 nF								
330 nF								
470 nF								

I. Values in shaded cells indicate thickness class in mm

2. Capacitance value of non E-6 series is on request

3. For special ordering code, please contact local sales force before order

4. For product with 5% tolerance, please contact local sales force before order



YAGEC	D Phíce	omp							Product specit	
	Surfac	e-Mount (Ceramic N	lultilayer	Capacitor	S Mid-volt	age NP0/2	K7R 100 V	to 630 V	18
CAPACII	TANCE RA	ANGE & TI	HICKNESS	FOR X7R						
	Sizes fron	n 1206 to 12								
CAP.	1206					1210				(20)(
	100 V	200 V	250 V	500 V	630 V	100 V	200 V	250 V	500 V	630V
100 pF										
150 pF										
220 pF										
330 pF										
470 pF										
680 pF										
I.0 nF										
I.5 nF		0.85±0.1	0.85±0.1		I.25±0.2					
2.2 nF	0.05.0.1			1.25±0.2						
3.3 nF	0.85±0.1									
4.7 nF 6.8 nF							0.85±0.1	0.85±0.1		
10 nF										1.25±0.2
I5 nF						0.85±0.1			1.25±0.2	
22 nF				-		0.05±0.1				
33 nF			-	1.6±0.2	1.6±0.2					
47 nF		1.25±0.2	1.25±0.2	1.0±0.2						
68 nF							1.25±0.2	1.25±0.2		
100 nF		1.6±0.2	1.6±0.2				1120 2012	1120 2012		
150 nF	1.25±0.2									
220 nF										
330 nF						I.25±0.2 -				
470 nF	1.6±0.2									
680 nF										
ΙμF						2.0±0.2				
2.2 μF	1.6±0.2									

- I. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-6 series is on request
- 3. For product with 5% tolerance, please contact local sales force before order

YAGEO	Phico	тр						Product	specification 1
	Surface	-Mount Ce	eramic Mul	tilayer Cap	acitors	Mid-voltage	NP0/X7R	100 V to 630 V	18
CAPACITA	ance ran	IGE & THI	<u>CKNESS FO</u>	R X7R					
	Sizes from I								
CAP.	1808		220 1/		1812				
	100 V	200 V	250 V	500 V	100 V	200 V	250 \	/ 500 V	630 V
100 pF									
150 pF									
220 pF									
330 pF									
470 pF									
680 pF									
I.0 nF									
I.5 nF									
2.2 nF									
3.3 nF									
4.7 nF						0.85±0.1	0.85±0.		1.35±0.2
6.8 nF					0.85±0.1			I.25±0.2	
10 nF				1.25±0.2					
I5 nF	1.25±0.2	1.25±0.2	1.25±0.2						
22 nF									
33 nF									1.6±0.2
47 nF									
68 nF						1.25±0.2	1.25±0.2	2	
100 nF								1.6±0.2	
150 nF					1.25±0.2				
220 nF									
330 nF						1.6±0.2	1.6±0.2	2	
470 nF									
680 nF					1.6±0.2				
ΙµF									

- I. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-6 series is on request
- 3. For product with 5% tolerance, please contact local sales force before order



Surface-Mount Ceramic Multilayer Capacitors Mid-voltage NP0/X7R 100 V to 630 V

THICKNESS CLASSES AND PACKING QUANTITY

Table I	3						
SIZE	THICKNESS	TAPE WIDTH	Ø180 MM		Ø330 MM /		QUANTITY
CODE	CLASSIFICATION	QUANTITY PER REEL	Paper	Blister	Paper	Blister	PER BULK CASE
0201	0.3 ±0.03 mm	8 mm	5,000		50,000		
0402	0.5 ±0.05 mm	8 mm	10,000		50,000		50,000
0603	0.8 ±0.1 mm	8 mm	4,000		15,000		15,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		10,000
0805	0.8 / 0.85 ±0.1 mm	8 mm	4,000		15,000		8,000
	1.25 ±0.2 mm	8 mm		3,000		10,000	5,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		
	0.8 / 0.85 ±0.1 mm	8 mm	4,000		15,000		
1206	1.00 / 1.15 ±0.1 mm	8 mm		3,000		10,000	
1200	1.25 ±0.2 mm	8 mm		3,000		10,000	
	1.6 ±0.15 mm	8 mm		2,500		10,000	
	1.6 ±0.2 mm	8 mm		2,000		8,000	
	0.6 / 0.7 ±0.1 mm	8 mm		4,000		15,000	
	0.85 ±0.1 mm	8 mm		4,000		10,000	
_	1.15 ±0.1 mm	8 mm		3,000		10,000	
	1.15 ±0.15 mm	8 mm		3,000		10,000	
	1.25 ±0.2 mm	8 mm		3,000			
1210	1.5 ±0.1 mm	8 mm		2,000			
	1.6 / 1.9 ±0.2 mm	8 mm		2,000			
	2.0 ±0.2 mm	8 mm		2,000 1,000			
	2.5 ±0.2 mm	8 mm		1,000 500			
	1.15 ±0.15 mm	l2 mm		3,000			
	1.25 ±0.2 mm	l2 mm		3,000			
1808	1.35 ±0.15 mm	l2 mm		2,000			
	1.5 ±0.1 mm	l2 mm		2,000			
	1.6 ±0.2 mm	l2 mm		2,000		8,000	
	2.0 ±0.2 mm	l2 mm		2,000			
	0.6 / 0.85 ±0.1 mm	l2 mm		2,000			
	1.15 ±0.1 mm	l2 mm		1,000			
	1.15 ±0.15 mm	l2 mm		1,000			
	1.25 ±0.2 mm	l2 mm		1,000			
1812	1.35 ±0.15 mm	l2 mm		1,000			
	1.5 ±0.1 mm	l2 mm		1,000			
	1.6 ±0.2 mm	l2 mm		1,000			
	2.0 ±0.2 mm	l2 mm		1,000			
	2.5 ±0.2 mm	12 mm		500			

Surface-Mount Ceramic Multilayer Capacitors Mid-voltage NP0/X7R 100 V to 630 V

ELECTRICAL CHARACTERISTICS

NP0/X7R DIELECTRIC CAPACITORS; NISN TERMINATIONS

Unless otherwise specified, all test and measurements shall be made under standard atmospheric conditions for testing as given in 5.3 of IEC 60068-1:

- Temperature: 15 °C to 35 °C
- Relative humidity: 25% to 75%
- Air pressure: 86 kPa to 106 kPa

Before the measurements are made, the capacitor shall be stored at the measuring temperature for a time sufficient to allow the entire capacitor to reach this temperature.

The period as prescribed for recovery at the end of a test is normally sufficient for this purpose.

Table	14		
DESCRIP	TION		VALUE
Capacitar	nce range	0.47 g	pF to 2.2 µF
Capacitar	nce tolerance		
NP0	C < 10 _P F	±0.25	pF, ±0.5 pF
	C ≥ 10 pF	±2%,	±5%, ±10%
X7R		±5% ⁽¹⁾ , ±	±10%, ±20%
Dissipatio	on factor (D.F.)		
NP0	C < 30 _P F	≤ / (4	100 + 20C)
	C ≥ 30 _P F		≤0.1 %
X7R			≤ 2.5 %
Exception	n	X7R /0603/100V, $12nF \le C \le 100nF$, X7R/1206/2.2uF/100V	≤ 5%
_		X7R/1206/100V/1uF; X7R/1210/100V/1uF and 2.2uF;	≤ 3.5%
Insulation	n resistance after 1 minute at U_r (DC)	$R_{ins} \ge 10 \text{ G}\Omega \text{ or } R_{ins} \ge 500 seconds which$	hever is less
	n capacitance change as a function of tempe ture characteristic/coefficient):	rature	
NP0		±	±30 ppm/°C
X7R			±15%
•	g temperature range:		
NP0/X7	′R	_55 °C ⁻	to +125 °C

NOTE

I. Capacitance tolerance ±5% doesn't available for X7R full product range, please contact local sales force before order



Surface-Mount Ceramic Multilayer Capacitors Mid-voltage NP0/X7R 100 V to 630 V

SOLDERING RECOMMENDATION

Table 15

SOLDERING METHOD	SIZE 0201	0402	0603	0805	1206	≥ 2 0
Reflow	Reflow only	> 100 nF	> 1.0 µF	> 2.2 µF	> 2.2 µF	Reflow only
Reflow/Wave		≤ 100 nF	≤ I.0 µF	≤ 2.2 µF	≤ 2.2 µF	

TESTS AND REQUIREMENTS

TEST	TEST METH	HOD	PROCEDURE	REQUIREMENTS
Mounting	IEC 60384- 21/22	4.3	The capacitors may be mounted on printed-circuit boards or ceramic substrates	No visible damage
Visual Inspection and Dimension Check		4.4	Any applicable method using × 10 magnification	In accordance with specification
Capacitance		4.5.I	Class I: $f = MHz \text{ for } C \le nF$, measuring at voltage $ V_{ms} \text{ at } 20 \text{ °C}$ $f = KHz \text{ for } C > nF$, measuring at voltage $ V_{rms} \text{ at } 20 \text{ °C}$ Class 2: $f = KHz \text{ for } C \le 10 \mu\text{F}$, measuring at voltage $ V_{rms} \text{ at } 20 \text{ °C}$	Within specified tolerance
Dissipation Factor (D.F.)		4.5.2	Class I: $f = MHz \text{ for } C \le nF$, measuring at voltage V_{rms} at 20 °C $f = KHz \text{ for } C > nF$, measuring at voltage V_{rms} at 20 °C Class 2: $f = KHz \text{ for } C \le 0 \mu F$, measuring at voltage V_{rms} at 20 °C	In accordance with specification
Insulation Resistance		4.5.3	$U_r \le 500$ V: At Ur for 1 minute $U_r > 500$ V: At 500 V for 1 minute	In accordance with specification

Product specification 14

18

YAGEO Phicomp Surface-Mo

Irface-Mount Ceramic Multilayer Capacitors	Mid-voltage	NP0/X7R	100 V to 630 V

TEST	TEST MET	HOD	PROCEDURE	REQUIREMENTS	
TEST Temperature coefficient	TEST MET	HOD 4.6	Capacitance shall be measured by the steps shown in the following table. The capacitance change should be measured after 5 min at each specified temperature stage. $\boxed{\begin{array}{c c} Step & Temperature(^{\circ}C) \\ a & 25\pm2 \\ \hline b & Lower temperature\pm3^{\circ}C \\ c & 25\pm2 \\ \hline d & Upper Temperature\pm2^{\circ}C \\ \hline e & 25\pm2 \\ \hline d & Upper Temperature\pm2^{\circ}C \\ \hline e & 25\pm2 \\ \hline \end{array}}$ (1) Class I Temperature Coefficient shall be calculated from the formula as below Temp, Coefficient = $\frac{C2 - CI}{CI \times \Delta T} \times 10^6$ [ppm/°C]	REQUIREMENTS <general purpose="" series=""> Class1: Δ C/C: \pm30ppmClass2: X7R: Δ C/C: \pm15% Y5V: Δ C/C: 22~-82%<high capacitance="" series=""> Class2: X7R/X5R: Δ C/C: \pm15% Y5V: Δ C/C: 22~-82%</high></general>	
			CI: Capacitance at step c		
			C2: Capacitance at 125°C		
			$\Delta T: 100^{\circ}C(=125^{\circ}C-25^{\circ}C)$		
			(2) Class II Capacitance Change shall be calculated from the formula as below $\Delta C = \frac{C2 - CI}{CI} \times 100\%$		
			CI: Capacitance at step c		
			C2: Capacitance at step b or d		
Adhesion	IEC 60384- 21/22	4.7	A force applied for 10 seconds to the line joining the terminations and in a plane parallel to the substrate	Force size ≥ 0603: 5N	
Bending Strength		4.8	Mounting in accordance with IEC 60384-22 paragraph 4.3	No visible damage	
			Conditions: bending I mm at a rate of I mm/s, radius jig 5 mm	$\Delta C/C$ Class 1: NP0: within ±1% or 0.5 pF, whichever is greate Class2: X7R: ±10%	



Surface-Mount Ceramic Multilayer Capacitors	Mid-voltage	NP0/X7R	100 V to 630 V

TEST	TEST METH	HOD	PROCEDURE	REQUIREMENTS
Resistance to Soldering		4.9	Precondition: 150 +0/–10 °C for 1 hour, then keep for 24 \pm 1 hours at room	Dissolution of the end face plating shall not exceed 25% of the length of the edge concerned
Heat			temperature Preheating: for size ≤ 1206: 120 °C to 150 °C for 1 minute Preheating: for size >1206: 100 °C to 120 °C for 1 minute and 170 °C to 200 °C for 1	Δ C/C Class 1: NP0: within ±0.5% or 0.5 pF, whichever is greater Class2: X7R: ±10%
			minute Solder bath temperature: 260 ±5 °C Dipping time: 10 ±0.5 seconds Recovery time: 24 ±2 hours	D.F. within initial specified value R _{ins} within initial specified value
Solderability		4.10	Preheated the temperature of 80 °C to 140 °C and maintained for 30 seconds to 60 seconds. 1. Temperature: 235±5°C	The solder should cover over 95% of the critical area of each termination
			 / Dipping time: 2 ±0.5 s 2. Temperature: 245±5°C / Dipping time: 3 ±0.5 s (lead free) Depth of immersion: 10mm 	
Rapid Change of	IEC 60384- 21/22	4.11	Preconditioning; 150 +0/–10 °C for 1 hour, then keep for	No visual damage
Temperature			 24 ±1 hours at room temperature 5 cycles with following detail: 30 minutes at lower category temperature 30 minutes at upper category temperature 	Δ C/C Class 1: NP0: within ±1% or 1 pF, whichever is greater Class2: X7R: ±15%
			Recovery time 24 ±2 hours -	D.F. meet initial specified value R _{ins} meet initial specified value



Surface-Mount Ceramic Multilayer Capacitors Mid-voltage NP0/X7R 100 V to 630 V

TEST	TEST METH	OD	PROCEDURE	REQUIREMENTS
Damp Heat		4.13	 3. Preconditioning, class 2 only: 150 +0/-10 °C /1 hour, then keep for 24 ±1 hour at room temp 4. Initial measure: Spec: refer initial spec C, D, IR 5. Damp heat test: 500 ±12 hours at 40 ±2 °C; 90 to 95% R.H. 6. Recovery: Class 1: 6 to 24 hours Class 2: 24 ±2 hours 7. Final measure: C, D, IR P.S. If the capacitance value is less than the minimum value permitted, then after the other measurements have been made the capacitor shall be precondition according to "IEC 60384 4.1" and then the requirement shall be met. 	No visual damage after recovery $\Delta C/C$ Class 1: NP0: within ±2% or 1 pF, whichever is greater Class2: X7R: ±15% D.F. Class 1: NP0: $\leq 2 \times$ specified value Class2: X7R: $\geq 25 \lor \leq 5\%$ R _{ins} Class 1: NP0: $\geq 2,500 \text{ M}\Omega \text{ or } R_{ins} \times C_r \geq 25 \text{ whichever is less}$ Class2: X7R: $\geq 500 \text{ M}\Omega \text{ or } R_{ins} \times C_r \geq 25 \text{ whichever is less}}$
Endurance	IEC 60384- 21/22	4.14	 Preconditioning, class 2 only: 150 +0/-10 °C /1 hour, then keep for 24 ±1 hour at room temp Initial measure: Spec: refer initial spec C, D, IR Endurance test: Temperature: NP0/X7R: 125 °C Specified stress voltage applied for 1,000 hours: High voltage series follows with below stress condition: Applied 2.0 × Ur for 100 V series Applied 1.5 × Ur for 200/250 V series Applied 1.3 × Ur for 500 V, 630 V series Applied 1.2 × U_r for 1 KV, 2 KV, 3 KV series Recovery time: 24 ±2 hours Final measure: C, D, IR P.S. If the capacitance value is less than the minimum value permitted, then after the other measurements have been made the capacitor shall be precondition according to "IEC 60384 4.1" and then the requirement shall be met. 	No visual damage $\Delta C/C$ Class I: NP0: within ±2% or 1 pF, whichever is greater Class2: X7R: ±15% D.F. Class I: NP0: $\leq 2 \times$ specified value Class2: X7R: $\geq 25 \lor : \leq 5\%$ R _{ins} Class I: NP0: $\geq 4,000 \text{ M}\Omega \text{ or}$ R _{ins} $\times C_r \geq 40s \text{ whichever is less}$ Class2: X7R: $\geq 1,000 \text{ M}\Omega \text{ or}$ R _{ins} $\times C_r \geq 50s \text{ whichever is less}$
Voltage Proof		4.6	Specified stress voltage applied for $1 \sim 5$ seconds $Ur \leq 100 V$: series applied 2.5 Ur $100 V < Ur \leq 200 V$ series applied (1.5 Ur + 100) $200 V < Ur \leq 500 V$ series applied (1.3 Ur + 100) Ur > 500 V: 1.3 Ur $Ur \geq 1000 V$: 1.2 Ur Charge/Discharge current is less than 50 mA	No breakdown or flashover



Surface-Mount Ceramic Multilayer Capacitors Mid-voltage NP0/X7R 100 V to 630 V

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 19	Mar 7, 2017	-	- 0805 L4 spec updated
Version 18	Dec 9, 2016	-	- Soldering recommendation update
Version 17	Aug 16, 2016	-	- Capacitance range & thickness update
Version 16	Apr. 16, 2015	-	- Capacitance range & thickness
Version 15	Apr. 16, 2015	-	- Electrical characteristics update
Version 14	Sep. 25, 2014	-	- Electrical characteristics update
Version 13	Apr. 21, 2014	-	- Electrical characteristics update
Version 12	Dec. 12, 2013	-	- Electrical characteristics update
Version 11	Jun. 17, 2013	-	- Test method and procedure updated
Version 10	Nov 22, 2012	-	- Test method and procedure updated
Version 9	Feb 02, 2012	-	- Test method and procedure updated
Version 8	Apr 22, 2011	-	- NP0 0402 100V added
Version 7	Mar 01, 2011	-	- Dimension updated
Version 6	Sep 30, 2010	-	- Update the thickness of 0805 100V
Version 5	Sep 28, 2010	-	- Product range updated
			- Thickness classes and packing quantity table updated
Version 4	Jun 17, 2010	-	- Update the dimension of 0805, 1206 and 1812
Version 3	Mar 25, 2010	-	- Product range update
Version 2	Mar 15, 2010	-	- Product range update
Version I	Oct 30, 2009	-	- Change to dual brand datasheet that describe Mid-voltage NP0/X7R series with RoHS compliant
			- Replace the "100V to 630V" part of pdf files: UP-NP0X7R_MV_100-to- 500V_0, UY-NP0X7R_MV_100-to-500V_0, NP0_16V-to-100V_6, NP0_50-to-500V_10, X7R_16-to-500V_9 and X7R_16V-to-100V_9
			- Define global part number
			- Description of "Halogen Free compliant" added
			- Test method and procedure updated
Version 0	Sep 08, 2005	_	- New

<u>REVISION HISTORY</u>



Product specification 18

18

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for Multilayer Ceramic Capacitors MLCC - SMD/SMT category:

Click to view products by Yageo manufacturer:

Other Similar products are found below :

D55342E07B523DR-T/R NCA1206X7R103K50TRPF NCA1206X7R104K16TRPF NIN-FB391JTRF NIN-FC2R7JTRF NMC0402NPO220J50TRPF NMC0402X5R105K6.3TRPF NMC0402X5R224K6.3TRPF NMC0402X7R103J25TRPF NMC0402X7R153K16TRPF NMC0603NPO330G50TRPF NMC0603NPO331F50TRPF NMC0603X5R475M6.3TRPF NMC0805NPO220J100TRPF NMC0805NPO270J50TRPF NMC0603NPO681F50TRPF NMC0805NPO820J50TRPF NMC0805X7R224K25TRPF NMC1206X7R102K50TRPF NMC1210Y5V105Z50TRPLPF NMC-H0805X7R472K250TRPF NMC-L0402NPO7R0C50TRPF NMC12063NPO2R2B50TRPF NMC-Q0402NPO8R2D200TRPF C1206C101J1GAC C1608C0G2A221J C1608X7R1E334K C2012C0G2A472J 2220J2K00562KXT KHC201E225M76N0T00 1812J2K00332KXT CCR06CG153FSV CDR14BP471CJUR CDR31BX103AKWR CDR33BX683AKUS CGA2B2C0G1H010C CGA2B2C0G1H040C CGA2B2C0G1H050C CGA2B2C0G1H060D CGA2B2C0G1H070D CGA2B2C0G1H120J CGA2B2C0G1H151J CGA2B2C0G1H07C CGA2B2C0G1H050C CGA2B2C0G1H390J CGA2B2C0G1H391J CGA2B2C0G1H3R3C CGA2B2C0G1H680J CGA2B2C0G1H6R8D CGA2B2C0G1H820J