

*Innovative Service Around the Globe*

**YAGEO**

# DATA SHEET

## THYRISTOR SURGE SUPPRESSORS MODEMS/LINE CARD

P61089B series

RoHS compliant & Halogen free



Product specification- December 18, 2018 v.0



## Thyristor Surge Suppressors (TSS) Data Sheet

### General Description

This device has been especially designed to protect 2 new high voltage, as well as classical SLICs, against transient overvoltages.

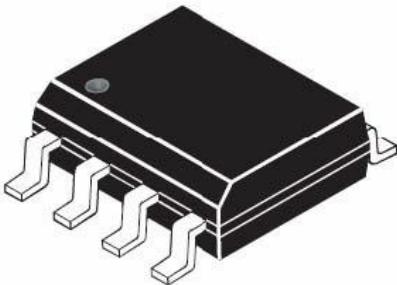
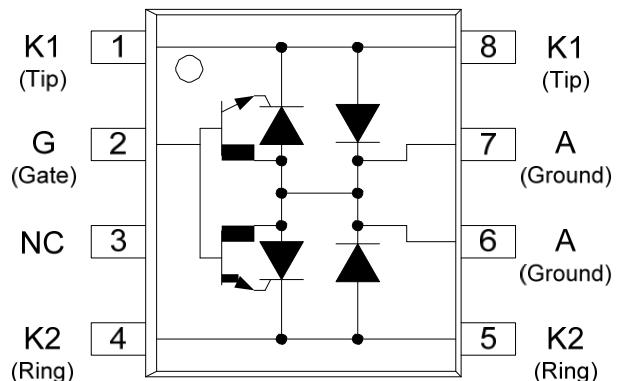
Positive overvoltages are clamped by 2 diodes. Negative surges are suppressed by 2 thyristors, their breakdown voltage being referenced to  $-V_{BAT}$  through the gate.

This component presents a very low gate triggering current ( $I_{GT}$ ) in order to reduce the current consumption on printed circuit board during the firing phase.

This devices are not subject to ageing and provide a fail safe mode in short circuit for a better protection. They are used to help equipment to meet various standards such as UL1950, IEC950/CSA C22.2, UL1459 and FCC part68.

### Features

- Dual line programmable transient voltage suppressor
- Wide negative firing voltage range:  $V_{MGL} = -155V$
- Holding current:  $I_H > 150mA$
- Marking: H61089B
- Low dynamic switching voltages:  $V_{FP}$  and  $V_{DGL}$
- Low gate triggering current:  $I_{GT} = 5mA$  max
- Halogen Free

Package	Device Symbol
 SOP-8	  K1 (Tip) G (Gate) NC K2 (Ring)  8 K1 (Tip) 7 A (Ground) 6 A (Ground) 5 K2 (Ring)

## Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ )

Parameter	Symbol	Value	Unit
Repetitive peak off-state voltage, $V_{GK}=0$	$V_{DRM}$	-170	V
Repetitive peak gate-cathode voltage, $V_{KA}=0$	$V_{GKRM}$	-170	V
Non-repetitive peak on-state current 10/1000μs (Telcordia (Bellcore) GR-1089-CORE.Issue 2.February 1999, Section4) 5/320μs (ITU-T K.20, K.21 & K.45, K.44 open-circuit voltage wave shape 10/700μs) 1.2/50μs (Telcordia (Bellcore) GR-1089-CORE.Issue 2.February 1999, Section4) 2/10μs (Telcordia (Bellcore) GR-1089-CORE.Issue 2.February 1999, Section4)	$I_{PPSM}$	30 40 100 120	A
Non-repetitive peak on-state current. $V_{GG}=-75\text{V}$ 50Hz to 60Hz 0.1s 1s 5s 300s 900s	$I_{TSM}$	11 4.8 2.7 0.95 0.93	A
Operating free-air temperature range	$T_A$	-40 to +85	°C
Operating junction temperature range	$T_J$	-40 to +150	°C
Storage temperature range	$T_{STG}$	-40 to +150	°C
Lead soldering temperature, 10 seconds	$T_{LS}$	300(Mix.)	°C

## Thermal Characteristics

Parameter	Test Conditions	Max	Unit
$R_{\theta JA}$ Junction to free air thermal temperature	$T_A = 25^\circ\text{C}$ , EIA/JESD51-3 PCB, EIA/JESD51-2 environment, $P_{TOT} = 1.7\text{W}$	120	°C/W

## Parameter Measurement Information

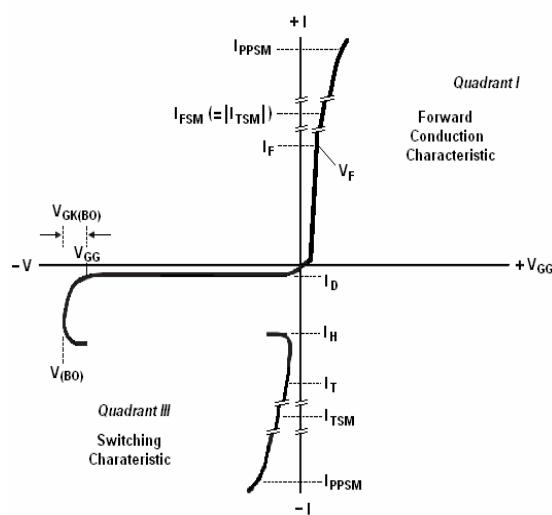


Figure 1. Voltage-Current Characteristic  
Unless otherwise noted, all voltages are referenced to the anode

## Electrical Characteristics, Rating at 25°C unless otherwise specified

Parameter		Test Conditions	Min.	Typ.	Max.	Unit
I <sub>D</sub>	Off-state current	V <sub>D</sub> =V <sub>DRM</sub> , V <sub>GK</sub> =0, V <sub>G2</sub> ≥+5V T <sub>J</sub> =25°C T <sub>J</sub> =85°C			-5 -50	μA
V <sub>(BO)</sub>	Breakover voltage	2/10μs, I <sub>PP</sub> =-56A, R <sub>S</sub> =45Ω, V <sub>GG</sub> =-48V, C <sub>G</sub> =220nF 1.2/50μs, I <sub>PP</sub> =-53A, R <sub>S</sub> =47Ω, V <sub>GG</sub> =-48V, C <sub>G</sub> =220nF		-57 -60		V
V <sub>GK(BO)</sub>	Gate-cathode impulse breakover voltage	2/10μs, I <sub>PP</sub> =-56A, R <sub>S</sub> =45Ω, V <sub>GG</sub> =-48V, C <sub>G</sub> =220nF 1.2/50μs, I <sub>PP</sub> =-53A, R <sub>S</sub> =47Ω, V <sub>GG</sub> =-48V, C <sub>G</sub> =220nF		9 12	20	V
V <sub>F</sub>	Forward voltage	I <sub>F</sub> =5A, T <sub>W</sub> =200μs			3	V
V <sub>FRM</sub>	Peak forward recovery voltage	2/10μs, I <sub>PP</sub> =-56A, R <sub>S</sub> =45Ω, V <sub>GG</sub> =-48V, C <sub>G</sub> =220nF 1.2/50μs, I <sub>PP</sub> =-53A, R <sub>S</sub> =47Ω, V <sub>GG</sub> =-48V, C <sub>G</sub> =220nF		6 8		V
I <sub>H</sub>	Holding current	I <sub>T</sub> =-1A, di/dt=1A/ms, V <sub>GG</sub> =-48V	-150			mA
I <sub>GKS</sub>	Gate reverse current	V <sub>GG</sub> =V <sub>GK</sub> =V <sub>GKRM</sub> , V <sub>KA</sub> =0 T <sub>J</sub> =25°C T <sub>J</sub> =85°C			-5 -50	μA
I <sub>GT</sub>	Gate trigger current	I <sub>T</sub> =-3A, t <sub>p(g)</sub> ≥20μs, V <sub>GG</sub> =-48V			5	mA
V <sub>GT</sub>	Gate-cathode trigger voltage	I <sub>T</sub> =-3A, t <sub>p(g)</sub> ≥20μs, V <sub>GG</sub> =-48V		2.5	4	V
Q <sub>GS</sub>	Gate switching charge	1.2/50μs, I <sub>PP</sub> =-53A, R <sub>S</sub> =47Ω, V <sub>GG</sub> =-48V, C <sub>G</sub> =220nF		0.1		μC
C <sub>KA</sub>	Cathode-anode off- state capacitance	F=1MHz, V <sub>D</sub> =1V, I <sub>G</sub> =0 V <sub>D</sub> =-3V V <sub>D</sub> =-48V			100 50	pF

## Typical Characteristics

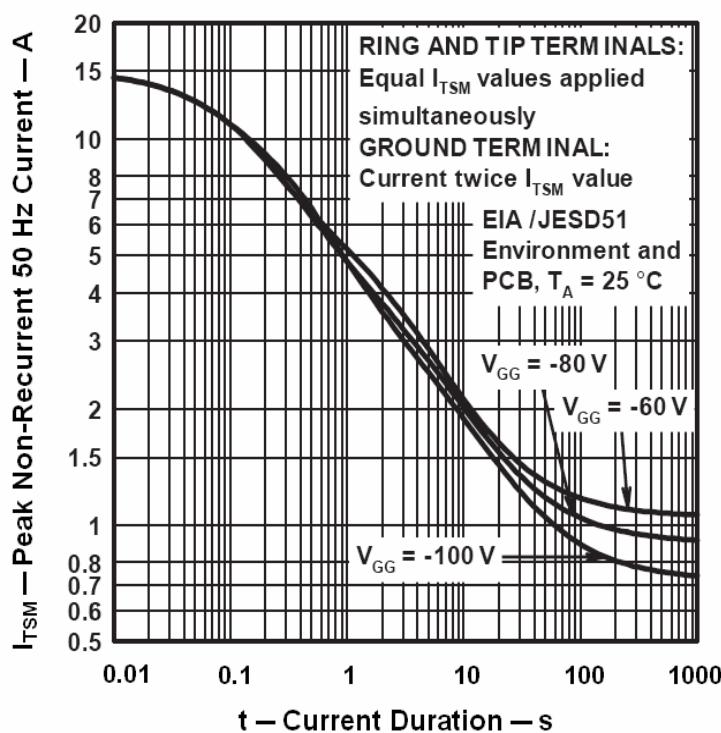
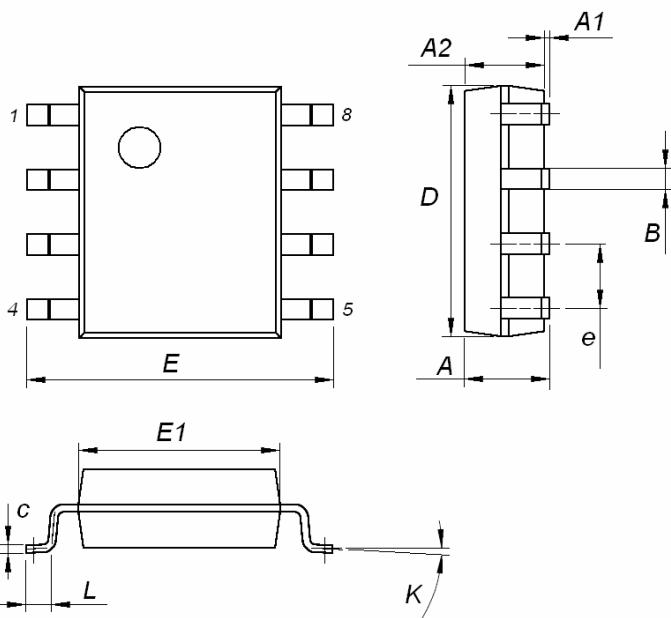


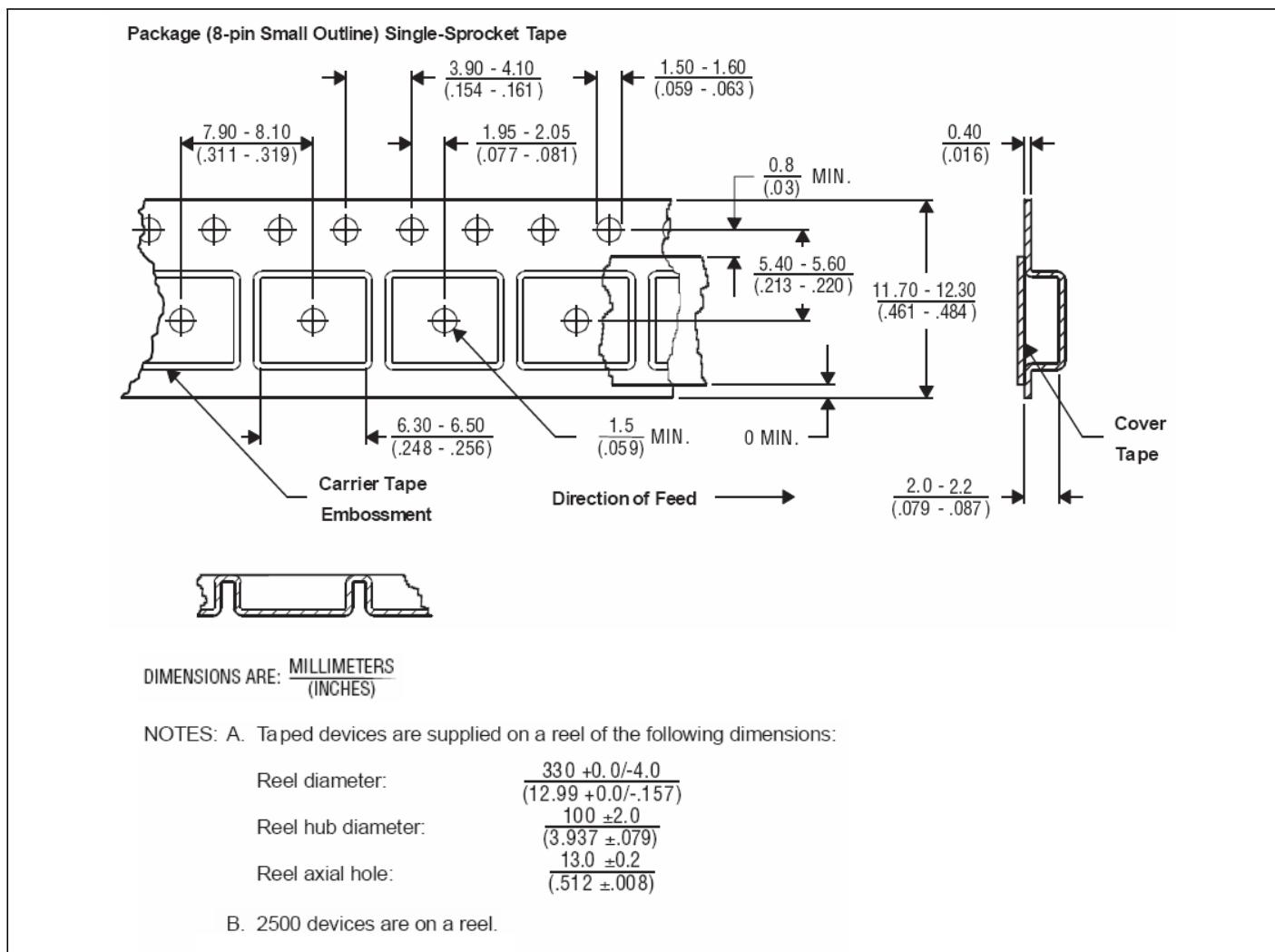
Figure 2. Non-repetitive Peak On-State Current Against Duration

## Dimensions (SOP-8)



Symbol	Dimension (mm)		
	Min.	Typ.	Max.
A			1.75
A1	0.10		0.25
A2	1.35	1.55	1.75
B	0.35	0.42	0.49
C	0.19		0.25
D	4.80	4.90	5.00
E	5.80	6.00	6.20
E1	3.80	3.95	4.00
e		1.27	
L	0.40		0.90
K	0°		8°

## Tape Package Information



# X-ON Electronics

Largest Supplier of Electrical and Electronic Components

***Click to view similar products for Thyristor Surge Protection Devices - TSPD category:***

***Click to view products by Yageo manufacturer:***

Other Similar products are found below :

[BEP3100TA](#) [P0720SB](#) [P0720SC](#) [P1100SC](#) [P2300SB](#) [P2300SD](#) [P2600SB](#) [P3500SB](#) [SKKH 57/16E](#) [SKKH 72/22E](#) [H4](#) [SKKH 72/08E](#)  
[NP1100SAT3G](#) [NP3100SBT3G](#) [SK20NBMH10](#) [P3800FNLTP](#) [TISP4P035L1NR-S](#) [TISP4011H1BJR-S](#) [SKKH 72/20E](#) [H4](#) [SKKH92/16E](#)  
[SKKH 172/16E](#) [TISP4350H3BJR-S](#) [TISP4A265H3BJR](#) [TISP7082F3DR-S](#) [TB0640H-13-F](#) [TB3100H-13-F](#) [TB3100M-13-F](#) [TB3500L-13-F](#)  
[TD330N16KOFHPSA2](#) [P0080EAL](#) [P0080ECL](#) [P0080Q22CLRP](#) [P0080S3NLRP](#) [P0080SALRP](#) [P0080SAMCLRP](#) [P0080SB](#) [P0080SBLRP](#)  
[P0080SCLRP](#) [P0080SCMCLRP](#) [P0080SDLRP](#) [P3203UCLRP](#) [P0220SALRP](#) [P0220SCMCLRP](#) [P0300EAL](#) [P0300SALRP](#) [P0300SBLRP](#)  
[P0300SCLRP](#) [P0300SCMCLRP](#) [P3100Q12BLRP](#) [P0640SALRP](#) [P0640SBLRP](#)