

# DATA SHEET

## AUTOMOTIVE GRADE SURGE CHIP RESISTORS

SR series

20%, 10%, 5%

sizes 0402/0603/0805/1206/1210/1218/2010/2512

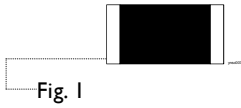
RoHS compliant & Halogen free





**MARKING**

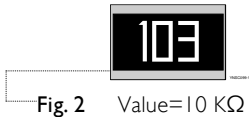
**SR0402**



No Marking

Fig. 1

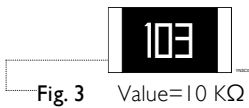
**SR1218**



E-24 series: 3 digits  
First two digits for significant figure and 3rd digit for number of zeros

Fig. 2 Value=10 KΩ

**SR0603 / SR0805 / SR1206 / SR1210 / SR2010 / SR2512**



E-24 series: 3 digits  
First two digits for significant figure and 3rd digit for number of zeros

Fig. 3 Value=10 KΩ

**NOTE**

For further marking information, please refer to data sheet “Chip resistors marking”.

**TAPING REEL & POWER**

Table 1

TYPE	POWER, W (P70)			
	CODING			
	07	7W	7T	47
0402	1/16	1/8	1/5	-
0603	1/10	1/5	1/4	-
0805	1/8	1/4	1/3	1/2
1206	1/4	1/2	3/4	1
1210	1/2	1	-	-
1218	1	1.5	-	-
2010	3/4	1.25	-	-
2512	1	2	-	-

**CONSTRUCTION**

The resistor is constructed on top of a high-grade ceramic body. Internal metal electrodes are added at each end and connected by a resistive glaze. The resistive glaze is covered by a lead-free glass. The composition of the glaze is adjusted to give the approximately required resistance value. The whole element is covered by a protective overcoat. The top of overcoat is marked with the resistance value. Finally, the two external terminations (Ni/matte tin) are added, as shown in Fig.4.

**OUTLINES**

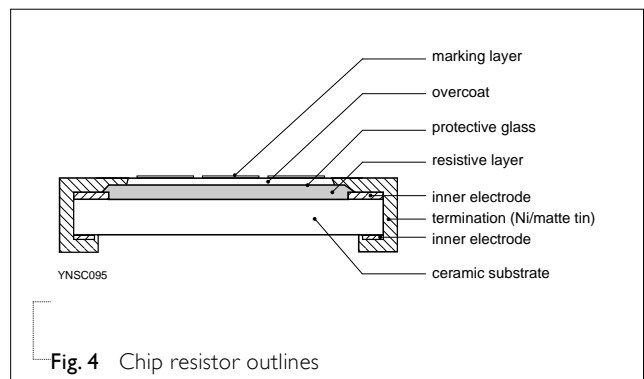
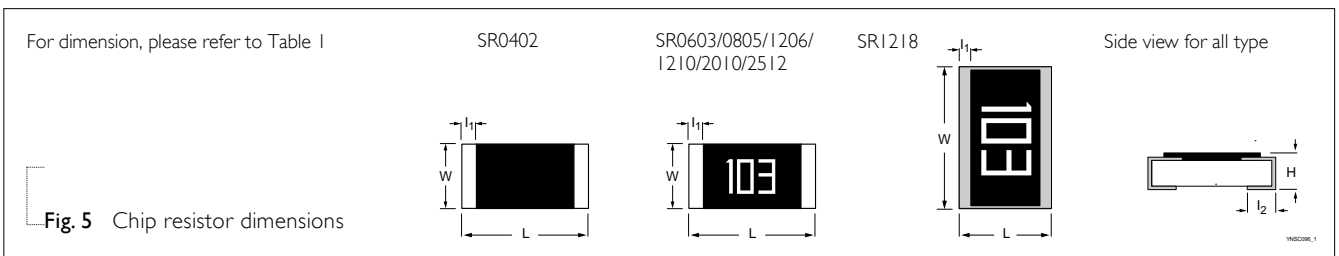


Fig. 4 Chip resistor outlines

**DIMENSIONS**

Table 2

TYPE	L (mm)	W (mm)	H (mm)	I <sub>1</sub> (mm)	I <sub>2</sub> (mm)
SR0402	1.00±0.05	0.50±0.05	0.35±0.05	0.20±0.10	0.25±0.10
SR0603	1.60±0.10	0.80±0.10	0.45±0.10	0.25±0.15	0.25±0.15
SR0805	2.00±0.10	1.25±0.10	0.50±0.10	0.35±0.20	0.35±0.20
SR1206	3.10±0.10	1.60±0.10	0.55±0.10	0.45±0.20	0.40±0.20
SR1210	3.10±0.10	2.60±0.15	0.55±0.10	0.45±0.15	0.50±0.20
SR1218	3.10±0.10	4.60±0.10	0.55±0.10	0.45±0.20	0.40±0.20
SR2010	5.00±0.10	2.50±0.15	0.55±0.10	0.55±0.15	0.50±0.20
SR2512	6.35±0.10	3.10±0.15	0.55±0.10	0.60±0.20	0.50±0.20



**ELECTRICAL CHARACTERISTICS**

Table 3

TYPE	POWER	RESISTANCE RANGE	CHARACTERISTICS				
			Operating Temperature Range	Max. Working Voltage	Max. Overload Voltage	Dielectric Withstanding Voltage	Temperature Coefficient of Resistance
SR0402	1/16W	E24 5%, 10%, 20% 1 Ω ≤ R ≤ 1M Ω	-55 °C to +155 °C	50 V	100 V	100 V	10 Ω < R ≤ 1M Ω ±100 ppm/°C
	1/8W						
	1/5W						
SR0603	1/10W	E24 5%, 10%, 20% 1 Ω ≤ R ≤ 1M Ω	-55 °C to +155 °C	75V	150V	150V	10 Ω < R ≤ 1M Ω ±100 ppm/°C
	1/5W						
	1/4W						
SR0805	1/8 W	E24 5%, 10%, 20% 1 Ω ≤ R ≤ 1M Ω	-55 °C to +155 °C	150V	300V	300V	10 Ω < R ≤ 1M Ω ±100 ppm/°C
	1/4W						
	1/3W						
SR1206	1/2W	E24 5%, 10%, 20% 1 Ω ≤ R ≤ 1M Ω	-55 °C to +155 °C	200 V	400 V	500 V	10 Ω ≤ R ≤ 10Ω ±200 ppm/°C
	1/4 W						
	3/4W						
	1W						
SR1210	1/2W	E24 5%, 10%, 20% 1 Ω ≤ R ≤ 1M Ω	-55 °C to +155 °C	200 V	400 V	500 V	10 Ω ≤ R ≤ 10Ω ±200 ppm/°C
	1W						
SR1218	1W	E24 5%, 10%, 20% 1 Ω ≤ R ≤ 1M Ω	-55 °C to +155 °C	200 V	400 V	500 V	10 Ω ≤ R ≤ 10Ω ±200 ppm/°C
	1.5W						
SR2010	3/4W	E24 5%, 10%, 20% 1 Ω ≤ R ≤ 1M Ω	-55 °C to +155 °C	200 V	400 V	500 V	10 Ω ≤ R ≤ 10Ω ±200 ppm/°C
	1.25W						
SR2512	1 W	E24 5%, 10%, 20% 1 Ω ≤ R ≤ 1M Ω	-55 °C to +155 °C	200 V	400 V	500 V	10 Ω ≤ R ≤ 10Ω ±200 ppm/°C
	2W						

**FOOTPRINT AND SOLDERING PROFILES**

Recommended footprint and soldering profiles, please refer to data sheet “Chip resistors mounting”.

**PACKING STYLE AND PACKAGING QUANTITY**

Table 4 Packing style and packaging quantity

PACKING STYLE	REEL DIMENSION	SR0402	SR0603/0805/1206	SR1210	SR1218/2010/2512
Paper taping reel (R)	7" (178 mm)	10,000	5,000	5,000	---
	13" (330 mm)	50,000	20,000	20,000	---
Embossed taping reel (K)	7" (178 mm)	---	---	---	4,000

**NOTE**

I. For paper/embossed tape and reel specification/dimensions, please refer to data sheet “Chip resistors packing”.

**FUNCTIONAL DESCRIPTION**

**OPERATING TEMPERATURE RANGE**

Range: -55 °C to +155 °C

**POWER RATING**

Each type rated power at 70 °C:

- SR0402: 1/16W, 1/8W, 1/5W
- SR0603: 1/10W, 1/5W, 1/4W
- SR0805: 1/8W, 1/4W, 1/3W, 1/2W
- SR1206: 1/4W, 1/2W, 3/4W, 1W
- SR1210: 1/2W, 1W
- SR1218: 1W, 1.5W
- SR2010: 3/4W, 1.25W
- SR2512: 1W, 2W

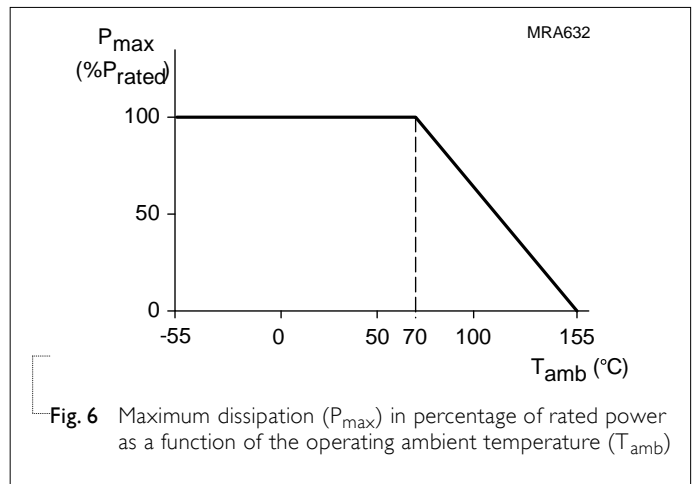
**RATED VOLTAGE**

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

$$V = \sqrt{P \times R}$$

Where

- V = Continuous rated DC or AC (rms) working voltage (V)
- P = Rated power (W)
- R = Resistance value (Ω)



**PULSE LOAD BEHAVIOR**

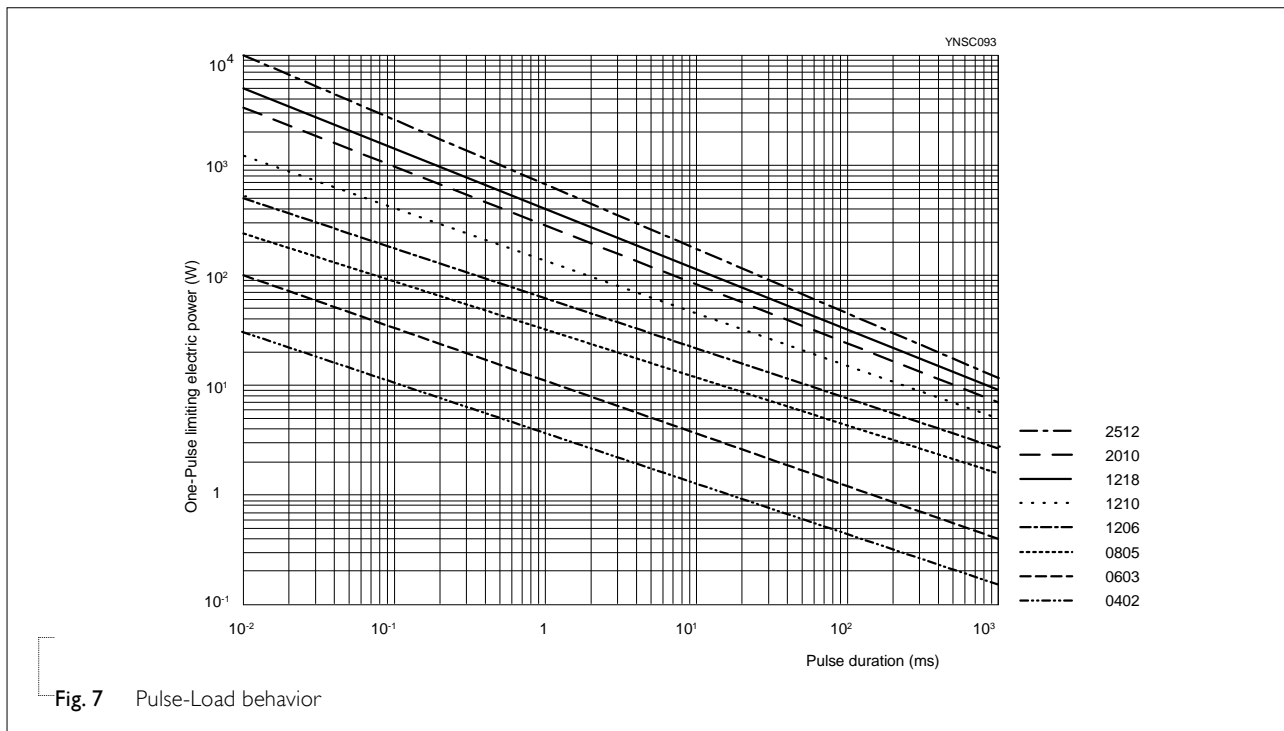


Fig. 7 Pulse-Load behavior

**TESTS AND REQUIREMENTS**

Table 5 Test condition, procedure and requirements

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
High Temperature Exposure	AEC-Q200 Test 3 MIL-STD-202 Method 108	1,000 hours at T <sub>A</sub> = 155 °C, unpowered	±(3.0%+0.05Ω)
Moisture Resistance	AEC-Q200 Test 6 MIL-STD-202 Method 106	Each temperature / humidity cycle is defined at 8 hours (method 106F), 3 cycles / 24 hours for 10d. with 25 °C / 65 °C 95% R.H, without steps 7a & 7b, unpowered	±(2.0%+0.05Ω)
Biased Humidity	AEC-Q200 Test 7 MIL-STD-202 Method 103	1,000 hours; 85 °C / 85% RH 10% of operating power Measurement at 24±4 hours after test conclusion.	±(3.0%+0.05Ω)
Operational Life	AEC-Q200 Test 8 MIL-STD-202 Method 108	1,000 hours at 125 °C, derated voltage applied for 1.5 hours on, 0.5 hour off, still-air required	±(3.0%+0.05Ω)
Resistance to Soldering Heat	AEC-Q200 Test 15 MIL-STD-202 Method 210	Condition B, no pre-heat of samples Lead-free solder, 260±5 °C, 10±1 seconds immersion time Procedure 2 for SMD: devices fluxed and cleaned with isopropanol	±(1.0%+0.05Ω) No visible damage

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Thermal Shock	AEC-Q200 Test I6	-55/+125 °C	±(1.0%+0.05Ω)
	MIL-STD-202 Method I07	Number of cycles is 300. Devices mounted Maximum transfer time is 20 seconds. Dwell time is 15 minutes. Air – Air	
ESD	AEC-Q200 Test I7	Human Body Model,	±(3.0%+0.05Ω)
	AEC-Q200-002	I <sub>pos.</sub> + I <sub>neg.</sub> discharges 0201: 500V 0402/0603: 1KV 0805 and above: 2KV	
Solderability - Wetting	AEC-Q200 Test I8	Electrical Test not required Magnification 50X	Well tinned (≥95% covered) No visible damage
	J-STD-002	SMD conditions: (a) Method B, aging 4 hours at 155 °C dry heat, dipping at 235±3 °C for 5±0.5 seconds. (b) Method B, steam aging 8 hours, dipping at 215±3 °C for 5±0.5 seconds. (c) Method D, steam aging 8 hours, dipping at 260±3 °C for 30±0.5 seconds.	
Board Flex	AEC-Q200 Test 21	Chips mounted on a 90mm glass epoxy resin PCB (FR4)	±(1.0%+0.05Ω)
	AEC-Q200-005	Bending for 0201/0402: 5 mm 0603/0805: 3 mm 1206 and above: 2 mm Holding time: minimum 60 seconds	
Temperature Coefficient of Resistance (T.C.R.)	MIL-STD-202 Method 304	At +25/-55 °C and +25/+125 °C	Refer to table 2
		<b>Formula:</b> $T.C.R = \frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/°C)}$ <p>Where</p> <p>t<sub>1</sub> = +25 °C or specified room temperature  t<sub>2</sub> = -55 °C or +125 °C test temperature  R<sub>1</sub> = resistance at reference temperature in ohms  R<sub>2</sub> = resistance at test temperature in ohms</p>	
Short Time Overload	IEC60115-1 4.13	2.5 times of rated voltage or maximum overload voltage whichever is less for 5 sec at room temperature	±(2.0%+0.05Ω)

REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 9	Aug. 04, 2021	-	- Upgrade to Automotive Grade
Version 8	Jul. 22, 2019	-	- Update power rating
Version 7	Sep. 27, 2018	-	- Extend resistance range of 0402 ~ 2512 to 1Mohm, - Tighten TCR of all sizes for $10\Omega < R \leq 1M\Omega$ from $\pm 200$ ppm/°C to $\pm 100$ ppm/°C - Add SR1210, SR1218, SR2010 7W (double power)
Version 6	Oct. 02, 2017	-	- Add SR0402 7T (triple power), SR0805 47 (quadruple power), SR2512 7W (double power)
Version 5	Nov. 11, 2016	-	- Update 7T power for 1206
Version 4	Sep. 01, 2015	-	- Update SR0603 Dielectric Withstanding Voltage to 150V - Update 7T power for 0603/0805 & 7W for 1210
Version 3	Jul. 31, 2015	-	- Comply with AEC-Q200 standard
Version 2	Jan. 06, 2014	-	- Add SR0402/0603/1210 - Update electrical characteristic
Version 1	Mar 18, 2011	-	- Change to dual brand datasheet that describes SR0805 to SR2512 with RoHS compliant - Define global part number
Version 0	Oct 19, 2004	-	-

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