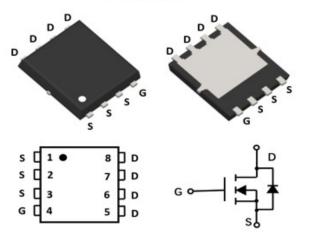
# **N-Channel Enhancement Mode Field Effect Transistor**

## PDFN 5X6



## **Product Summary**

- V<sub>DS</sub>
- ID
- R<sub>DS(ON)</sub>( at V<sub>GS</sub>= 10V)
- R<sub>DS(ON)</sub>( at V<sub>GS</sub>= 4.5V) • 100% UIS Tested
- 100% UIS Tested
   100% ⊽V<sub>DS</sub> Tested

## **General Description**

• Trench Power MV MOSFET technology

30V

105A

<3.0mohm

<4.0mohm

- Excellent package for heat dissipation
- High density cell design for low R<sub>DS(ON)</sub>

## **Applications**

- DC-DC Converters
- Power management functions
- Backlighting

## ■ Absolute Maximum Ratings (T<sub>A</sub>=25°Cunless otherwise noted)

| Parameter                                      |         | Symbol                           | Limit    | Unit  |  |
|--|---------|----------------------------------|----------|-------|--|
| Drain-source Voltage                           |         | V <sub>DS</sub>                  | 30       | V     |  |
| Gate-source Voltage                            |         | V <sub>GS</sub>                  | ±20      | V     |  |
| Drain Current                                  | Tc=25℃  | 1                                | 105      | А     |  |
|  | Tc=100℃ | - I <sub>D</sub>                 | 66       | A     |  |
| Pulsed Drain Current <sup>A</sup>              |         | I <sub>DM</sub>                  | 415      | A     |  |
| Total Power Dissipation @ T <sub>c</sub> =25°C |         | P <sub>D</sub>                   | 49       | W     |  |
| Single Pulse Avalanche Energy <sup>B</sup>     |         | E <sub>AS</sub>                  | 507      | mJ    |  |
| Thermal Resistance Junction-to-Case            |         | R <sub>eJC</sub>                 | 2.55     | °C/ W |  |
| Junction and Storage Temperature Range         |         | T <sub>J</sub> ,T <sub>STG</sub> | -55~+150 | °C    |  |

### Ordering Information (Example)

| PREFERED P/N | PACKING<br>CODE | Marking    | MINIMUM<br>PACKAGE(pcs) | INNER BOX<br>QUANTITY(pcs) | OUTER CARTON<br>QUANTITY(pcs) | DELIVERY MODE |  |
|--------------|-----------------|------------|-------------------------|----------------------------|-------------------------------|---------------|--|
| YJG105N03A   | F1              | YJG105N03A | 5000                    | 10000                      | 100000                        | 13" reel      |  |



# YJG105N03A

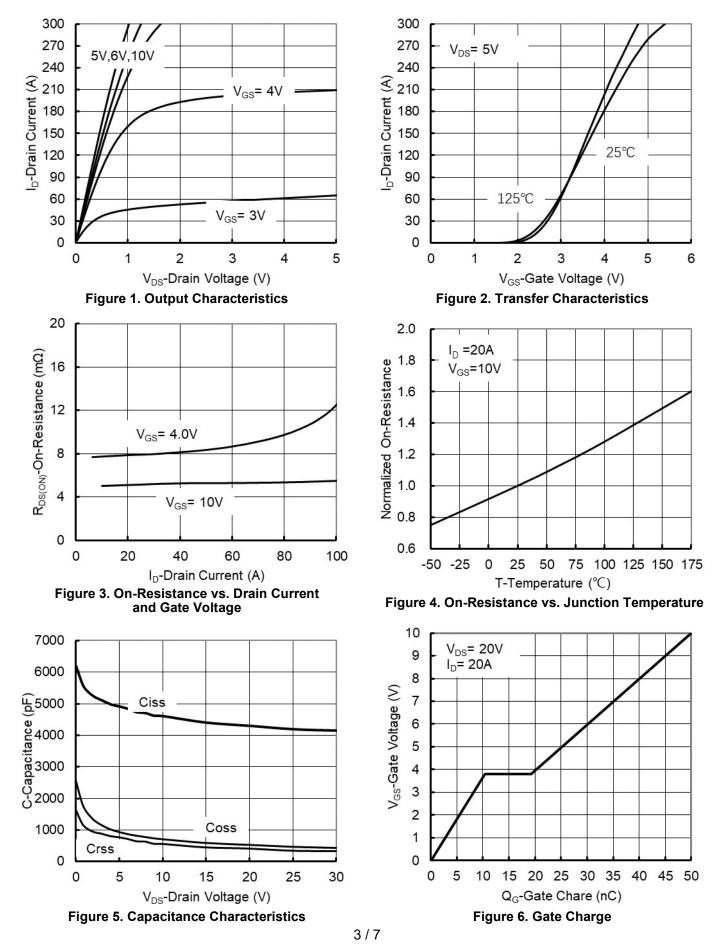
#### ■ Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)

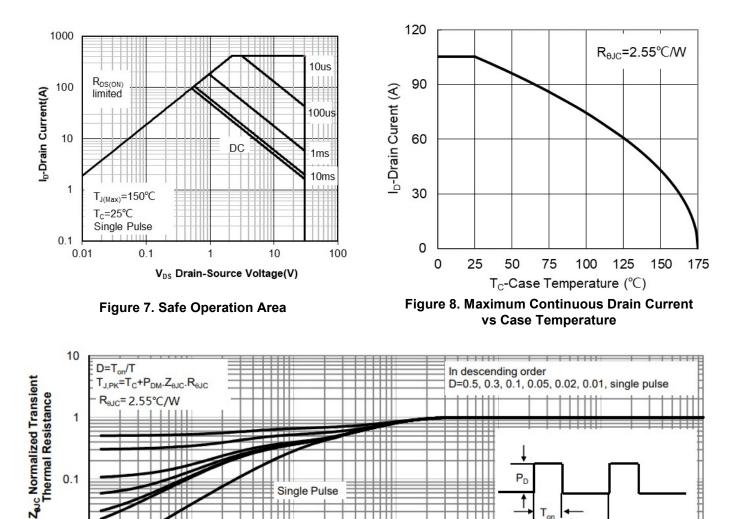
| Parameter                             | Symbol              | Conditions   | Min | Тур  | Мах  | Units |  |
|---------------------------------------|---------------------|--|-----|------|------|-------|--|
| Static Parameter                      |                     |  |     |      |      |       |  |
| Drain-Source Breakdown Voltage        | BV <sub>DSS</sub>   | $V_{GS}$ = 0V, I <sub>D</sub> =250µA                             | 30  |      |      | V     |  |
| Zero Gate Voltage Drain Current       | I <sub>DSS</sub>    | V <sub>DS</sub> =30V,V <sub>GS</sub> =0V                         |     |      | 1    | μΑ    |  |
| Gate-Body Leakage Current             | I <sub>GSS</sub>    | $V_{GS}$ = $\pm 20V$ , $V_{DS}$ = $0V$                           |     |      | ±100 | nA    |  |
| Gate Threshold Voltage                | V <sub>GS(th)</sub> | $V_{DS}$ = $V_{GS}$ , $I_D$ =250 $\mu$ A                         | 1.0 | 1.5  | 2.5  | V     |  |
| Olatia Desia Orazoa On Desistenza     | 5                   | V <sub>GS</sub> = 10V, I <sub>D</sub> =20A                       |     | 2.45 | 3.0  | - mΩ  |  |
| Static Drain-Source On-Resistance     | R <sub>DS(ON)</sub> | V <sub>GS</sub> = 4.5V, I <sub>D</sub> =15A                      |     | 2.9  | 4.0  |       |  |
| Diode Forward Voltage                 | V <sub>SD</sub>     | I <sub>S</sub> =20A,V <sub>GS</sub> =0V                          |     | 0.85 | 1.2  | V     |  |
| Maximum Body-Diode Continuous Current | Is                  |  |     |      | 105  | А     |  |
| Dynamic Parameters                    |                     |  |     |      |      |       |  |
| Input Capacitance                     | C <sub>iss</sub>    |  |     | 4401 |      | pF    |  |
| Output Capacitance                    | Coss                | V <sub>DS</sub> =15V,V <sub>GS</sub> =0V,f=1MHZ                  |     | 581  |      |       |  |
| Reverse Transfer Capacitance          | C <sub>rss</sub>    |  |     | 439  |      |       |  |
| Switching Parameters                  |                     |  |     |      |      |       |  |
| Total Gate Charge                     | Qg                  |  |     | 49.5 |      |       |  |
| Gate-Source Charge                    | Q <sub>gs</sub>     | V <sub>GS</sub> =10V,V <sub>DS</sub> =20V,I <sub>D</sub> =20A    |     | 10.4 |      | nC    |  |
| Gate-Drain Charge                     | Q <sub>gd</sub>     |  |     | 8.9  |      |       |  |
| Reverse Recovery Charge               | Q <sub>rr</sub>     |  |     | 7.5  |      |       |  |
| Reverse Recovery Time                 | t <sub>rr</sub>     | - I <sub>F</sub> =20A, di/dt=500A/us                             |     | 23   |      |       |  |
| Turn-on Delay Time                    | t <sub>D(on)</sub>  |  |     | 13   |      |       |  |
| Turn-on Rise Time                     | t <sub>r</sub>      |  |     | 22   |      | ns    |  |
| Turn-off Delay Time                   | t <sub>D(off)</sub> | $V_{GS}$ =10V, $V_{DD}$ =15V, $I_{D}$ =2A, $R_{GEN}$ =3 $\Omega$ |     | 63   |      |       |  |
| Turn-off fall Time                    | t <sub>f</sub>      |  |     | 33   |      | 1     |  |

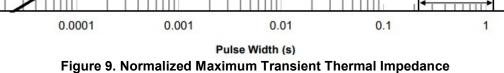
A. Pulse Test: Pulse Width  ${\leqslant}300 \text{us,Duty cycle} {\leqslant}2\%.$ 

B.  $R_{\text{BJA}}$  is the sum of the junction-to-case and case-to-ambient thermal resistance, where the case thermal reference is defined as the solder mounting surface of the drain pins.  $R_{\text{BJC}}$  is guaranteed by design, while  $R_{\text{BJA}}$  is determined by the board design. The maximum rating presented here is based on mounting on a 1 in 2 pad of 2oz copper.

## Typical Performance Characteristics







S-E619 Rev.3.1,12-Oct-20

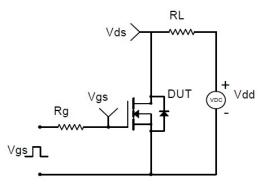
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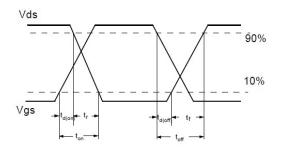
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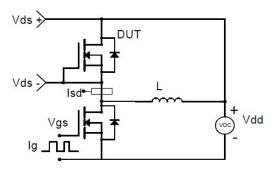
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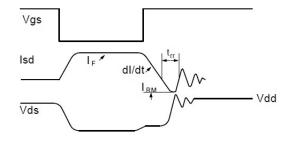
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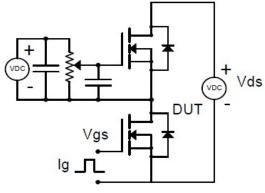


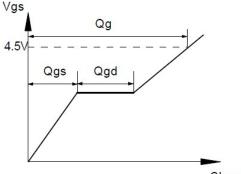
## **Resistive Switching Test Circuit & Waveforms**





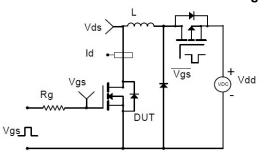
## **Diode Recovery Test Circuit & Waveforms**

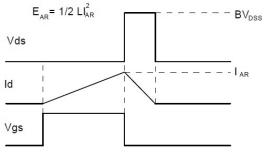




Charge

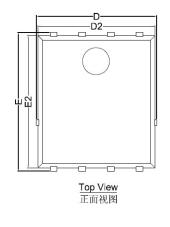
## Gate Charge Test Circuit & Waveform

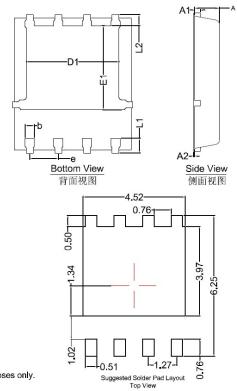




**Unclamped Inductive Switching (UIS) Test Circuit & Waveforms** 

## PDFN5X6 Package information





| SYMBOL   | MILLIMETER |      |      |  |  |
|----------|------------|------|------|--|--|
| STIVIDUL | MIN        | NOM  | MAX  |  |  |
| D        | 5.15       | 5.35 | 5.55 |  |  |
| Е        | 5.95       | 6.15 | 6.35 |  |  |
| А        | 1.00       | 1.10 | 1.20 |  |  |
| A1       | 0.254 BSC  |      |      |  |  |
| A2       |            |      | 0.10 |  |  |
| D1       | 3.92       | 4.12 | 4.32 |  |  |
| E1       | 3,52       | 3.72 | 3.92 |  |  |
| D2       | 5.00       | 5.20 | 5.40 |  |  |
| E2       | 5.66       | 5.86 | 6.06 |  |  |
| L1       | 0.56       | 0.66 | 0.76 |  |  |
| L2       | 0.50 BSC   |      |      |  |  |
| b        | 0.31       | 0.41 | 0.51 |  |  |
| е        | 1.27 BSC   |      |      |  |  |

Note:

1.Controlling dimension:in millimeters.
2.General tolerance:±0.10mm.
3.The pad layout is for reference purposes only.



# YJG105N03A

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