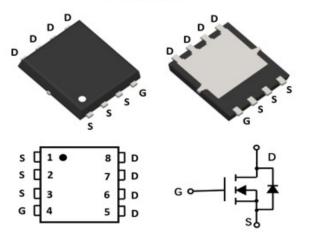
N-Channel Enhancement Mode Field Effect Transistor

PDFN 5X6



Product Summary

- V_{DS}
- ID
- R_{DS(ON)}(at V_{GS}= 10V)
- R_{DS(ON)}(at V_{GS}= 4.5V) • 100% UIS Tested
- 100% UIS Tested
 100% ⊽V_{DS} Tested

General Description

• Trench Power MV MOSFET technology

30V

105A

<3.0mohm

<4.0mohm

- Excellent package for heat dissipation
- High density cell design for low R_{DS(ON)}

Applications

- DC-DC Converters
- Power management functions
- Backlighting

■ Absolute Maximum Ratings (T_A=25°Cunless otherwise noted)

Parameter		Symbol	Limit	Unit	
Drain-source Voltage		V _{DS}	30	V	
Gate-source Voltage		V _{GS}	±20	V	
Drain Current	Tc=25℃	1	105	А	
	Tc=100℃	- I _D	66	A	
Pulsed Drain Current ^A		I _{DM}	415	A	
Total Power Dissipation @ T _c =25°C		P _D	49	W	
Single Pulse Avalanche Energy ^B		E _{AS}	507	mJ	
Thermal Resistance Junction-to-Case		R _{eJC}	2.55	°C/ W	
Junction and Storage Temperature Range		T _J ,T _{STG}	-55~+150	°C	

Ordering Information (Example)

PREFERED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE	
YJG105N03A	F1	YJG105N03A	5000	10000	100000	13" reel	



YJG105N03A

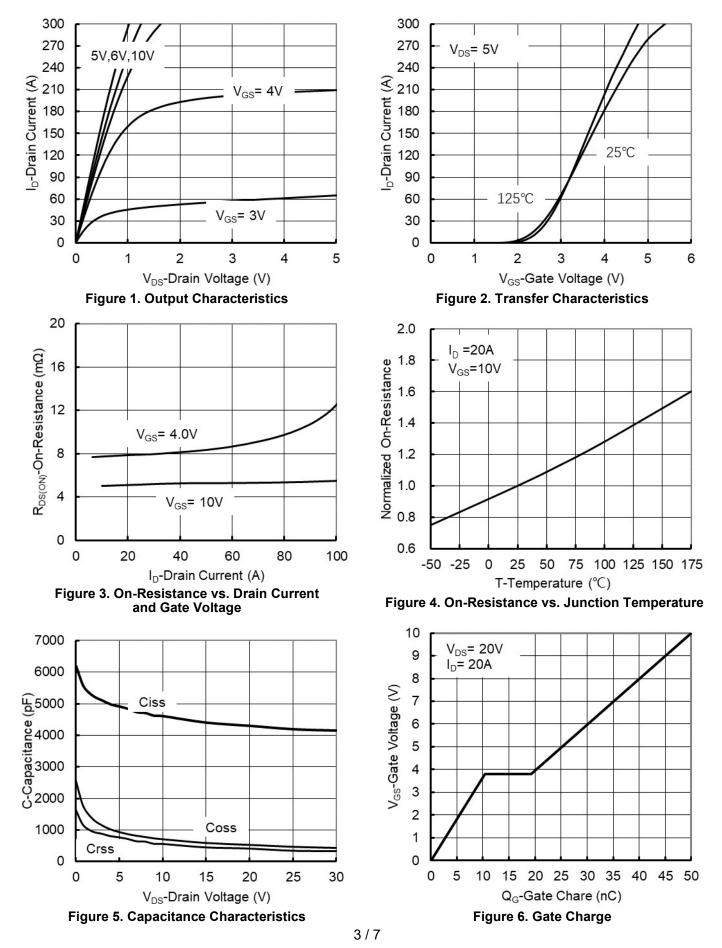
■ Electrical Characteristics (T_J=25°C unless otherwise noted)

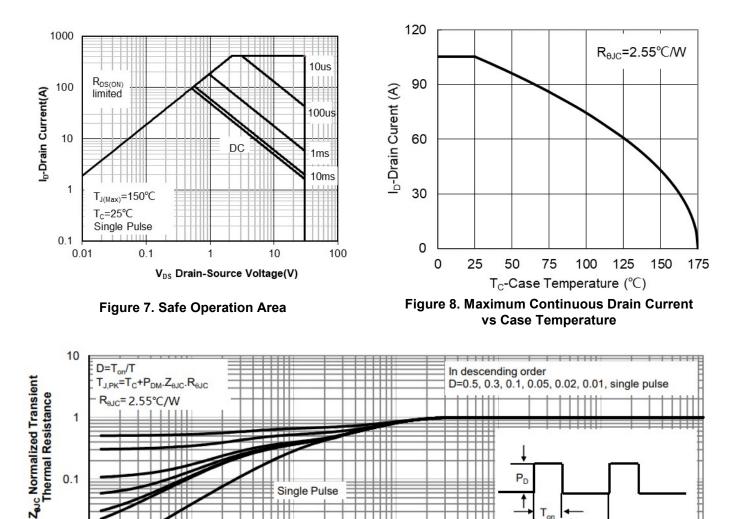
Parameter	Symbol	Conditions	Min	Тур	Мах	Units	
Static Parameter							
Drain-Source Breakdown Voltage	BV _{DSS}	V_{GS} = 0V, I _D =250µA	30			V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V,V _{GS} =0V			1	μΑ	
Gate-Body Leakage Current	I _{GSS}	V_{GS} = $\pm 20V$, V_{DS} = $0V$			±100	nA	
Gate Threshold Voltage	V _{GS(th)}	V_{DS} = V_{GS} , I_D =250 μ A	1.0	1.5	2.5	V	
Olatia Desia Orazoa On Desistenza	5	V _{GS} = 10V, I _D =20A		2.45	3.0	- mΩ	
Static Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} = 4.5V, I _D =15A		2.9	4.0		
Diode Forward Voltage	V _{SD}	I _S =20A,V _{GS} =0V		0.85	1.2	V	
Maximum Body-Diode Continuous Current	Is				105	А	
Dynamic Parameters							
Input Capacitance	C _{iss}			4401		pF	
Output Capacitance	Coss	V _{DS} =15V,V _{GS} =0V,f=1MHZ		581			
Reverse Transfer Capacitance	C _{rss}			439			
Switching Parameters							
Total Gate Charge	Qg			49.5			
Gate-Source Charge	Q _{gs}	V _{GS} =10V,V _{DS} =20V,I _D =20A		10.4		nC	
Gate-Drain Charge	Q _{gd}			8.9			
Reverse Recovery Charge	Q _{rr}			7.5			
Reverse Recovery Time	t _{rr}	- I _F =20A, di/dt=500A/us		23			
Turn-on Delay Time	t _{D(on)}			13			
Turn-on Rise Time	t _r			22		ns	
Turn-off Delay Time	t _{D(off)}	V_{GS} =10V, V_{DD} =15V, I_{D} =2A, R_{GEN} =3 Ω		63			
Turn-off fall Time	t _f			33		1	

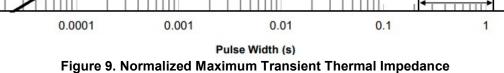
A. Pulse Test: Pulse Width ${\leqslant}300 \text{us,Duty cycle} {\leqslant}2\%.$

B. R_{BJA} is the sum of the junction-to-case and case-to-ambient thermal resistance, where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{BJC} is guaranteed by design, while R_{BJA} is determined by the board design. The maximum rating presented here is based on mounting on a 1 in 2 pad of 2oz copper.

Typical Performance Characteristics







S-E619 Rev.3.1,12-Oct-20

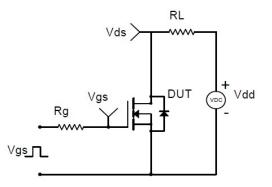
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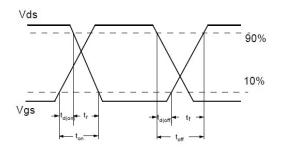
1E-05

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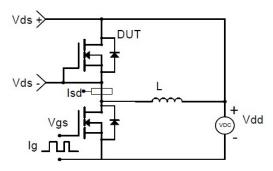
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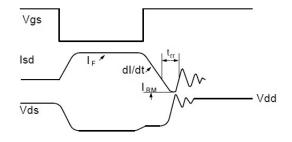
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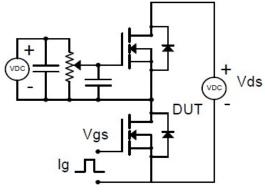


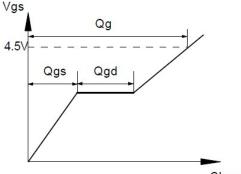
Resistive Switching Test Circuit & Waveforms





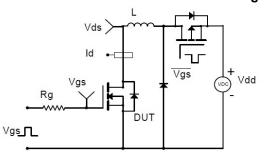
Diode Recovery Test Circuit & Waveforms

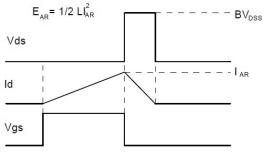




Charge

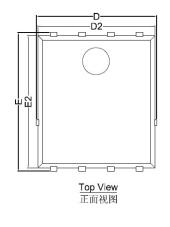
Gate Charge Test Circuit & Waveform

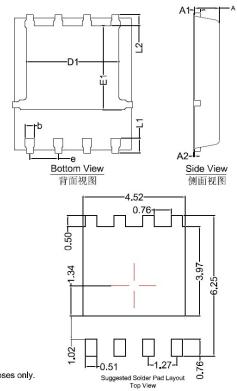




Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

PDFN5X6 Package information





SYMBOL	MILLIMETER				
STIVIDUL	MIN	NOM	MAX		
D	5.15	5.35	5.55		
Е	5.95	6.15	6.35		
А	1.00	1.10	1.20		
A1	0.254 BSC				
A2			0.10		
D1	3.92	4.12	4.32		
E1	3,52	3.72	3.92		
D2	5.00	5.20	5.40		
E2	5.66	5.86	6.06		
L1	0.56	0.66	0.76		
L2	0.50 BSC				
b	0.31	0.41	0.51		
е	1.27 BSC				

Note:

1.Controlling dimension:in millimeters.
2.General tolerance:±0.10mm.
3.The pad layout is for reference purposes only.



YJG105N03A

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