

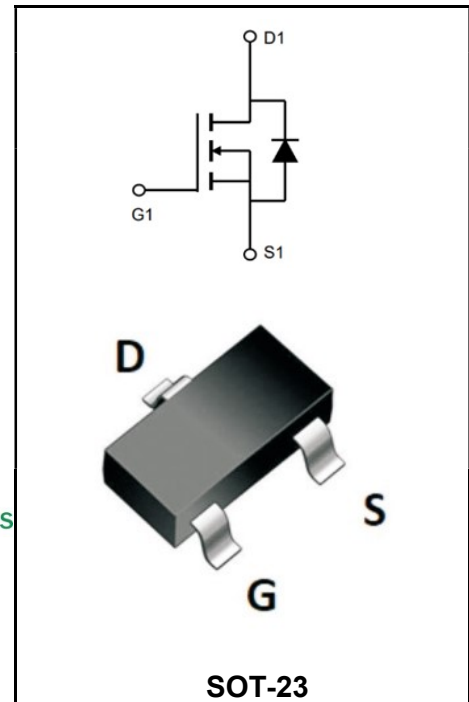
**60V N-CHANNEL ENHANCEMENT MODE MOSFET**

**MAIN CHARACTERISTICS**

$I_D$	3A
$V_{DSS}$	60V
$R_{DS(ON)-typ}(@V_{GS}=10V)$	<100mΩ <b>(Type:80mΩ)</b>

**Features**

- ◆ Adopt advanced trench technology to provide excellent RDS(ON), low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.
- ◆ Battery protection
- ◆ Load switch
- ◆ Uninterruptible power supply



**Mechanical Data**

- ◆ Case: Molded plastic
- ◆ Mounting Position: Any
- ◆ Molded Plastic: UL Flammability Classification Rating 94V-0
- ◆ Solder bath temperature 275°C maximum, 10s per JESD22-106

**Product Specification Classification**

Part Number	Part Number	Marking	Pack
YFW3N06	SOT-23	6003	3000PCS/Tape

**Maximum Ratings at Tc=25°C unless otherwise specified**

Characteristics	Symbols	Value	Units
Drain-Source Voltage	<b>VDS</b>	60	<b>V</b>
Gate-Source Voltage	<b>VGS</b>	±20	<b>V</b>
Continue Drain Current @TA=25°C	<b>ID</b>	3.0	<b>A</b>
@TA=70°C		1.8	
Pulsed Drain Current (Note1)	<b>IDM</b>	9.2	<b>A</b>
Power Dissipation(TA = 25°C)	<b>PD</b>	1	<b>W</b>
Operating Temperature Range	<b>TJ</b>	-50 to +150	<b>°C</b>
Storage Temperature Range	<b>TSTG</b>	-50 to +150	<b>°C</b>
Thermal Resistance, Junction to Case	<b>RθJC</b>	80	<b>°C/W</b>
Thermal Resistance, Junction to Ambient	<b>RθJA</b>	125	<b>°C/W</b>

**Electrical Characteristics at Tc=25°C unless otherwise specified**

Characteristics	Test Condition	Symbol	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	<b>BV<sub>DSS</sub></b>	60	-	-	<b>V</b>
Temperature Coefficient	Reference to 25°C , $I_D = 1\text{mA}$	<b>BV<sub>DSS</sub></b>	-	0.054	-	<b>V/°C</b>
Drain-Source On-State Resistance	$V_{GS} = 10\text{ V}, I_D = 2\text{ A}$	<b>R<sub>DS(on)</sub></b>	-	80	100	<b>mΩ</b>
	$V_{GS} = 4.5\text{ V}, I_D = 1\text{ A}$		-	85	110	
Gate-Source Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	<b>V<sub>GS(th)</sub></b>	1.2	-	2.5	<b>V</b>
Temperature Coefficient			-	-4.96	-	<b>mV/°C</b>
Drain-Source Leakage Current $T_J = 25^\circ\text{C}$	$V_{DS} = 48\text{ V}, V_{GS} = 0\text{ V}$	<b>I<sub>DSS</sub></b>	-	-	1	<b>uA</b>
	$T_J = 55^\circ\text{C}$ $V_{DS} = 48\text{ V}, V_{GS} = 0\text{ V}$		-	-	5	
Gate Leakage Current	$V_{GS} = \pm 20\text{ V}, V_{DS} = 0\text{V}$	<b>I<sub>GSS</sub></b>	-	-	±100	<b>nA</b>
Forward Transconductance	$V_{DS} = 5\text{V}, I_D = 2\text{A}$	<b>g<sub>fs</sub></b>	-	13	-	<b>S</b>
Total Gate Charge(4.5V)	$I_D = 2\text{ A}, V_{DS} = 48\text{ V}, V_{GS} = 4.5\text{ V}$	<b>Q<sub>G</sub></b>	-	5	7.0	<b>nC</b>
Gate to Source Charge		<b>Q<sub>GS</sub></b>	-	1.68	2.4	
Gate to Drain Charge		<b>Q<sub>GD</sub></b>	-	1.9	2.7	
Turn-on Delay Time(Note2)	$I_D = 2\text{ A}, V_{DD} = 30\text{ V}, R_G = 3.3, V_{GS} = 10\text{V}$	<b>t<sub>d(ON)</sub></b>	-	1.6	3.2	<b>nS</b>
Rise Time(Note2)		<b>tr</b>	-	7.2	13	
Turn-Off Delay Time(Note2)		<b>t<sub>d(OFF)</sub></b>	-	25	50	
Fall Time(Note2)		<b>t<sub>f</sub></b>	-	14.4	28.8	
Input Capacitance	$V_{GS} = 0\text{ V}, V_{DS} = 15\text{ V}, f = 1\text{MHz}$	<b>C<sub>iss</sub></b>	-	511	715	<b>pF</b>
Output Capacitance		<b>C<sub>OSS</sub></b>	-	38	53	
Reverse Transfer Capacitance		<b>C<sub>rss</sub></b>	-	25	35	

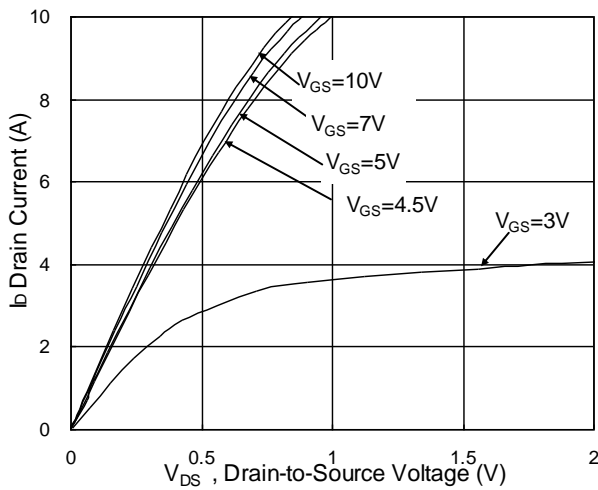
**Source-Drain Diode Characteristics at Ta=25°C unless otherwise specified**

Characteristics	Test Condition	Symbol	Min.	Typ.	Max.	Unit
Maximun Body-Diode Continuous Current	$V_G=V_D=0V$ , Force Curren	$I_S$	-	-	2.3	<b>A</b>
Maximun Body-Diode Pulsed Current		$I_{SM}$	-	-	9.2	<b>A</b>
Drain-Source Diode Forward Voltage	$V_{GS} = 0V, I_{SD} = 1A, T_J = 25^\circ C$	$V_{SD}$	-	-	1.2	<b>V</b>
Reverse Recovery Time	$I_F = 2A, diF/dt = 100A/\mu s, T_J = 25^\circ C$	$t_{rr}$	-	9.7	-	<b>nS</b>
Reverse Recovery Charge		$Q_{rr}$	-	5.8	-	<b>nC</b>

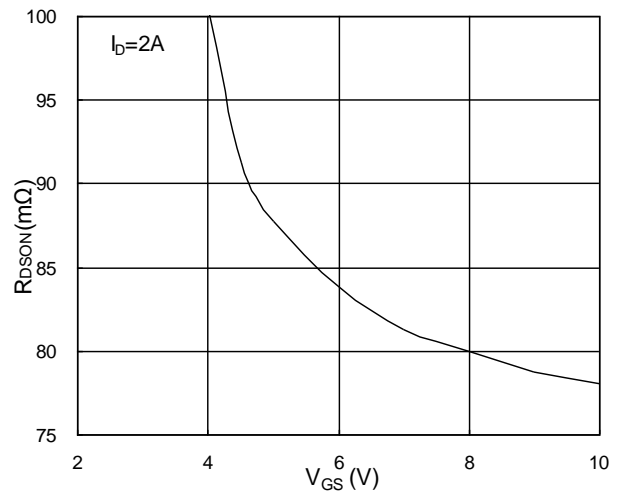
Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\cong 300\mu s$  , duty cycle  $\cong 2\%$
- 3.The power dissipation is limited by 150°C junction temperature.
- 4.The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

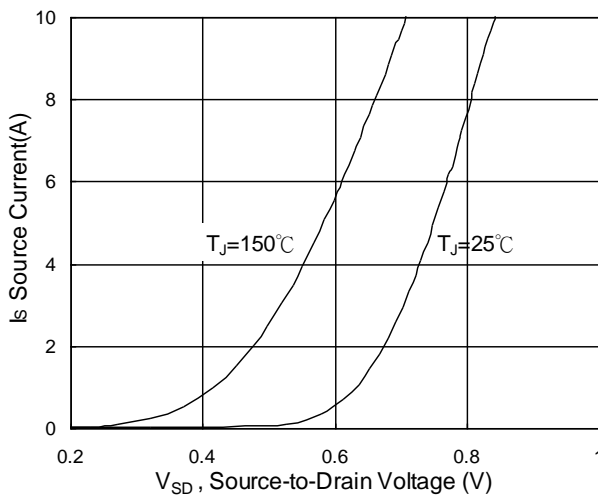
**Ratings and Characteristic Curves**



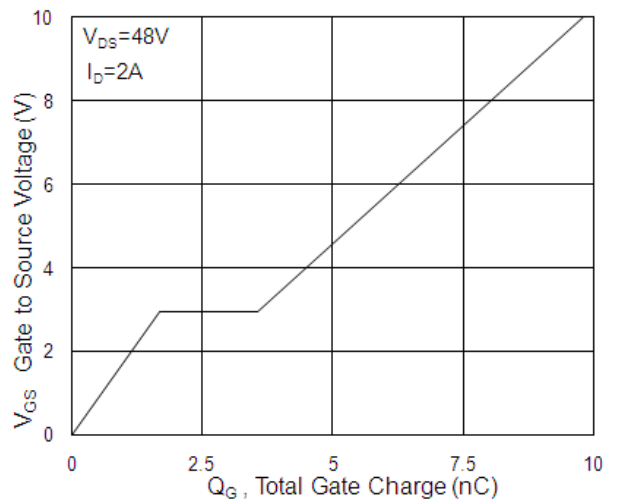
**Fig.1 Typical Output Characteristics**



**Fig.2 On-Resistance v.s Gate-Source**

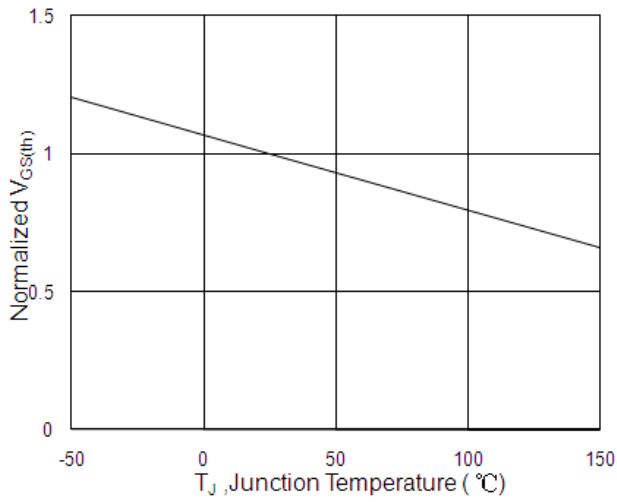


**Fig.3 Forward Characteristics of Reverse**

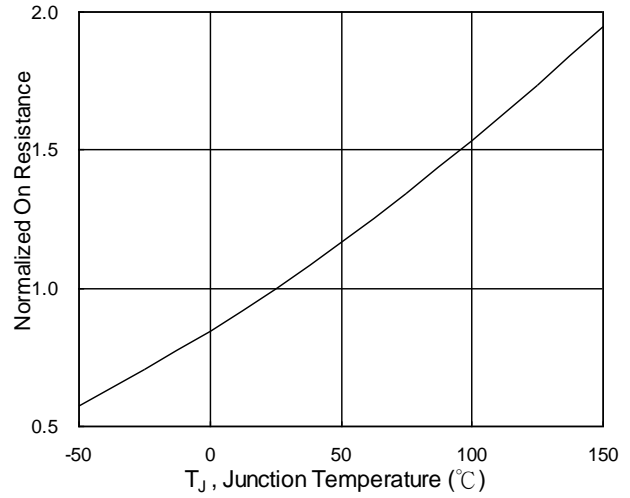


**Fig.4 Gate-Charge Characteristics**

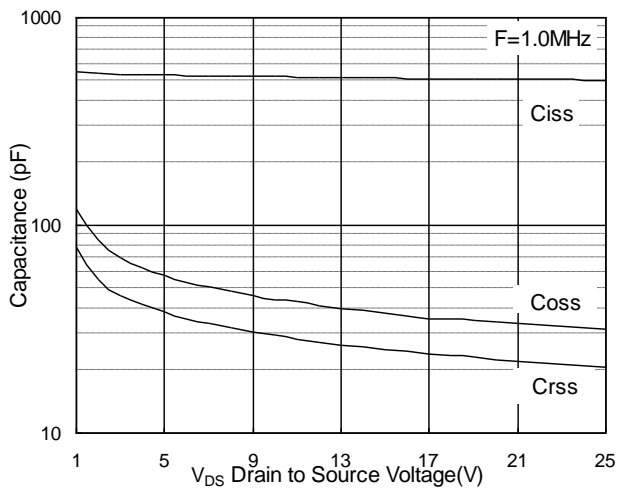
**Ratings and Characteristic Curves**



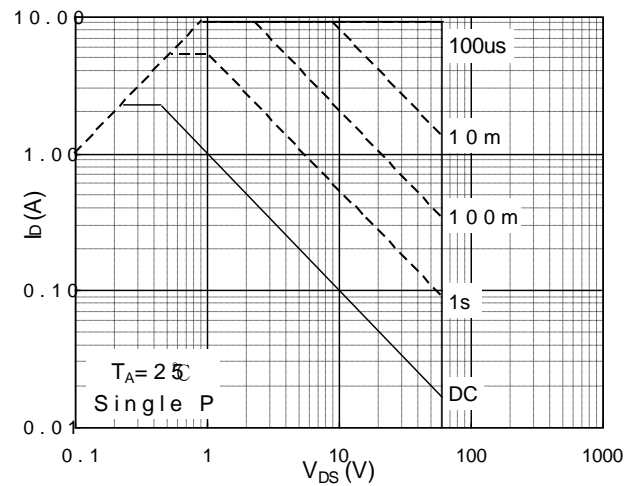
**Fig.5 Normalized  $V_{GS(th)}$  v.s  $T_J$**



**Fig.6 Normalized  $R_{DS(on)}$  v.s  $T_J$**



**Fig.7 Capacitance**



**Fig.8 Safe Operating Area**

Ratings and Characteristic Curves

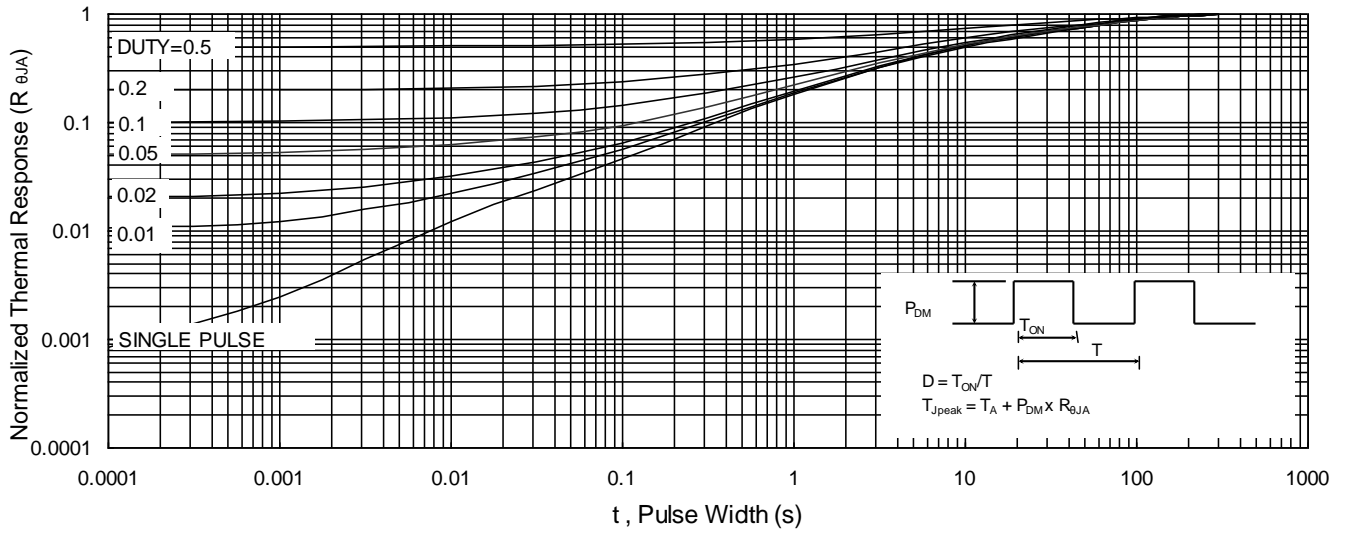


Fig.9 Normalized Maximum Transient Thermal Impedance

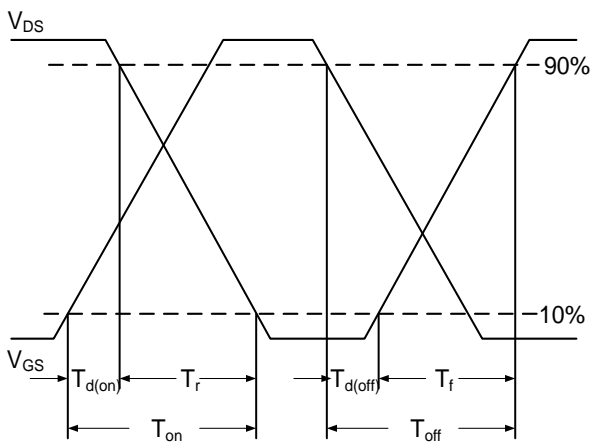


Fig.10 Switching Time Waveform

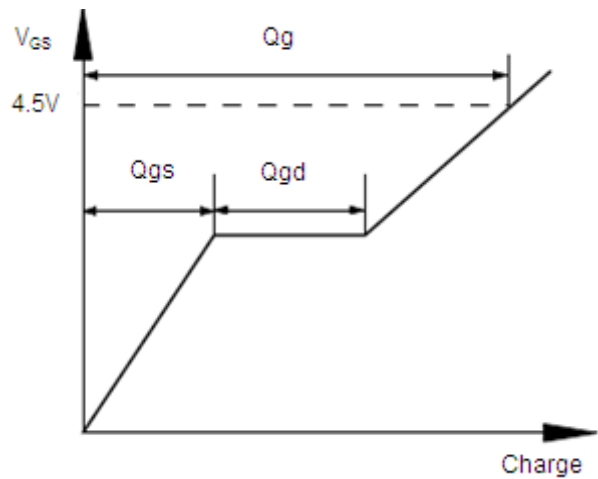
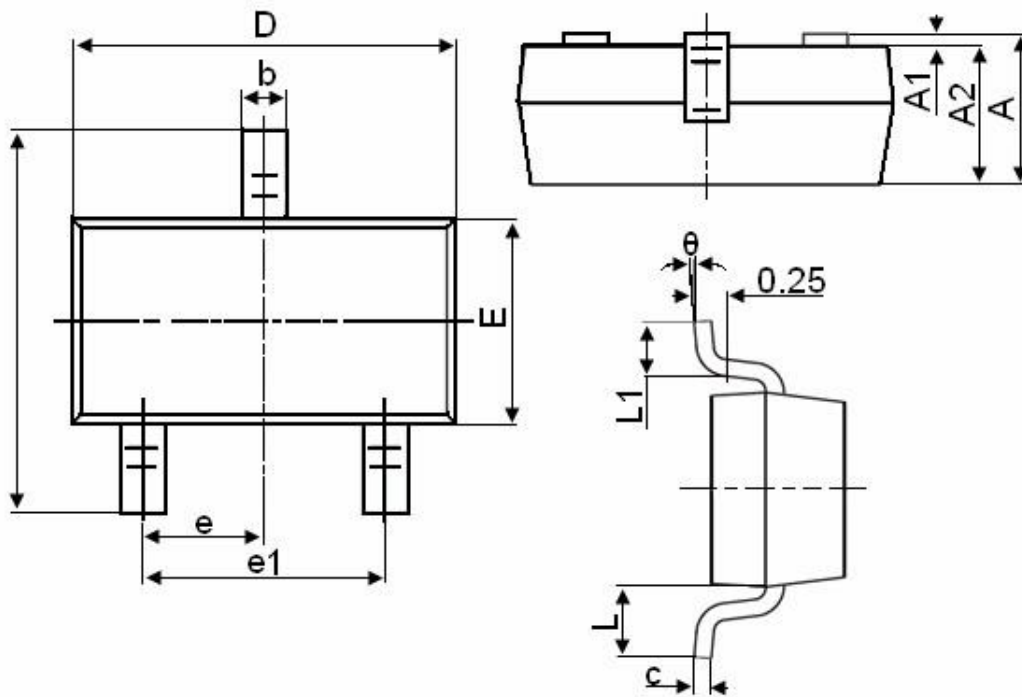


Fig.11 Gate Charge Waveform



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
θ	0°	8°

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