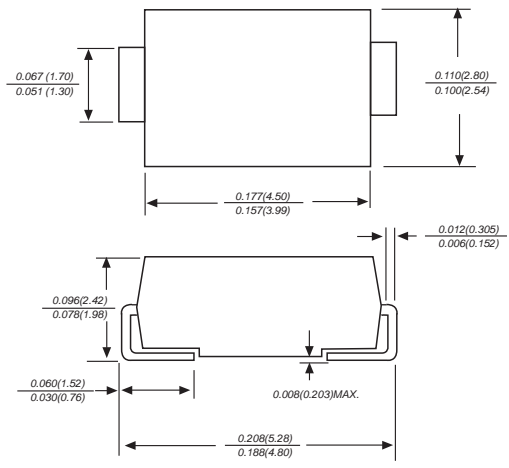
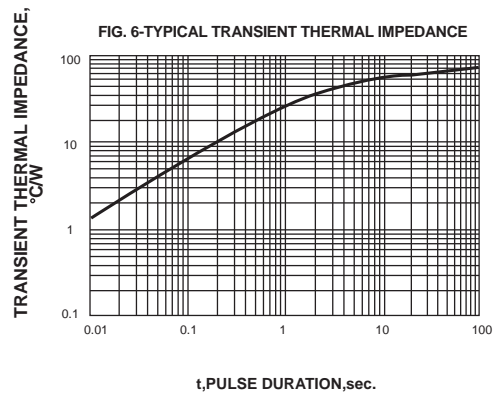
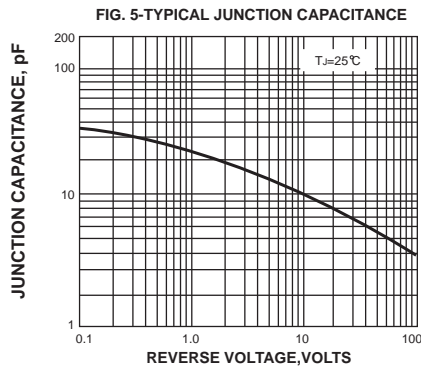
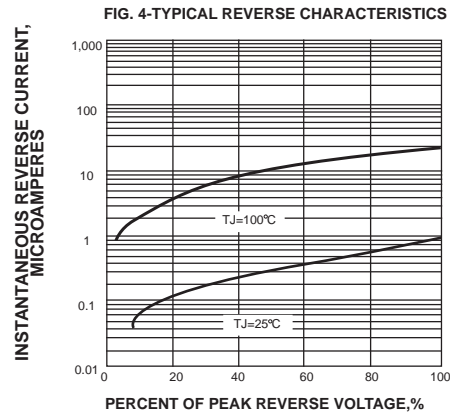
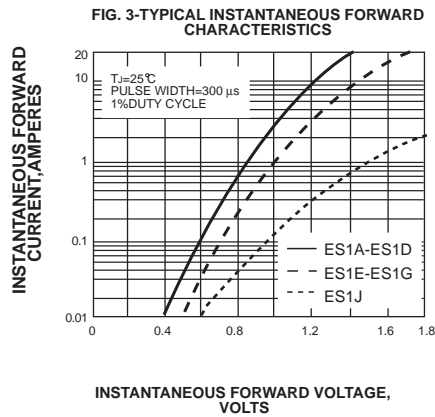
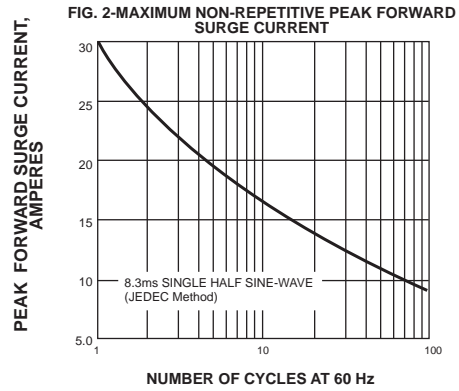
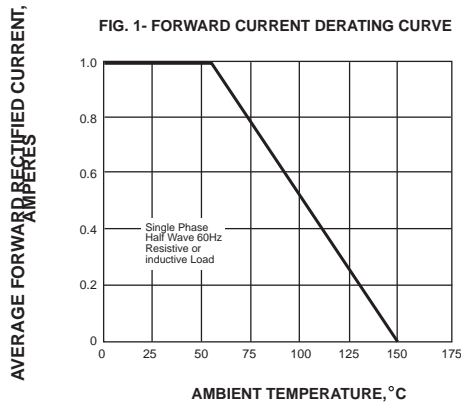


ES1A THRU ES1J

<p>DO-214AC/SMA</p>  <p><i>Dimensions in inches and (millimeters)</i></p>	<p style="text-align: center;">FEATURES</p> <ul style="list-style-type: none"> ◆ The plastic package carries Underwriters Laboratory Flammability Classification 94V-0 ◆ For surface mounted applications ◆ Low reverse leakage ◆ Built-in strain relief, ideal for automated placement ◆ High forward surge current capability ◆ High temperature soldering guaranteed: 250°C/10 seconds at terminals ◆ Glass passivated chip junction 																	
<p style="text-align: center;">MECHANICAL DATA</p> <p>Case: JEDEC DO-214AC molded plastic body over passivated chip Terminals: Solder plated, solderable per MIL-STD-750, Method 2026 Polarity: Color band denotes cathode end Mounting Position: Any Weight: 0.002 ounce, 0.07 grams</p>																		
<p>MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS</p>																		
<p>Ratings at 25°C ambient temperature unless otherwise specified. Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.</p>																		
<p style="text-align: center;">Catalog Number</p>	<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: left;">SYMBOLS</th> <th>ES1A</th> <th>ES1B</th> <th>ES1C</th> <th>ES1D</th> <th>ES1E</th> <th>ES1G</th> <th>ES1J</th> <th>UNITS</th> </tr> </thead> </table>	SYMBOLS	ES1A	ES1B	ES1C	ES1D	ES1E	ES1G	ES1J	UNITS								
SYMBOLS	ES1A	ES1B	ES1C	ES1D	ES1E	ES1G	ES1J	UNITS										
<p>Maximum repetitive peak reverse voltage</p>	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="text-align: left;">V_{RRM}</td> <td>50</td> <td>100</td> <td>150</td> <td>200</td> <td>300</td> <td>400</td> <td>600</td> <td>VOLTS</td> </tr> </table>	V_{RRM}	50	100	150	200	300	400	600	VOLTS								
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<p>Maximum DC blocking voltage</p>	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="text-align: left;">V_{DC}</td> <td>50</td> <td>100</td> <td>150</td> <td>200</td> <td>300</td> <td>400</td> <td>600</td> <td>VOLTS</td> </tr> </table>	V_{DC}	50	100	150	200	300	400	600	VOLTS								
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<p>Maximum average forward rectified current at $T_L=55^\circ\text{C}$</p>	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="text-align: left;">$I_{(AV)}$</td> <td colspan="7" style="text-align: center;">1.0</td> <td>Amp</td> </tr> </table>	$I_{(AV)}$	1.0							Amp								
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<p>Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)</p>	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="text-align: left;">I_{FSM}</td> <td colspan="7" style="text-align: center;">30.0</td> <td>Amps</td> </tr> </table>	I_{FSM}	30.0							Amps								
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<p>Maximum instantaneous forward voltage at 1.0A</p>	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="text-align: left;">V_F</td> <td colspan="3" style="text-align: center;">0.95</td> <td colspan="2" style="text-align: center;">1.25</td> <td colspan="2" style="text-align: center;">1.7</td> <td>Volts</td> </tr> </table>	V_F	0.95			1.25		1.7		Volts								
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<p>Maximum DC reverse current at rated DC blocking voltage $T_A=25^\circ\text{C}$ $T_A=100^\circ\text{C}$</p>	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="text-align: left;">I_R</td> <td colspan="7" style="text-align: center;">5.0</td> <td rowspan="2" style="text-align: center;">μA</td> </tr> <tr> <td></td> <td colspan="7" style="text-align: center;">50.0</td> </tr> </table>	I_R	5.0							μA		50.0						
I_R	5.0							μA										
	50.0																	
<p>Maximum reverse recovery time (NOTE 1)</p>	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="text-align: left;">t_{rr}</td> <td colspan="7" style="text-align: center;">35</td> <td>ns</td> </tr> </table>	t_{rr}	35							ns								
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<p>Typical junction capacitance (NOTE 2)</p>	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="text-align: left;">C_J</td> <td colspan="7" style="text-align: center;">15.0</td> <td>pF</td> </tr> </table>	C_J	15.0							pF								
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<p>Typical thermal resistance (NOTE 3)</p>	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="text-align: left;">$R_{\theta JA}$</td> <td colspan="7" style="text-align: center;">60.0</td> <td>°C/W</td> </tr> </table>	$R_{\theta JA}$	60.0							°C/W								
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<p>Operating junction and storage temperature range</p>	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="text-align: left;">T_J, T_{STG}</td> <td colspan="7" style="text-align: center;">-50 to +150</td> <td>°C</td> </tr> </table>	T_J, T_{STG}	-50 to +150							°C								
T_J, T_{STG}	-50 to +150							°C										
<p>Note: 1. Reverse recovery condition $I_F=0.5A, I_R=1.0A, I_{rr}=0.25A$ 2. Measured at 1MHz and applied reverse voltage of 4.0V D.C. 3. P.C.B. mounted with 0.2x0.2" (5.0x5.0mm) copper pad areas</p>																		

ES1A THRU ES1J



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