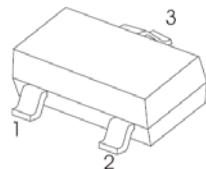


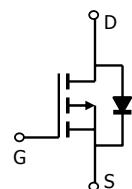
### ■ Features

- $V_{DS} (V) = -30V$
- $I_D = -4.2 A (V_{GS} = -10V)$
- $R_{DS(ON)} < 50m\Omega (V_{GS} = -10V)$
- $R_{DS(ON)} < 65m\Omega (V_{GS} = -4.5V)$
- $R_{DS(ON)} < 120m\Omega (V_{GS} = -2.5V)$

SOT - 23



1. GATE
2. SOURCE
3. DRAIN



### ■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	
Continuous Drain Current $T_a = 25^\circ C$ $T_a = 70^\circ C$	$I_D$	-4.2	A
		-3.5	
Pulsed Drain Current	$I_{DM}$	-30	
Power Dissipation $T_a = 25^\circ C$ $T_a = 70^\circ C$	$P_D$	1.4	W
		1	
Thermal Resistance.Junction- to-Ambient $t \leq 10s$	$R_{thJA}$	90	$^\circ C/W$
		125	
Thermal Resistance.Junction- to-Case	$R_{thJC}$	60	
Junction Temperature	$T_J$	150	$^\circ C$
Junction and Storage Temperature Range	$T_{stg}$	-55 to 150	

## P-Channel Enhancement MOSFET

■ Electrical Characteristics  $T_a = 25^\circ\text{C}$ 

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$V_{DSS}$	$I_D=-250 \mu\text{A}, V_{GS}=0\text{V}$	-30			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-24\text{V}, V_{GS}=0\text{V}$		-1		$\mu\text{A}$
		$V_{DS}=-24\text{V}, V_{GS}=0\text{V}, T_J=55^\circ\text{C}$		-5		
Gate-Body leakage current	$I_{GSS}$	$V_{DS}=0\text{V}, V_{GS}=\pm 12\text{V}$			$\pm 100$	nA
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=-250 \mu\text{A}$	-0.4		-1.3	V
Static Drain-Source On-Resistance	$R_{DS(\text{on})}$	$V_{GS}=-10\text{V}, I_D=-4.2\text{A}$			50	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}, I_D=-4\text{A}$			65	
		$V_{GS}=-2.5\text{V}, I_D=-1\text{A}$			120	
On state drain current	$I_D(\text{ON})$	$V_{GS}=-4.5\text{V}, V_{DS}=-5\text{V}$	-25			A
Forward Transconductance	$g_{FS}$	$V_{DS}=-5\text{V}, I_D=-5\text{A}$	7	11		S
Input Capacitance	$C_{iss}$	$V_{GS}=0\text{V}, V_{DS}=-15\text{V}, f=1\text{MHz}$		954		$\text{pF}$
Output Capacitance	$C_{oss}$			115		
Reverse Transfer Capacitance	$C_{rss}$			77		
Gate resistance	$R_g$	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$		6		$\Omega$
Total Gate Charge	$Q_g$	$V_{GS}=-4.5\text{V}, V_{DS}=-15\text{V}, I_D=-4\text{A}$		9.4		$\text{nC}$
Gate Source Charge	$Q_{gs}$			2		
Gate Drain Charge	$Q_{gd}$			3		
Turn-On DelayTime	$t_{d(\text{on})}$	$V_{GS}=-10\text{V}, V_{DS}=-15\text{V}, R_L=3.6 \Omega, R_{GEN}=6 \Omega$		6.3		$\text{ns}$
Turn-On Rise Time	$t_r$			3.2		
Turn-Off DelayTime	$t_{d(\text{off})}$			38.3		
Turn-Off Fall Time	$t_f$			12		
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F=-4\text{A}, dI/dt=100\text{A}/\mu\text{s}$		20.2		
Body Diode Reverse Recovery Charge	$Q_{rr}$	$I_F=5\text{A}, dI/dt=100\text{A}/\mu\text{s}$		11.2		$\text{nC}$
Maximum Body-Diode Continuous Current	$I_S$				-2.2	A
Diode Forward Voltage	$V_{SD}$	$I_S=-1\text{A}, V_{GS}=0\text{V}$		-0.75	-1	V

## P-Channel Enhancement MOSFET

## ■ Typical Characteristics

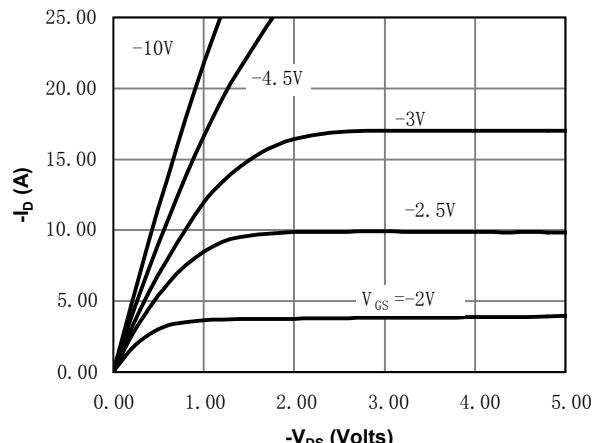


Fig 1: On-Region Characteristics

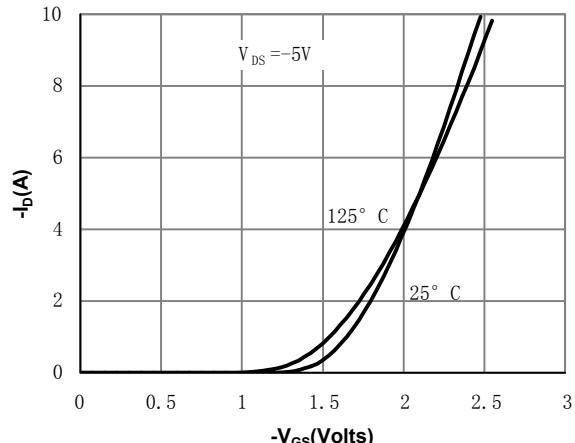


Figure 2: Transfer Characteristics

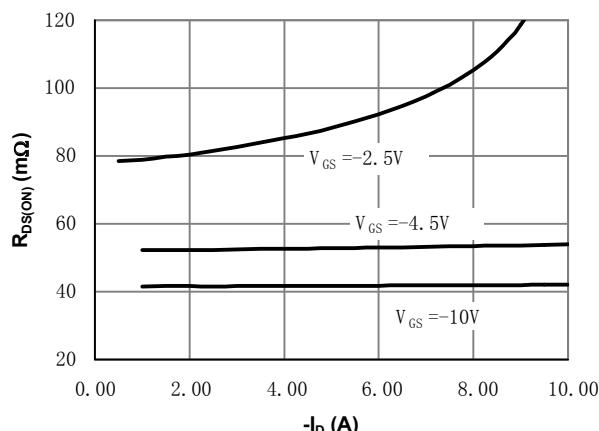


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

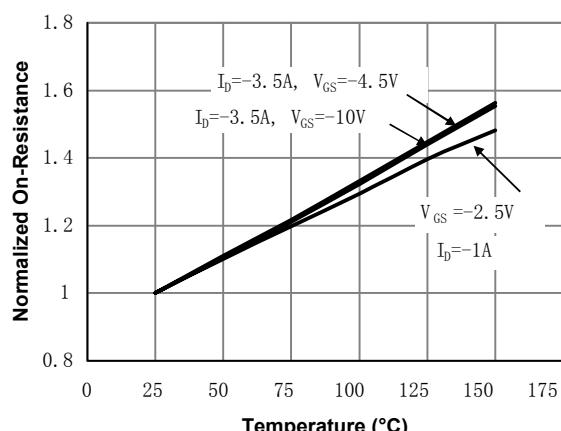


Figure 4: On-Resistance vs. Junction Temperature

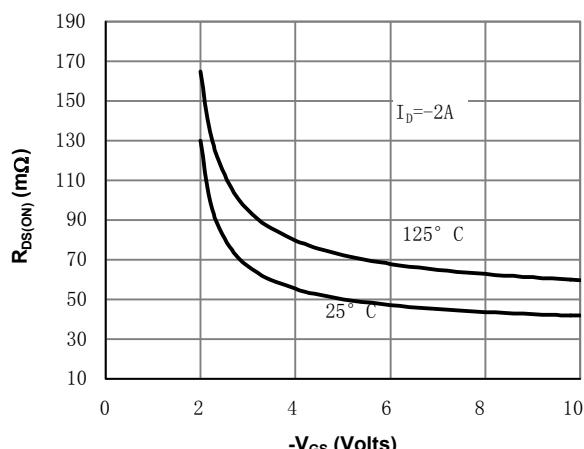


Figure 5: On-Resistance vs. Gate-Source Voltage

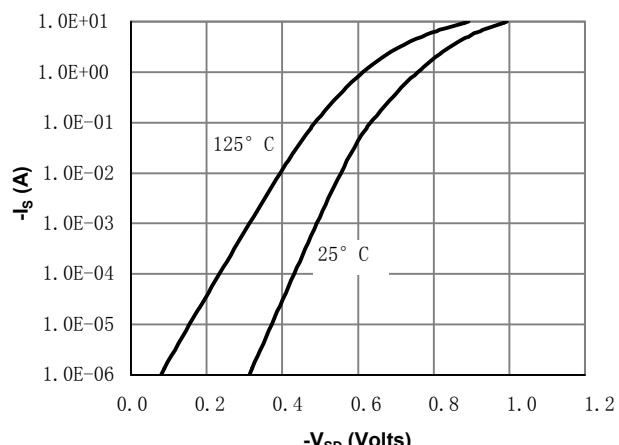
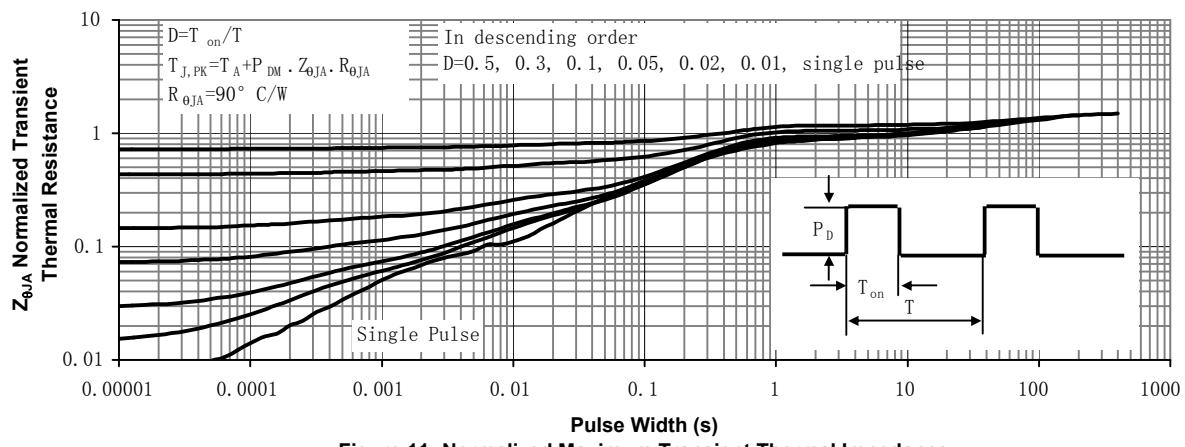
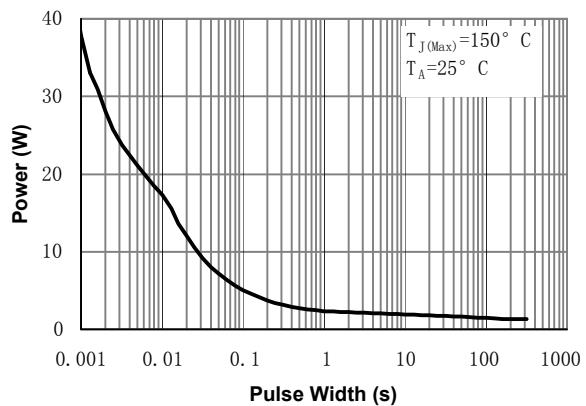
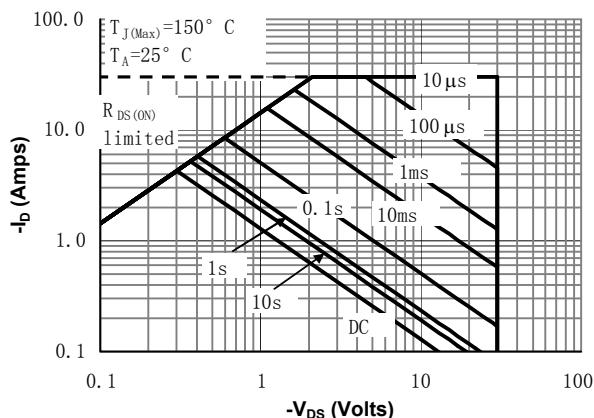
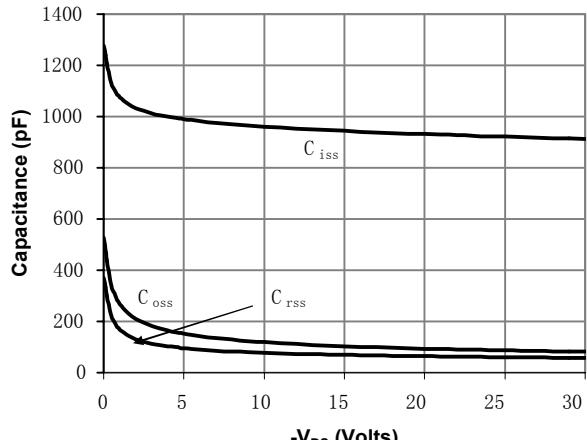
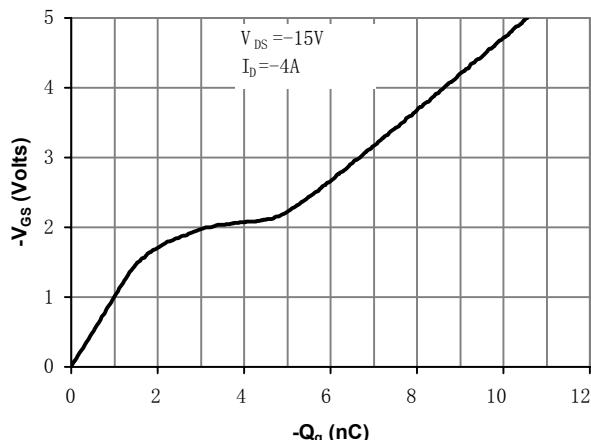
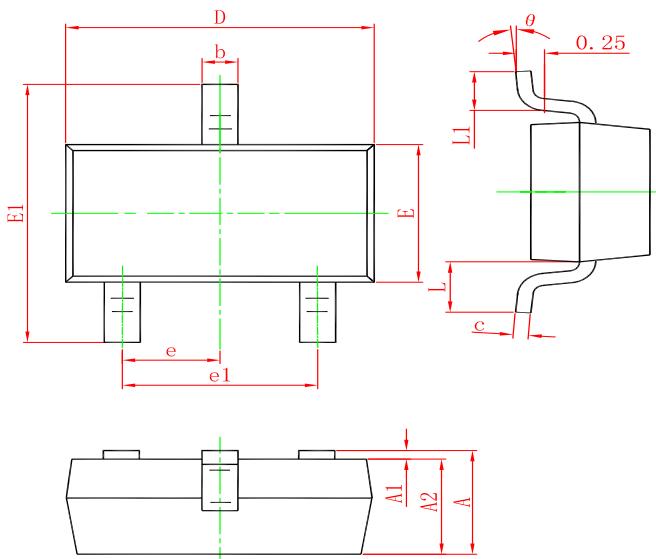


Figure 6: Body-Diode Characteristics

■ Typical Characteristics

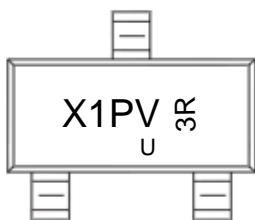


## SOT-23 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

## Marking



## Ordering information

Order code	Package	Baseqty	Deliverymode
UMW AO3401A	SOT-23	3000	Tape and reel

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