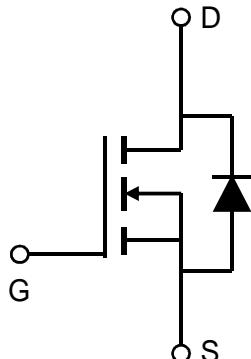


## General Description

The AO4466 uses advanced trench technology to provide excellent  $R_{DS(ON)}$  and low gate charge. This device is suitable for use as a load switch or in PWM applications. The source leads are separated to allow a Kelvin connection to the source, which may be used to bypass the source inductance.



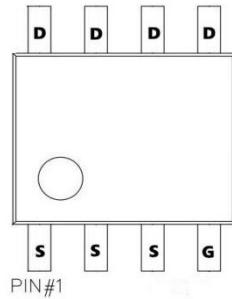
## Product Summary

$V_{DS}$  (V) = 30V

$I_D$  = 10A

$R_{DS(ON)} < 18m\Omega$  ( $V_{GS} = 10V$ )

$R_{DS(ON)} < 25m\Omega$  ( $V_{GS} = 4.5V$ )



## Absolute Maximum Ratings $T_A=25^\circ C$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>AF</sup> $T_A=25^\circ C$	$I_D$	10	A
$T_A=70^\circ C$	$I_D$	7	
Pulsed Drain Current <sup>B</sup>	$I_{DM}$	64	
Power Dissipation $T_A=25^\circ C$	$P_D$	3.1	W
$T_A=70^\circ C$	$P_D$	2	
Avalanche Current <sup>B, G</sup>	$I_{AR}$	12	A
Repetitive avalanche energy 0.1mH <sup>B, G</sup>	$E_{AR}$	7	mJ
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	°C

## Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient <sup>A</sup>	$R_{\theta JA}$	36	40	°C/W
Steady-State		62	75	°C/W
Maximum Junction-to-Lead <sup>C</sup>	$R_{\theta JL}$	18	24	°C/W

Electrical Characteristics ( $T_J=25^\circ C$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$BV_{DSS}$	Drain-Source Breakdown Voltage	$I_D=250\mu A, V_{GS}=0V$	30			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=30 V_{GS}=0V$ $T_J=55^\circ C$			1 5	$\mu A$
$I_{GSS}$	Gate-Body leakage current	$V_{DS}=0V, V_{GS}= \pm 20V$			100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1	1.5	2.5	V
$I_{D(ON)}$	On state drain current	$V_{GS}=4.5V, V_{DS}=5V$	64			A
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=10A$		13	18	$m\Omega$
		$V_{GS}=4.5V, I_D=5A$		19	25	$m\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS}=5V, I_D =3A$		17		S
$V_{SD}$	Diode Forward Voltage	$I_S=1A, V_{GS}=0V$		0.75	1	V
$I_S$	Maximum Body-Diode Continuous Current				2.4	A
$C_{iss}$	Input Capacitance	$V_{GS}=0V, V_{DS}=15V, f=1MHz$	298	373	448	pF
$C_{oss}$	Output Capacitance		46	67	88	pF
$C_{rss}$	Reverse Transfer Capacitance		24	41	58	pF
$R_g$	Gate resistance	$V_{GS}=0V, V_{DS}=0V, f=1MHz$	0.6	1.8	2.8	$\Omega$
$Q_g(10V)$	Total Gate Charge	$V_{GS}=10V, V_{DS}=15V, I_D=10A$	5.7	7.1	8.6	nC
$Q_g(4.5V)$	Total Gate Charge		2.7	3.5	4.2	nC
$Q_{gs}$	Gate Source Charge			1.2		nC
$Q_{gd}$	Gate Drain Charge			1.6		nC
$t_{D(on)}$	Turn-On DelayTime	$V_{GS}=10V, V_{DS}=15V, R_L=1.5\Omega, R_{GEN}=3\Omega$		4.3		ns
$t_r$	Turn-On Rise Time			2.8		ns
$t_{D(off)}$	Turn-Off DelayTime			15.8		ns
$t_f$	Turn-Off Fall Time			3		ns
$t_{rr}$	Body Diode Reverse Recovery Time	$I_F=10A, dI/dt=100A/\mu s$	8.4	10.5	12.6	ns
$Q_{rr}$	Body Diode Reverse Recovery Charge	$I_F=10A, dI/dt=100A/\mu s$	3.6	4.5	5.4	nC
$t_{rr}$	Body Diode Reverse Recovery Time	$I_F=10A, dI/dt=500A/\mu s$	4.7	6.0	7.2	ns
$Q_{rr}$	Body Diode Reverse Recovery Charge	$I_F=10A, dI/dt=500A/\mu s$	5.3	6.6	8	nC

A: The value of  $R_{GJA}$  is measured with the device mounted on 1in 2 FR-4 board with 2oz. Copper, in a still air environment with  $T_A=25^\circ C$ . The value in any given application depends on the user's specific board design.

B: Repetitive rating, pulse width limited by junction temperature.

C. The  $R_{GJA}$  is the sum of the thermal impedance from junction to lead  $R_{GJL}$  and lead to ambient.

D. The static characteristics in Figures 1 to 6 are obtained using <300  $\mu s$  pulses, duty cycle 0.5% max.

E. These tests are performed with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with  $T_A=25^\circ C$ . The SOA curve provides a single pulse rating.

F. The current rating is based on the  $t \leq 10s$  junction to ambient thermal resistance rating.

G:  $L=100\mu H, V_{DD}=0V, R_G=0\Omega$ , rated  $V_{DS}=30V$  and  $V_{GS}=10V$

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

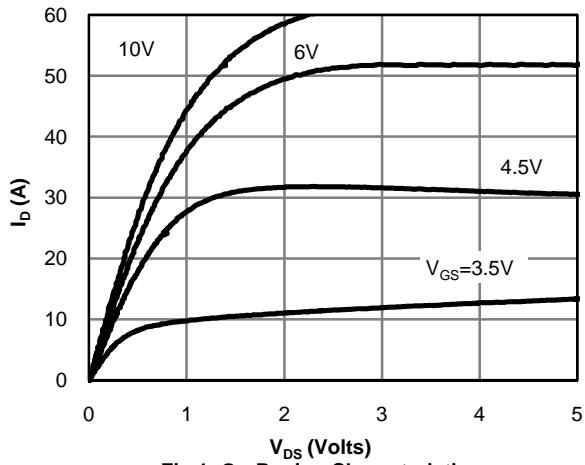


Fig 1: On-Region Characteristics

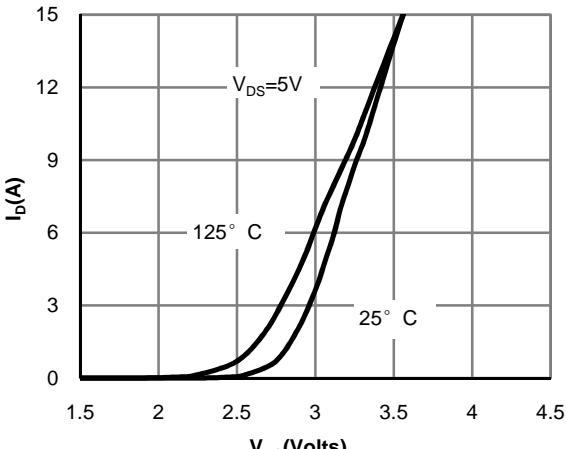


Figure 2: Transfer Characteristics

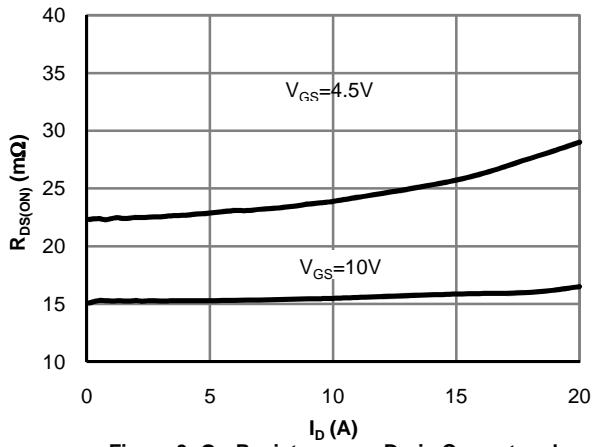


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

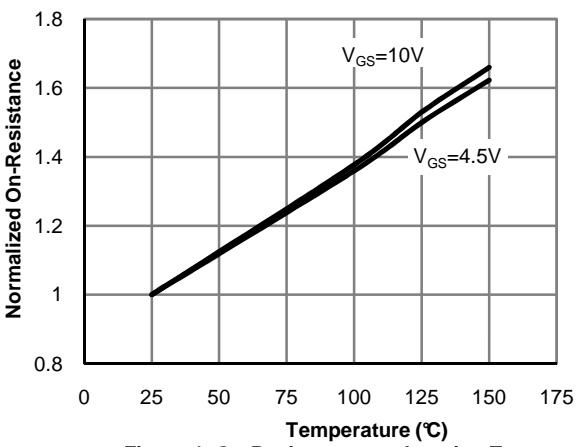


Figure 4: On-Resistance vs. Junction Temperature

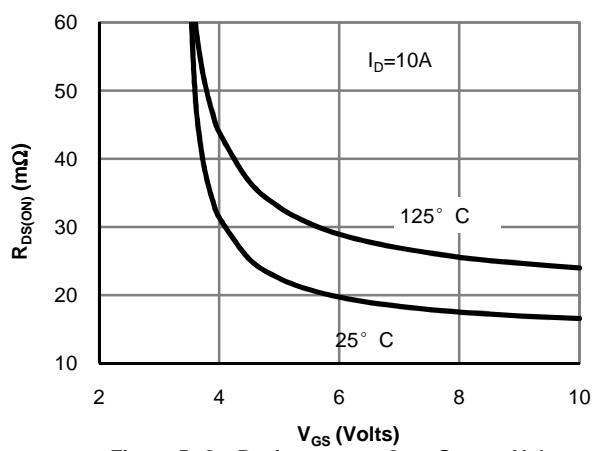


Figure 5: On-Resistance vs. Gate-Source Voltage

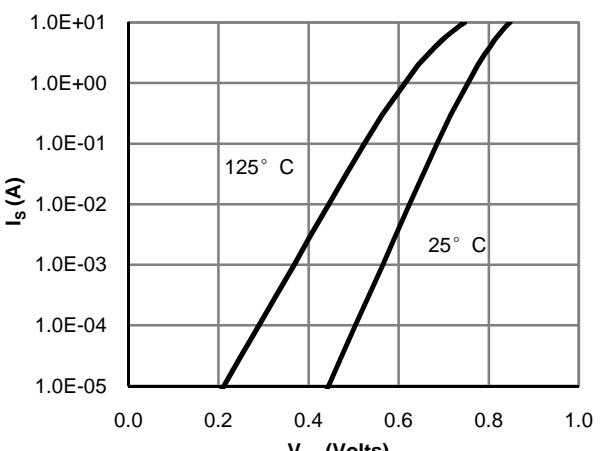
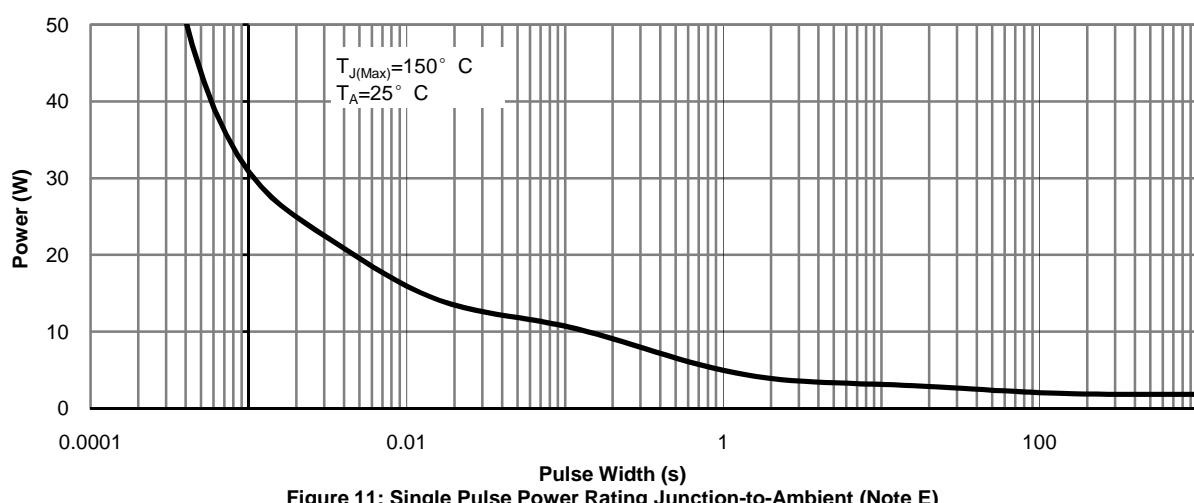
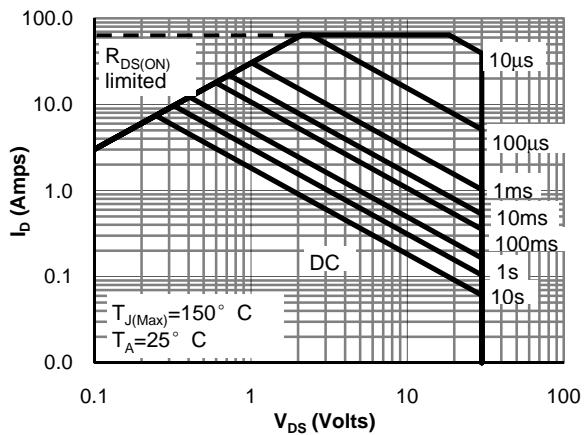
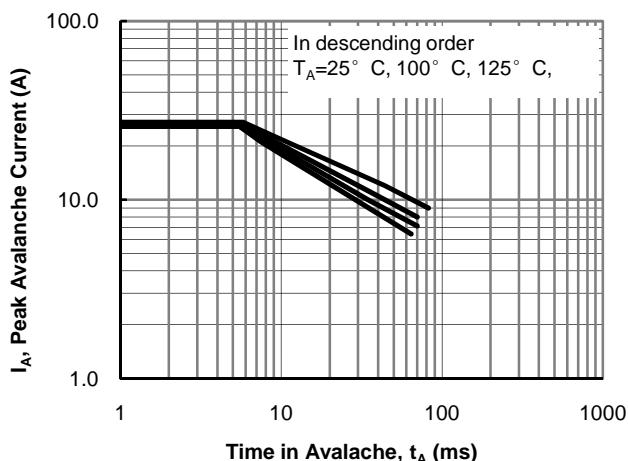
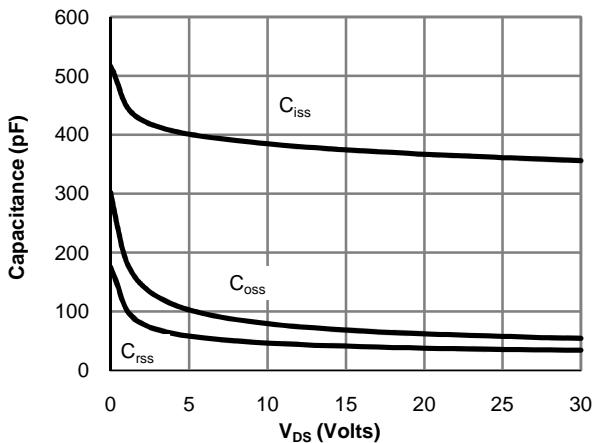
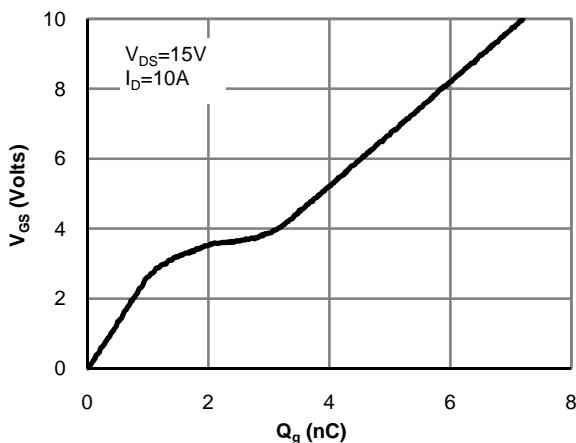


Figure 6: Body-Diode Characteristics

**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**



## TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

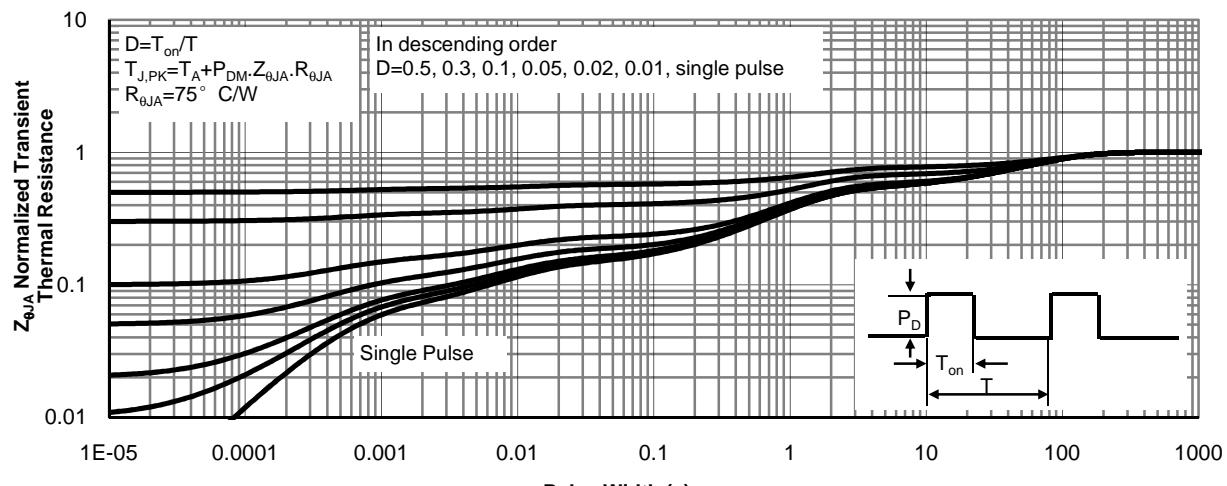
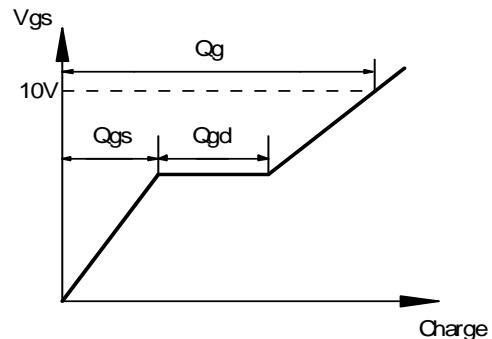
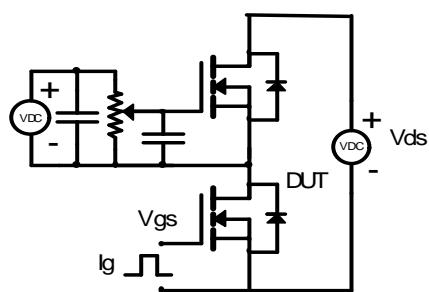
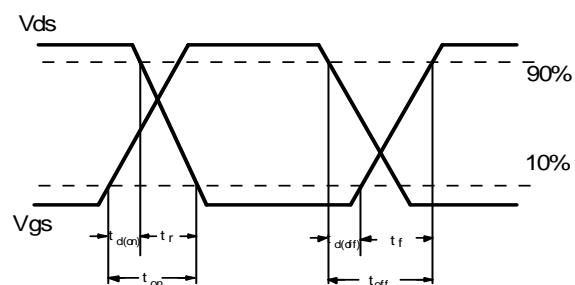
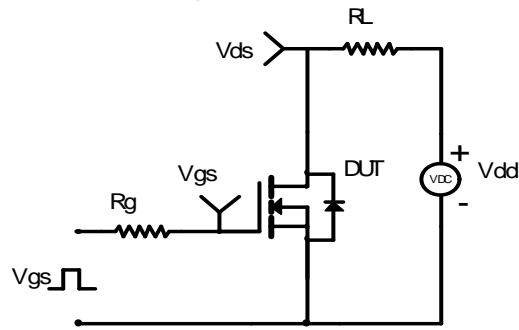


Figure 12: Normalized Maximum Transient Thermal Impedance

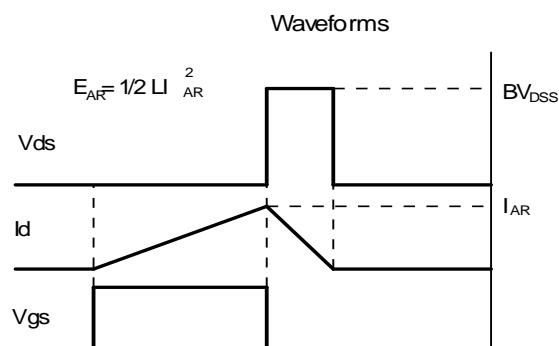
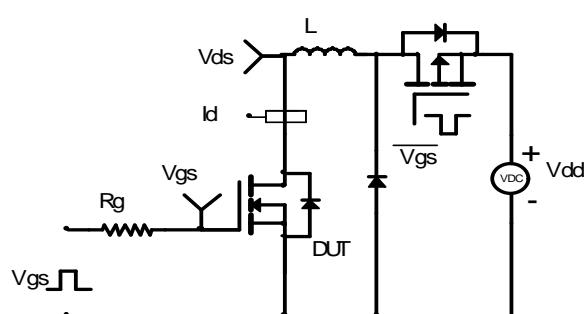
Gate Charge Test Circuit & Waveform



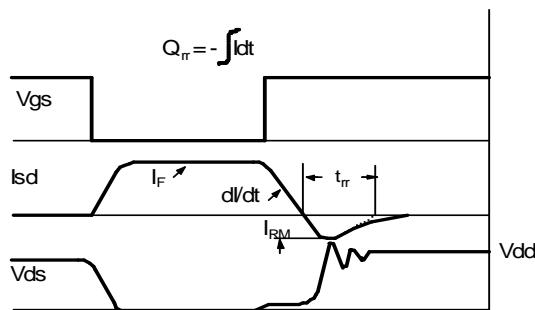
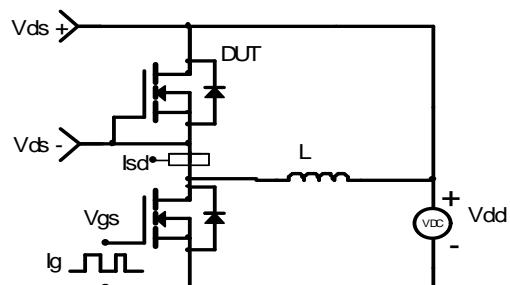
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit &

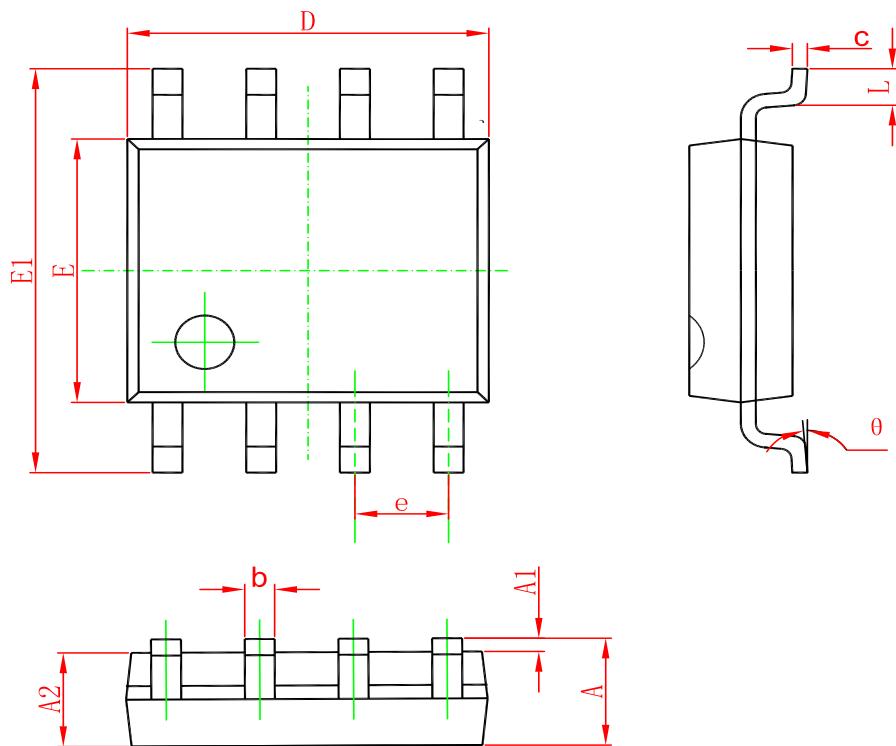


Diode Recovery Test Circuit & Waveforms

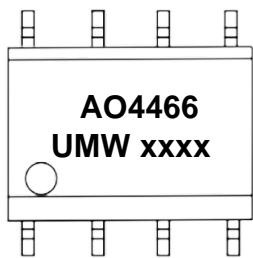


## PACKAGE OUTLINE DIMENSIONS

SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

**Marking**

("xxxx"代表年份周期)

**Ordering information**

Order code	Package	Baseqty	Deliverymode
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