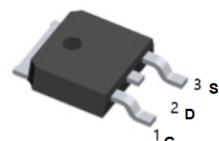
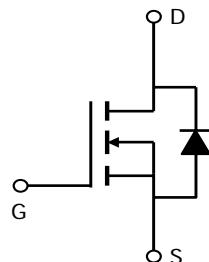


General Description

The AOD442 used advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. Those devices are suitable for use as a load switch or in PWM applications.



TO-252(DPAK) top view

General Features

| | |
|------------------------------------|--------|
| DS | 60V |
| I_D (at $V_{GS}=10V$) | 37A |
| $R_{DS(ON)}$ (at $V_{GS}=10V$) | < 20mΩ |
| $R_{DS(ON)}$ (at $V_{GS} = 4.5V$) | < 25mΩ |

Absolute Maximum Ratings $T_A=25^\circ C$ unless otherwise noted

| Parameter | Symbol | Maximum | Units |
|---|------------------|------------|-------|
| Drain-Source Voltage | V_{DS} | 60 | V |
| Gate-Source Voltage | V_{GS} | ± 20 | V |
| Continuous Drain Current ^G | I_D | 37 | A |
| $T_C=100^\circ C$ | | 26 | |
| Pulsed Drain Current ^C | I_{DM} | 60 | |
| Continuous Drain Current | I_{DSM} | 7 | A |
| $T_A=70^\circ C$ | | 5 | |
| Avalanche Current ^C | I_{AS}, I_{AR} | 30 | A |
| Avalanche energy $L=0.1mH$ ^C | E_{AS}, E_{AR} | 45 | mJ |
| Power Dissipation ^B | P_D | 60 | W |
| $T_C=100^\circ C$ | | 30 | |
| Power Dissipation ^A | P_{DSM} | 2.1 | W |
| $T_A=70^\circ C$ | | 1.3 | |
| Junction and Storage Temperature Range | T_J, T_{STG} | -55 to 175 | °C |

Thermal Characteristics

| Parameter | Symbol | Typ | Max | Units |
|--|-----------------|------|-----|-------|
| Maximum Junction-to-Ambient ^A | $R_{\theta JA}$ | 17.4 | 25 | °C/W |
| Maximum Junction-to-Ambient ^D | | 51 | 60 | °C/W |
| Maximum Junction-to-Case | $R_{\theta JC}$ | 1.8 | 2.5 | °C/W |

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

| Symbol | Parameter | Conditions | Min | Typ | Max | Units |
|-----------------------------|---------------------------------------|---|------|------|--------|------------------|
| STATIC PARAMETERS | | | | | | |
| BV_{DSS} | Drain-Source Breakdown Voltage | $I_D=250\mu\text{A}, V_{GS}=0\text{V}$ | 60 | | | V |
| I_{DSS} | Zero Gate Voltage Drain Current | $V_{DS}=48\text{V}, V_{GS}=0\text{V}$ $T_J=55^\circ\text{C}$ | | | 1 5 | μA |
| I_{GSS} | Gate-Body leakage current | $V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$ | | | 100 | nA |
| $V_{\text{GS(th)}}$ | Gate Threshold Voltage | $V_{DS}=V_{GS}, I_D=250\mu\text{A}$ | 1.6 | 2.1 | 2.7 | V |
| $I_{\text{D(ON)}}$ | On state drain current | $V_{GS}=10\text{V}, V_{DS}=5\text{V}$ | 60 | | | A |
| $R_{\text{DS(ON)}}$ | Static Drain-Source On-Resistance | $V_{GS}=10\text{V}, I_D=20\text{A}$ | | 16 | 20 | $\text{m}\Omega$ |
| | | $V_{GS}=4.5\text{V}, I_D=20\text{A}$ | | 20 | 25 | $\text{m}\Omega$ |
| g_{FS} | Forward Transconductance | $V_{DS}=5\text{V}, I_D=20\text{A}$ | | 65 | | S |
| V_{SD} | Diode Forward Voltage | $I_S=1\text{A}, V_{GS}=0\text{V}$ | | 0.7 | 1 | V |
| I_S | Maximum Body-Diode Continuous Current | | | | 32 | A |
| DYNAMIC PARAMETERS | | | | | | |
| C_{iss} | Input Capacitance | $V_{GS}=0\text{V}, V_{DS}=30\text{V}, f=1\text{MHz}$ | 1535 | 1920 | 2300 | pF |
| C_{oss} | Output Capacitance | | 108 | 155 | 200 | pF |
| C_{rss} | Reverse Transfer Capacitance | | 70 | 116 | 165 | pF |
| R_g | Gate resistance | $V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$ | 0.3 | 0.65 | 0.8 | Ω |
| SWITCHING PARAMETERS | | | | | | |
| $Q_g(10\text{V})$ | Total Gate Charge | $V_{GS}=10\text{V}, V_{DS}=30\text{V}, I_D=20\text{A}$ | 38 | 47.6 | 68 | nC |
| $Q_g(4.5\text{V})$ | Total Gate Charge | | 20 | 24.2 | 30 | nC |
| Q_{gs} | Gate Source Charge | | 4.8 | 6 | 7 | nC |
| Q_{gd} | Gate Drain Charge | | 8.5 | 14.4 | 20 | nC |
| $t_{\text{D(on)}}$ | Turn-On Delay Time | $V_{GS}=10\text{V}, V_{DS}=30\text{V}, R_L=1.5\Omega, R_{\text{GEN}}=3\Omega$ | | 7.4 | | ns |
| t_r | Turn-On Rise Time | | | 5.1 | | ns |
| $t_{\text{D(off)}}$ | Turn-Off Delay Time | | | 28.2 | | ns |
| t_f | Turn-Off Fall Time | | | 5.5 | | ns |
| t_{rr} | Body Diode Reverse Recovery Time | $I_F=20\text{A}, dI/dt=100\text{A}/\mu\text{s}$ | | 34 | 41 | ns |
| Q_{rr} | Body Diode Reverse Recovery Charge | $I_F=20\text{A}, dI/dt=100\text{A}/\mu\text{s}$ | | 46 | | nC |

2

A. The value of $R_{\text{on,JA}}$ is measured with the device mounted on 1in FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$. The Power dissipation P_{DSM} is based on $R_{\text{on,JA}}$ and the maximum allowed junction temperature of 150°C . The value in any given application depends on the user's specific board design, and the maximum temperature of 175°C may be used if the PCB allows it.

B. The power dissipation P_D is based on $T_{J(\text{MAX})}=175^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.

C. Repetitive rating, pulse width limited by junction temperature $T_{J(\text{MAX})}=175^\circ\text{C}$. Ratings are based on low frequency and duty cycles to keep initial $T_J=25^\circ\text{C}$.

D. The $R_{\text{on,JA}}$ is the sum of the thermal impedance from junction to case $R_{\text{on,JC}}$ and case to ambient.

E. The static characteristics in Figures 1 to 6 are obtained using $<300\mu\text{s}$ pulses, duty cycle 0.5% max.

F. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of $T_{J(\text{MAX})}=175^\circ\text{C}$. The SOA curve provides a single pulse rating.

G. The maximum current rating is package limited.

H. These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$.

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

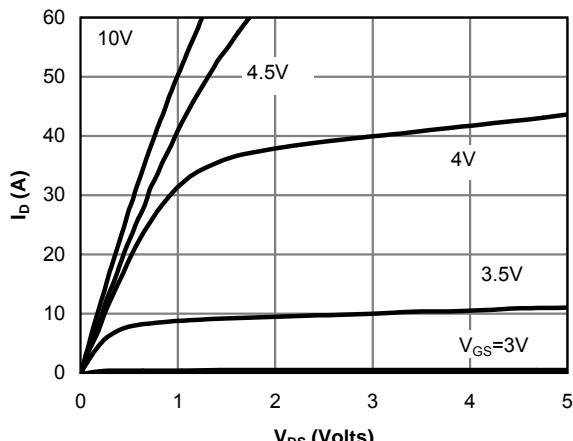


Fig 1: On-Region Characteristics (Note E)

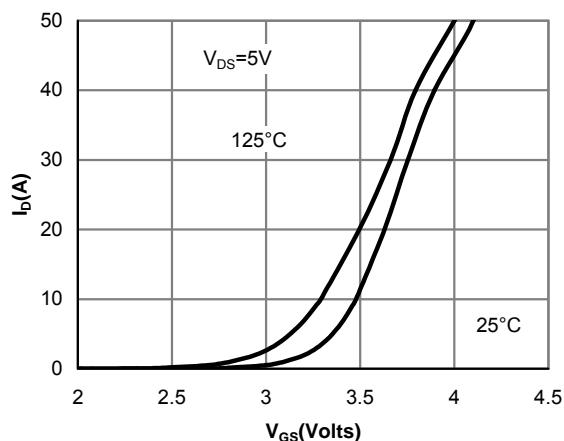


Figure 2: Transfer Characteristics (Note E)

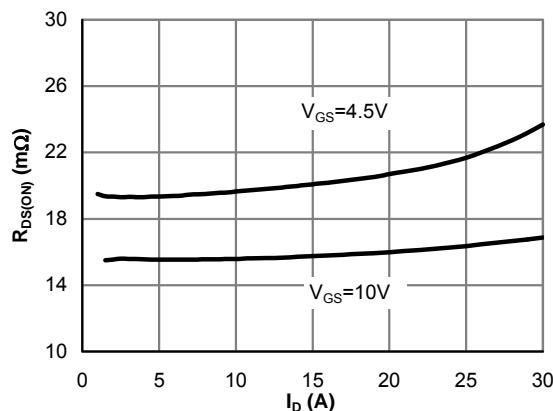


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

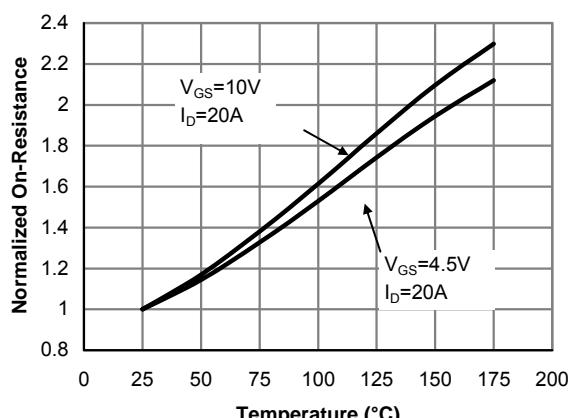


Figure 4: On-Resistance vs. Junction Temperature (Note E)

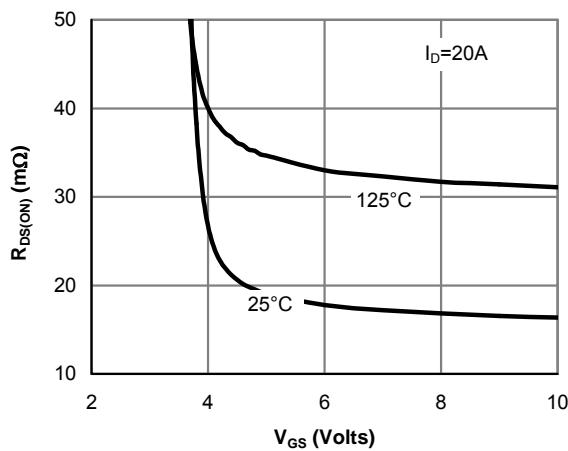


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

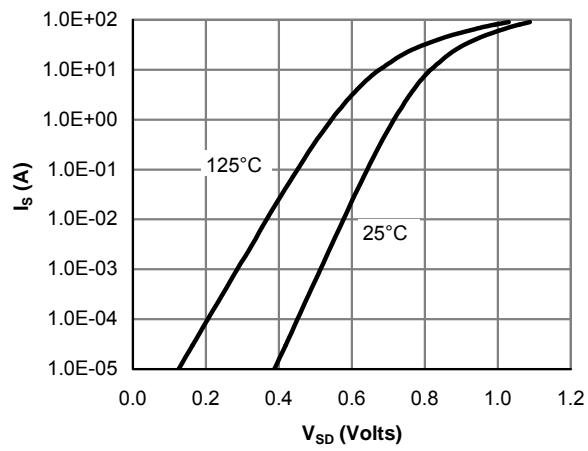


Figure 6: Body-Diode Characteristics (Note E)

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

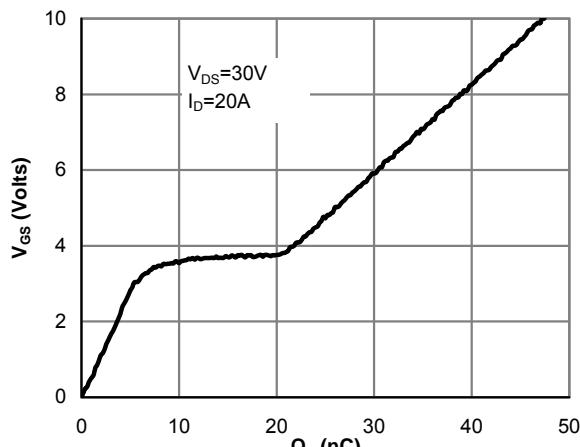


Figure 7: Gate-Charge Characteristics

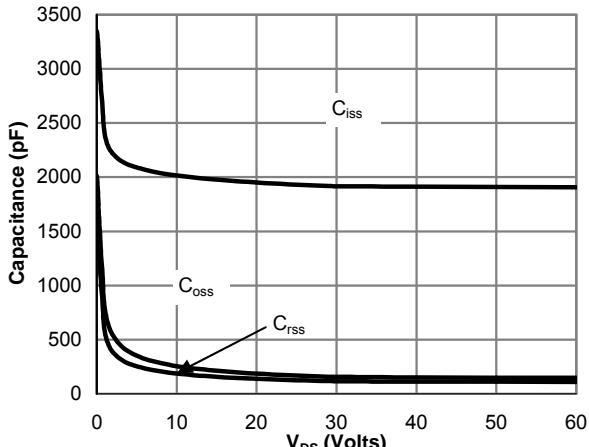


Figure 8: Capacitance Characteristics

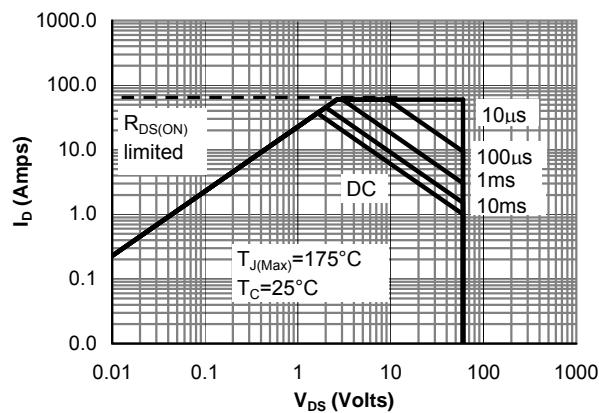


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

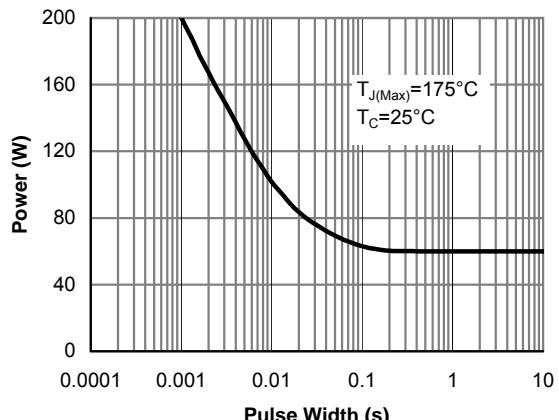


Figure 10: Single Pulse Power Rating Junction-to-Case (Note F)

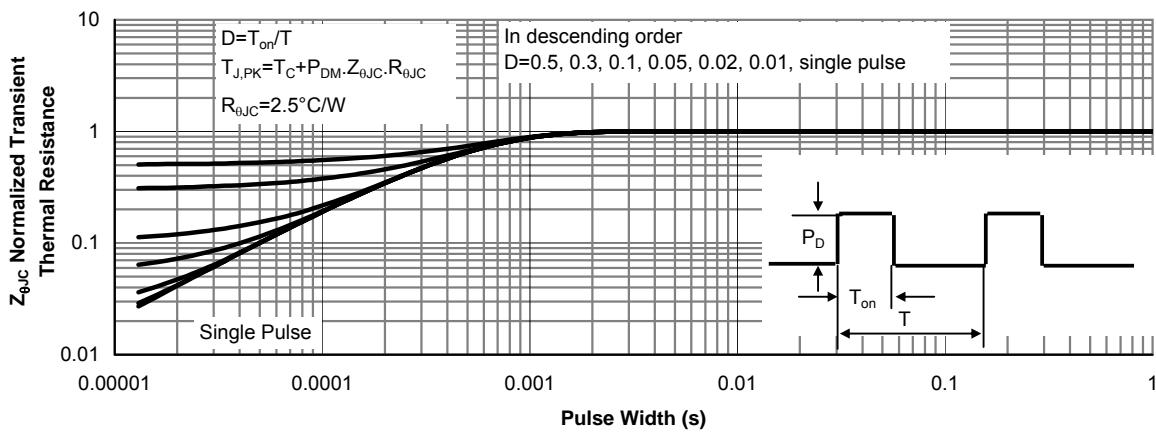


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

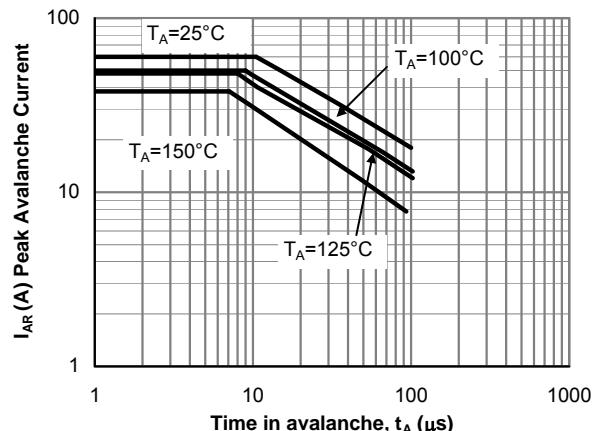
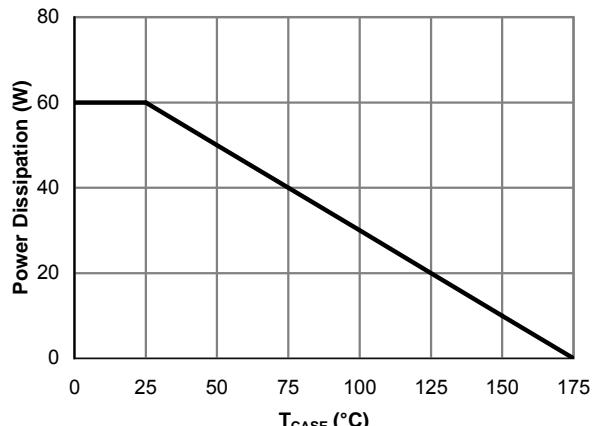
Figure 12: Single Pulse Avalanche capability
(Note C)

Figure 13: Power De-rating (Note F)

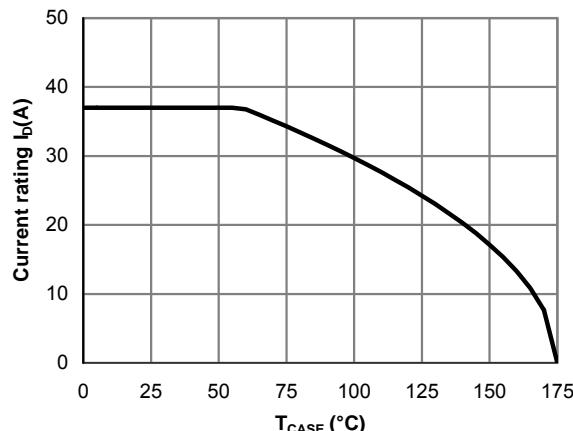


Figure 14: Current De-rating (Note F)

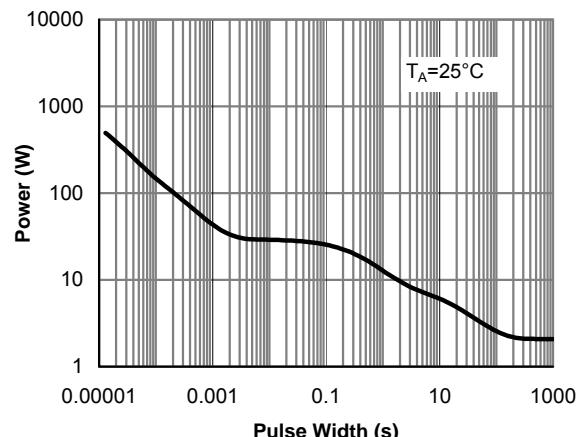


Figure 15: Single Pulse Power Rating Junction-to-Ambient (Note H)

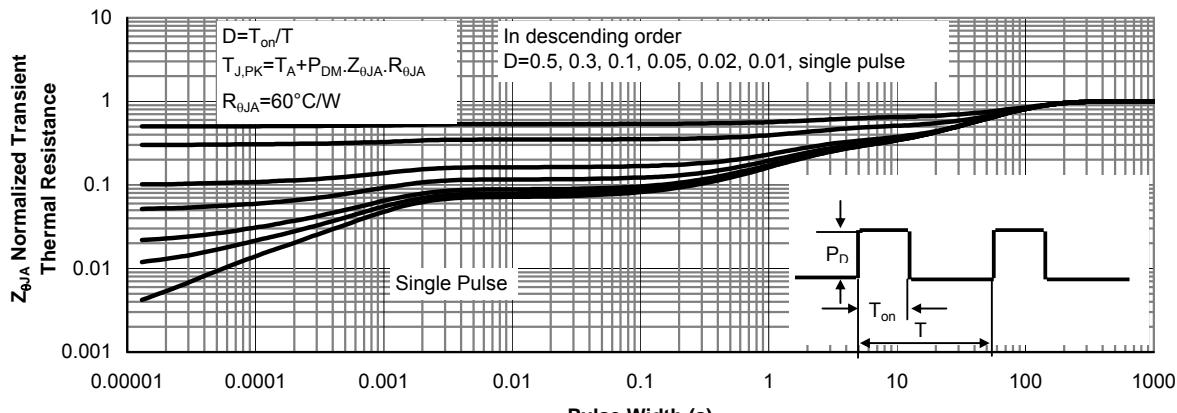
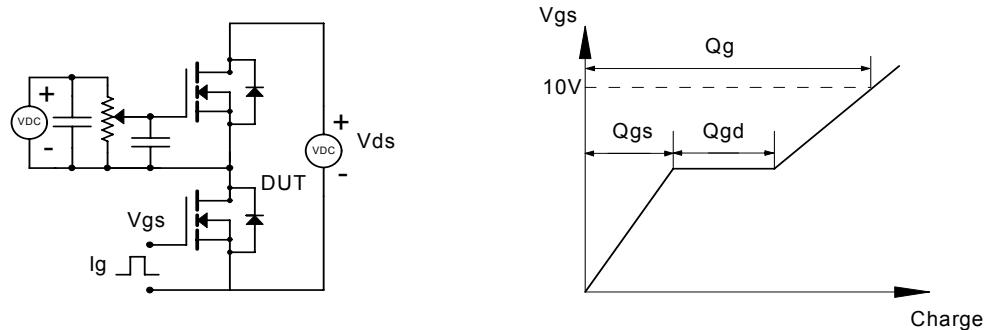
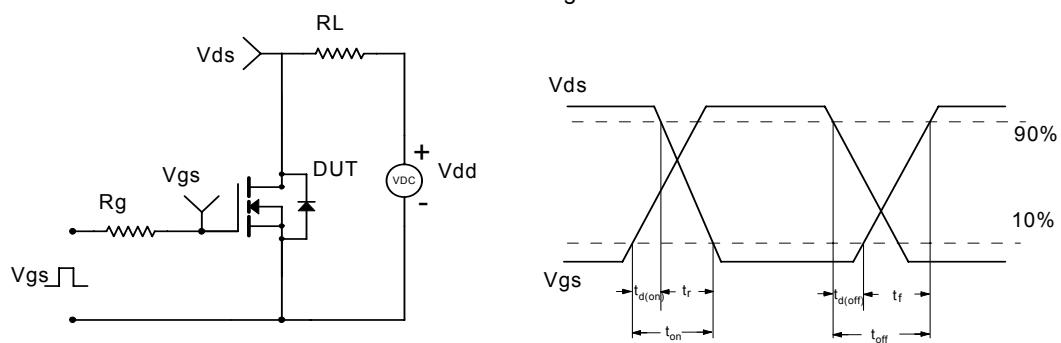


Figure 16: Normalized Maximum Transient Thermal Impedance (Note H)

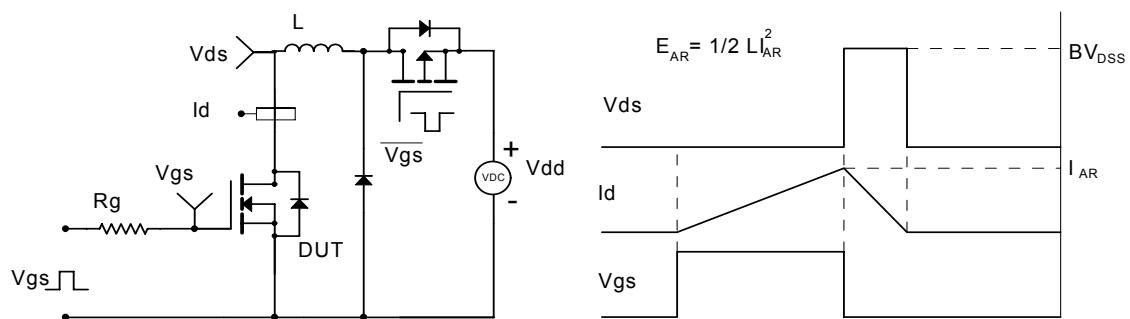
Gate Charge Test Circuit & Waveform



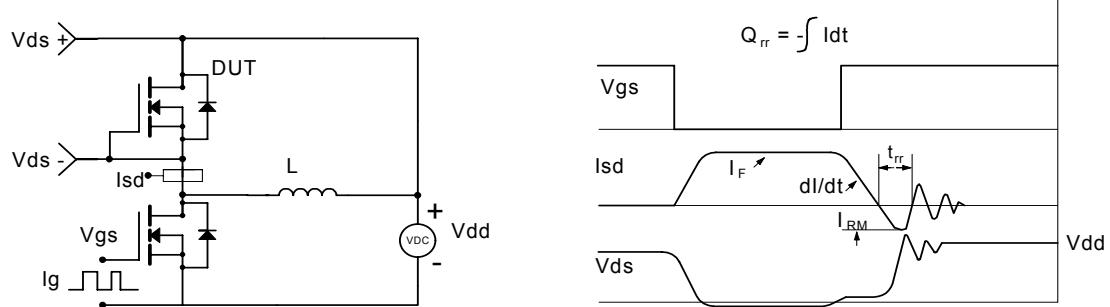
Resistive Switching Test Circuit & Waveforms



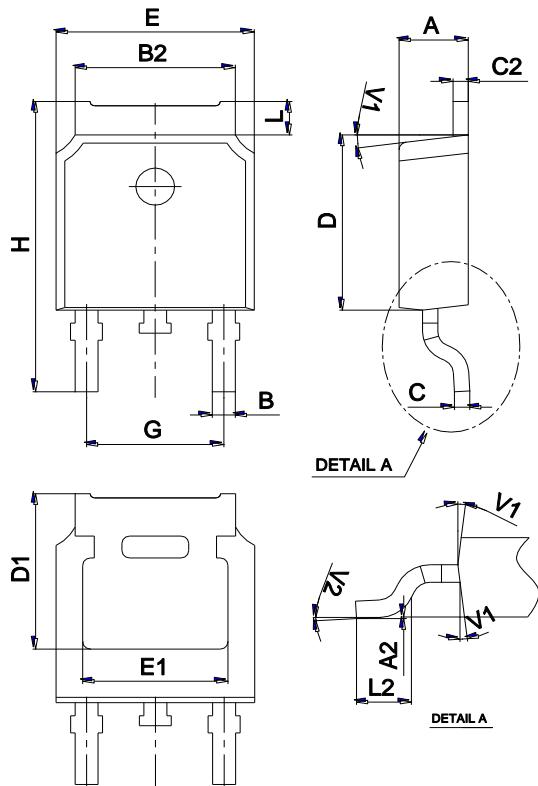
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Package Mechanical Data TO-252



| Ref. | Dimensions | | | | | |
|------|-------------|------|-------|----------|------|-------|
| | Millimeters | | | Inches | | |
| | Min. | Typ. | Max. | Min. | Typ. | Max. |
| A | 2.10 | | 2.50 | 0.083 | | 0.098 |
| A2 | 0 | | 0.10 | 0 | | 0.004 |
| B | 0.66 | | 0.86 | 0.026 | | 0.034 |
| B2 | 5.18 | | 5.48 | 0.202 | | 0.216 |
| C | 0.40 | | 0.60 | 0.016 | | 0.024 |
| C2 | 0.44 | | 0.58 | 0.017 | | 0.023 |
| D | 5.90 | | 6.30 | 0.232 | | 0.248 |
| D1 | 5.30REF | | | 0.209REF | | |
| E | 6.40 | | 6.80 | 0.252 | | 0.268 |
| E1 | 4.63 | | | 0.182 | | |
| G | 4.47 | | 4.67 | 0.176 | | 0.184 |
| H | 9.50 | | 10.70 | 0.374 | | 0.421 |
| L | 1.09 | | 1.21 | 0.043 | | 0.048 |
| L2 | 1.35 | | 1.65 | 0.053 | | 0.065 |
| V1 | | 7° | | | 7° | |
| V2 | 0° | | 6° | 0° | | 6° |

Ordering information

| Order code | Package | Baseqty | Delivery mode |
|------------|---------|---------|---------------|
| UMW AOD442 | TO-252 | 2500 | Tape and reel |

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