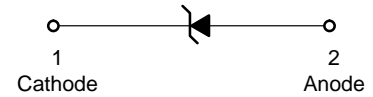


Description

The ESD7371 Series is designed to protect voltage sensitive components that require ultra-low capacitance from ESD and transient voltage events. Excellent clamping capability, low capacitance, high breakdown voltage, high linearity, low leakage, and fast response time make these parts ideal for ESD protection on designs where board space is at a premium. It has industry leading capacitance linearity over voltage making it ideal for RF applications. This capacitance linearity combined with the extremely small package and low insertion loss makes this part well suited for use in antenna line applications for wireless handsets and terminals.



Features

- Industry Leading Capacitance Linearity Over Voltage
- Low Capacitance (0.7 pF Max, I/O to GND)
- Stand-off Voltage: 5.3 V
- Low Leakage: < 1 nA
- Low Dynamic Resistance < 1 Ω
- IEC61000-4-2 Level 4 ESD Protection
- 1000 ESD IEC61000-4-2 Strikes ±8 kV Contact / Air Discharged

Typical Applications

- RF Signal ESD Protection
- RF Switching, PA, and Antenna ESD Protection
- Near Field Communications
- USB 2.0, USB 3.0

MAXIMUM RATINGS (T_A = 25°C unless otherwise noted)

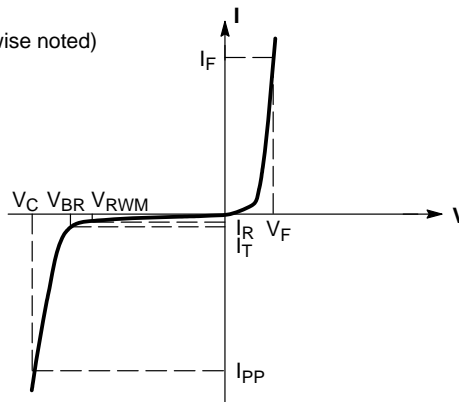
Rating	Symbol	Value	Unit
IEC 61000-4-2 (ESD) (Note 1)		20	kV
IEC 61000-4-5 (ESD) (Note 2)		3.0	A
Total Power Dissipation (Note 3) @ T _A = 25 °C	P _D	300	mW
Thermal Resistance, Junction-to-Ambient	R _{θJA}	400	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C
Lead Solder Temperature – Maximum (10 Second Duration)	T _L	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Non-repetitive current pulse at T_A = 25°C, per IEC61000-4-2 waveform.
2. Non-repetitive current pulse at T_A = 25°C, per IEC61000-4-5 waveform.
3. Mounted with recommended minimum pad size, DC board FR-4

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Working Peak Reverse Voltage
I_R	Maximum Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current



Uni-Directional

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}				5.3	V
Breakdown Voltage (Note 4)	V_{BR}	$I_T = 1 \text{ mA}$	7.0			V
Reverse Leakage Current	I_R	$V_{RWM} = 5.3 \text{ V}$		< 1.0	50	nA
Clamping Voltage (Note 5)	V_C	$I_{PP} = 1 \text{ A}$		11	15	V
Clamping Voltage (Note 5)	V_C	$I_{PP} = 3 \text{ A}$		14	20	V
Junction Capacitance	C_J	$V_R = 0 \text{ V}, f = 1 \text{ MHz}$ $V_R = 0 \text{ V}, f < 1 \text{ GHz}$		0.43 0.39	0.7 0.7	pF
Dynamic Resistance	R_{DYN}	TLP Pulse		0.45		Ω

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Breakdown voltage is tested from pin 1 to 2 and pin 2 to 1.
5. Non-repetitive current pulse at $T_A = 25^\circ\text{C}$, per IEC61000-4-5 waveform.

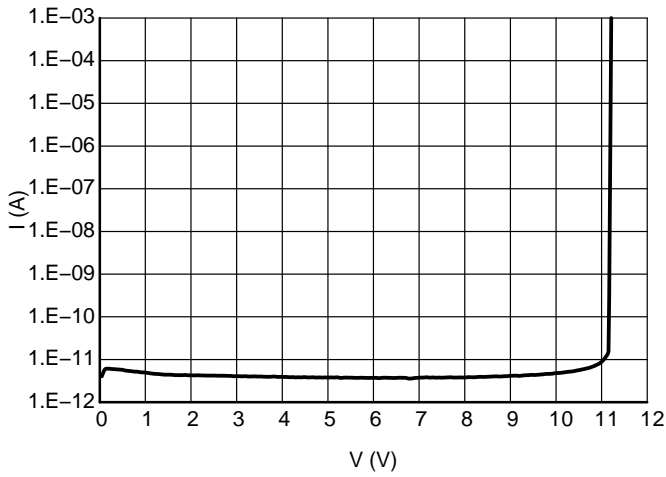


Figure 1. IV Characteristics

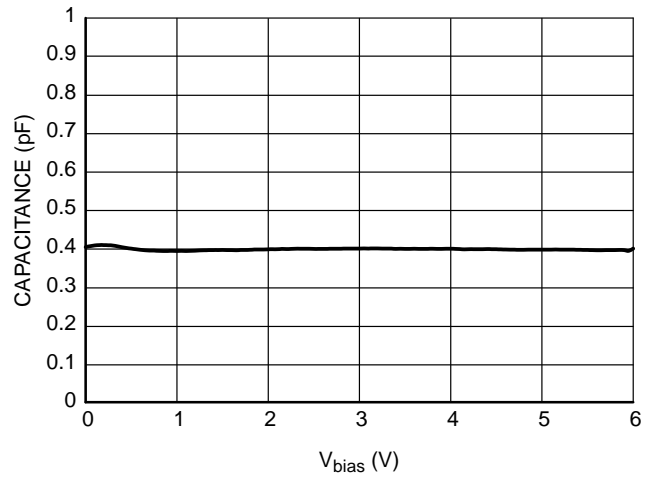


Figure 2. CV Characteristics

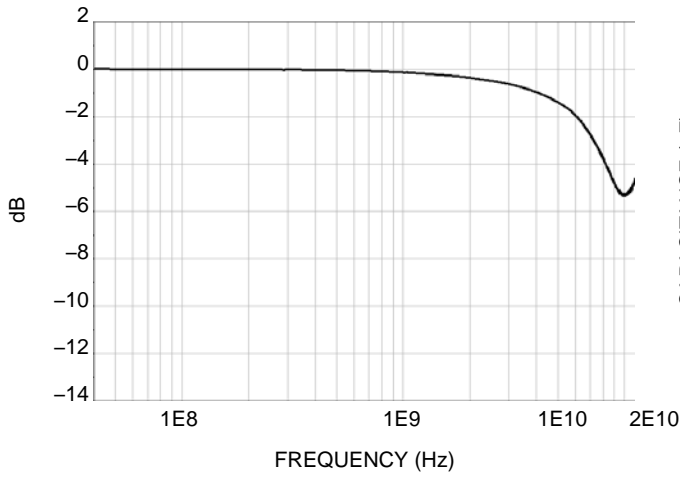


Figure 3. RF Insertion Loss

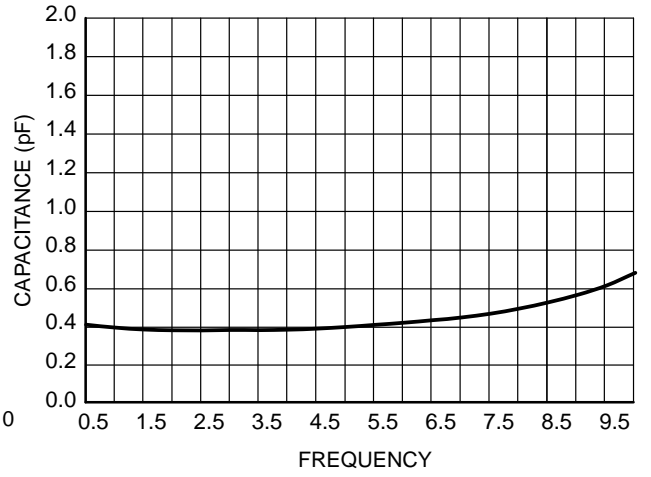


Figure 4. Capacitance over Frequency

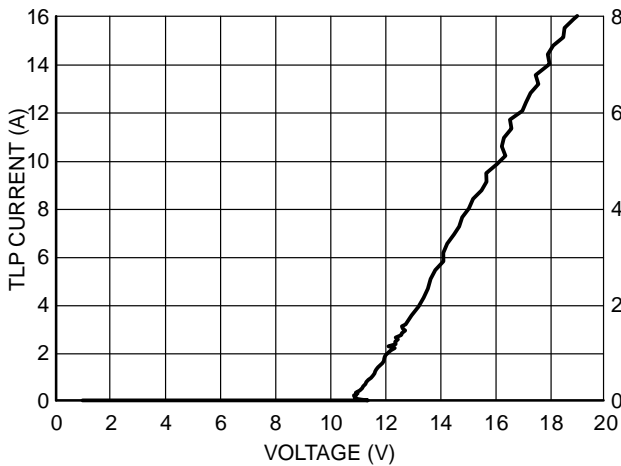


Figure 5. Positive TLP I-V Curve

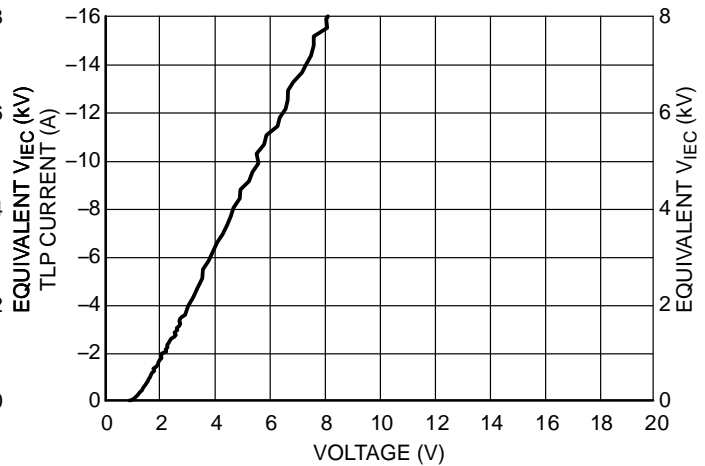


Figure 6. Negative TLP I-V Curve

IEC 61000-4-2 Spec.

Level	Test Voltage (kV)	First Peak Current (A)	Current at 30 ns (A)	Current at 60 ns (A)
1	2	7.5	4	2
2	4	15	8	4
3	6	22.5	12	6
4	8	30	16	8

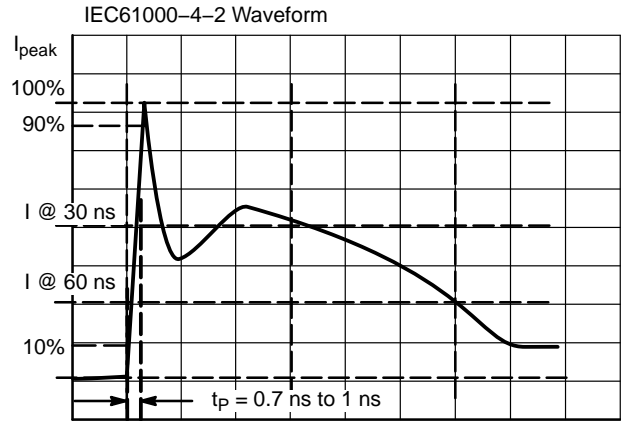


Figure 7. IEC61000-4-2 Spec

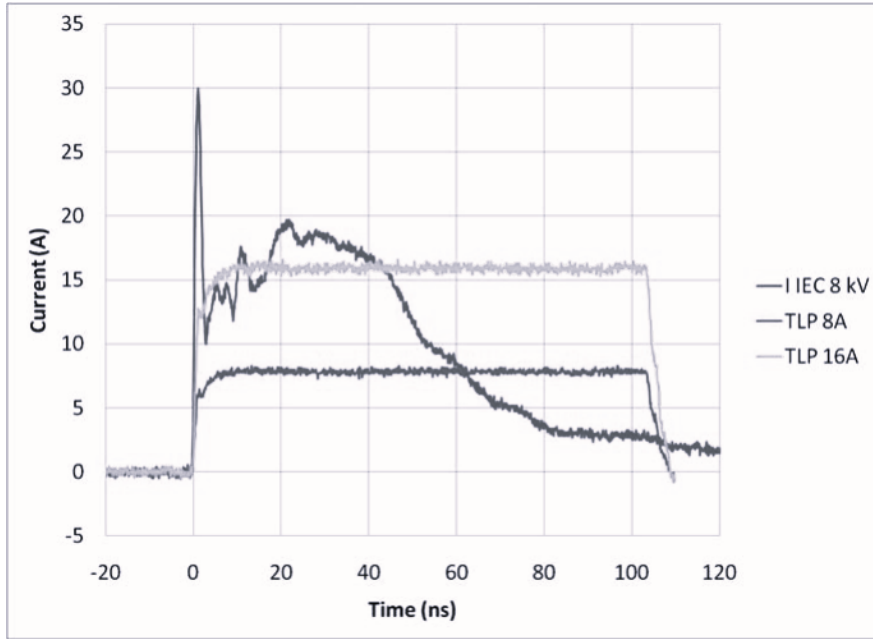


Figure 10. Comparison Between 8 kV IEC 61000-4-2 and 8 A and 16 A TLP Waveforms

Package outline dimensions

Marking

Ordering information

Order code	Package	Base qty	Delivery mode
UMW ESD7371XV2T1G	SOD-523	3000	Tape and reel

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