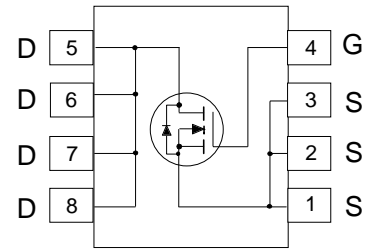


General Description

These devices are well suited for low voltage and battery powered applications where low in-line power loss and fast switching are required.



SOP-8

Features

- $V_{DS(V)} = 30V$
- $I_D = 15 A (V_{GS} = 4.5V)$
- $R_{DS(ON)} < 7.5m\Omega (V_{GS}=2.5V)$
- $R_{DS(ON)} < 10m\Omega (V_{GS}=4.5V)$
- Low gate charge (47nC typical).
- Fast switching speed.
- High performance trench technology for extremely low $R_{DS(ON)}$
- High power and current handling capability.

Absolute Maximum Ratings $T_A = 25^\circ C$ unless otherwise noted

Symbol	Parameter	FDS6570A	Units
V_{DSS}	Drain-Source Voltage	30	V
V_{GSS}	Gate-Source Voltage	± 8	V
I_D	Drain Current - Continuous (Note 1a) - Pulsed	15	A
		50	
P_D	Power Dissipation for Single Operation (Note 1a) (Note 1b) (Note 1c)	2.5	W
		1.2	
		1	
T_J, T_{sig}	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ C$

Thermal Characteristics

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Note 1a)	50	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case (Note 1)	25	$^\circ C/W$

Electrical Characteristics T_A = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
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Off Characteristics

BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	30			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C		29		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 16 V, V _{GS} = 0 V			1	μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 8 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -8 V, V _{DS} = 0 V			-100	nA

On Characteristics (Note 2)

V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	1	1.2	1.4	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C		-4		mV/°C
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 4.5 V, I _D = 15 A V _{GS} = 2.5 V, I _D = 12 A		6 8	7.5 10	mΩ
I _{D(on)}	On-State Drain Current	V _{GS} = 4.5 V, V _{DS} = 5.0 V	25			A
g _{FS}	Forward Transconductance	V _{DS} = 5 V, I _D = 15 A		70		S

Dynamic Characteristics

C _{iss}	Input Capacitance	V _{DS} = 10 V, V _{GS} = 0 V, f = 1.0 MHz		4700		pF
C _{oss}	Output Capacitance			850		pF
C _{rss}	Reverse Transfer Capacitance			310		pF

Switching Characteristics (Note 2)

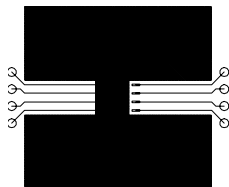
t _{d(on)}	Turn-On Delay Time	V _{DD} = 10 V, I _D = 1 A, V _{GS} = 4.5 V, R _{GEN} = 6 Ω		20	32	ns
t _r	Turn-On Rise Time			27	44	ns
t _{d(off)}	Turn-Off Delay Time			95	133	ns
t _f	Turn-Off Fall Time			35	56	ns
Q _g	Total Gate Charge	V _{DS} = 10 V, I _D = 15 A, V _{GS} = 5 V,		47	66	nC
Q _{gs}	Gate-Source Charge			7		nC
Q _{gd}	Gate-Drain Charge			10.5		nC

Drain-Source Diode Characteristics and Maximum Ratings

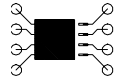
I _S	Maximum Continuous Drain-Source Diode Forward Current			2.1	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 2.1 A (Note 2)	0.65	1.2	V

Notes:

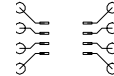
- R_{θJA} is the sum of the junction-to-case and case-to-ambient resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{θJC} is guaranteed by design while R_{θJA} is determined by the user's board design.



a) 50° C/W when mounted on a 0.5 in² pad of 2 oz. copper.



b) 105° C/W when mounted on a 0.02 in² pad of 2 oz. copper.



c) 125° C/W when mounted on a 0.003 in² pad of 2 oz. copper.

Scale 1 : 1 on letter size paper

- Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%

Typical Characteristics

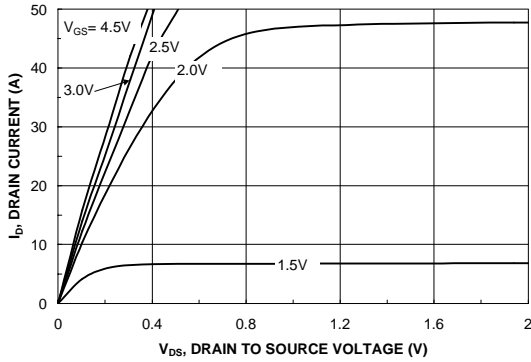


Figure 1. On-Region Characteristics.

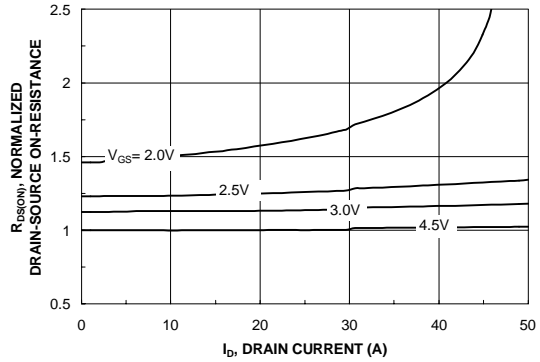


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

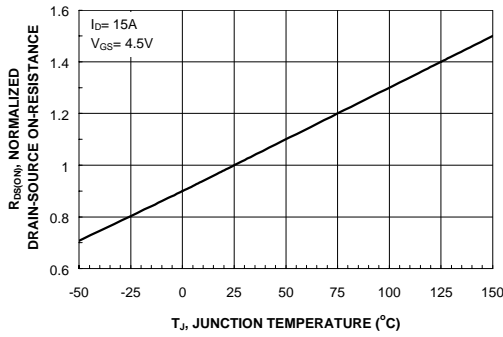


Figure 3. On-Resistance Variation with Temperature.

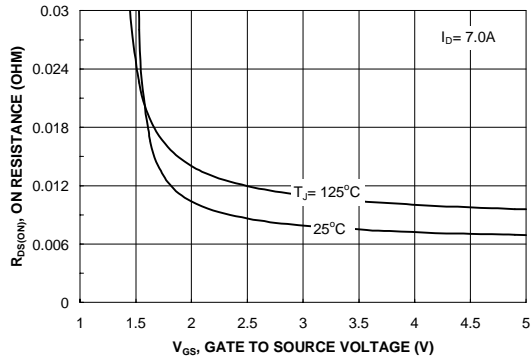


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

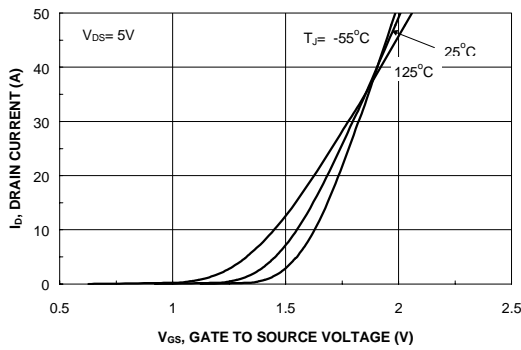


Figure 5. Transfer Characteristics.

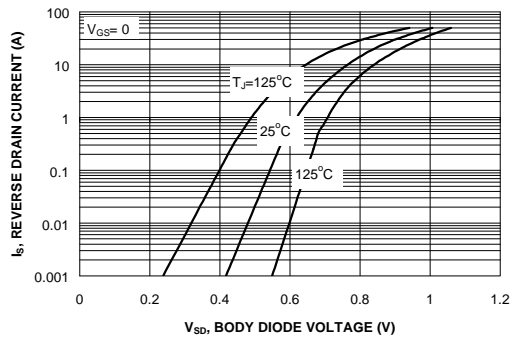


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

Typical Characteristics

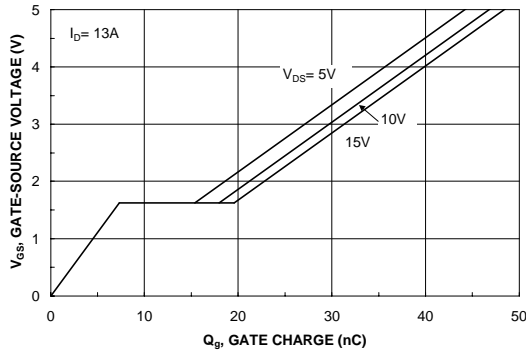


Figure 7. Gate Charge Characteristics.

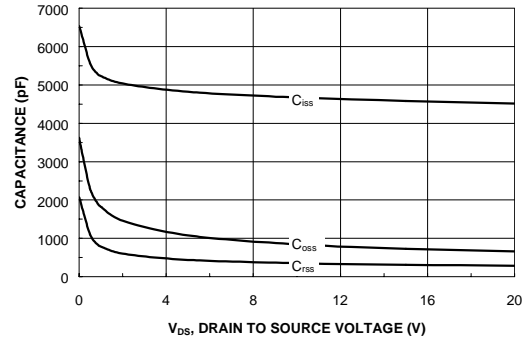


Figure 8. Capacitance Characteristics.

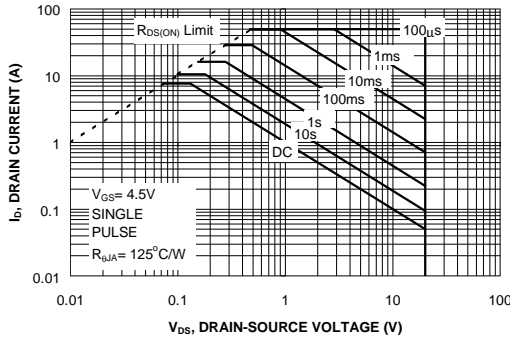


Figure 9. Maximum Safe Operating Area.

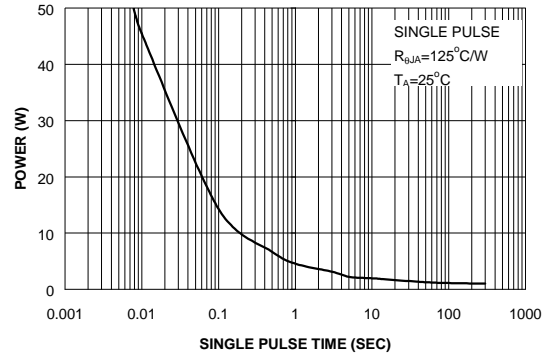


Figure 10. Single Pulse Maximum Power Dissipation.

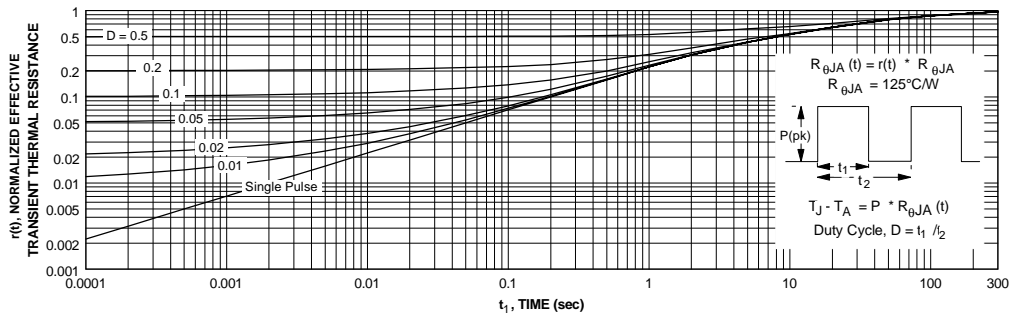
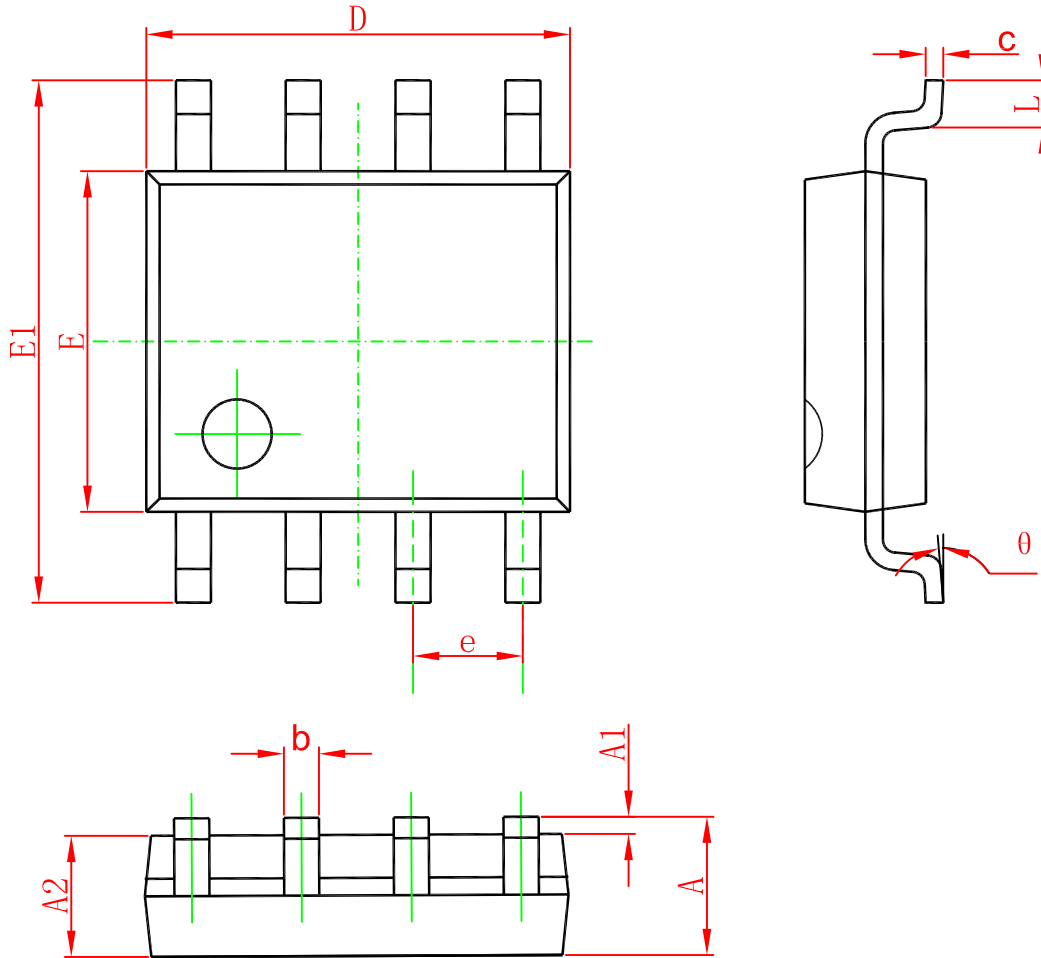


Figure 11. Transient Thermal Response Curve.

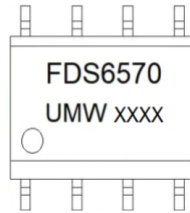
Thermal characterization performed using the conditions described in Note 1c. Transient thermal response will change depending on the circuit board design.

Package Mechanical Data SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
theta	0°	8°	0°	8°

Marking



Ordering information

Order code	Package	Baseqty	Deliverymode
UMW FDS6570A	SOP-8	3000	Tape and reel

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[DMN2990UFB-7B](#) [SSM3K35CT,L3F](#) [IPLK60R1K0PFD7ATMA1](#) [2N7002W-G](#) [MCAC30N06Y-TP](#) [IPWS65R035CFD7AXKSA1](#)
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