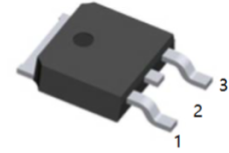
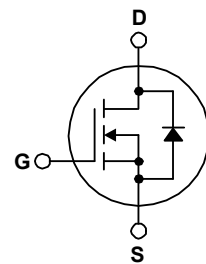


Description

This N-Channel enhancement mode power MOSFET is produced using proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.



1.G 2.D 3.S
TO-252(DPAK) top view



Features

- $V_{DS}(V) = 100V$
- $I_D = 15.6A$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 100m\Omega$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 110m\Omega$ ($V_{GS} = 5V$)
- Low Gate Charge (Typ. 14 nC)
- Low Crss (Typ. 35 pF)

Absolute Maximum Ratings $T_C = 25^\circ C$ unless otherwise noted.

Symbol	Parameter	FQD19N10LTM	Unit
V_{DSS}	Drain-Source Voltage	100	V
I_D	Drain Current	- Continuous ($T_C = 25^\circ C$)	15.6
		- Continuous ($T_C = 100^\circ C$)	9.8
I_{DM}	Drain Current - Pulsed (Note 1)	62.4	A
V_{GSS}	Gate-Source Voltage	± 20	V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	220	mJ
I_{AR}	Avalanche Current (Note 1)	15.6	A
E_{AR}	Repetitive Avalanche Energy (Note 1)	5.0	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	6.0	V/ns
P_D	Power Dissipation ($T_A = 25^\circ C$) *	2.5	W
	Power Dissipation ($T_C = 25^\circ C$)	50	W
	- Derate Above $25^\circ C$	0.4	W/ $^\circ C$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	$^\circ C$
T_L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds	300	$^\circ C$

Thermal Characteristics

Symbol	Parameter	FQD19N10LTM	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	2.5	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper), Max.	110	
	Thermal Resistance, Junction to Ambient (*1 in ² Pad of 2-oz Copper), Max.	50	

Electrical Characteristics T_c = 25°C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	100			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		0.09		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 100 V, V _{GS} = 0 V			1	μA
		V _{DS} = 80 V, T _C = 125°C			10	μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 20 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -20 V, V _{DS} = 0 V			-100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	1.0		2.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 7.8 A		74	100	mΩ
		V _{GS} = 5 V, I _D = 7.8 A		82	110	
g _{FS}	Forward Transconductance	V _{DS} = 30 V, I _D = 7.8 A		14		S
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz		670	870	pF
C _{oss}	Output Capacitance			160	210	pF
C _{rss}	Reverse Transfer Capacitance			35	45	pF
t _{d(on)}	Turn-On Delay Time	V _{DD} = 50 V, I _D = 19 A, R _G = 25 Ω		14	38	ns
t _r	Turn-On Rise Time			410	830	ns
t _{d(off)}	Turn-Off Delay Time			20	50	ns
t _f	Turn-Off Fall Time		(Note 4)	140	290	ns
Q _g	Total Gate Charge	V _{DS} = 80 V, I _D = 19 A, V _{GS} = 5 V		14	18	nC
Q _{gs}	Gate-Source Charge			2.9		nC
Q _{gd}	Gate-Drain Charge		(Note 4)	9.2		nC
I _S	Maximum Continuous Drain-Source Diode Forward Current				15.6	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				62.4	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 15.6 A			1.5	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 19 A, dI _F / dt = 100 A/μs		80		ns
Q _{rr}	Reverse Recovery Charge			0.195		μC

Notes:

1. Repetitive rating : pulse-width limited by maximum junction temperature.
2. L = 1.35 mH, I_{AS} = 15.6 A, V_{DD} = 25 V, R_G = 25 Ω, starting T_J = 25°C.
3. I_{SD} ≤ 19 A, di/dt ≤ 300 A/μs, V_{DD} ≤ BV_{DSS}, starting T_J = 25°C.
4. Essentially independent of operating temperature.

Typical Characteristics

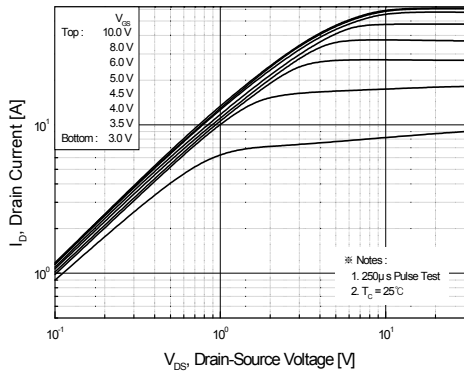


Figure 1. On-Region Characteristics

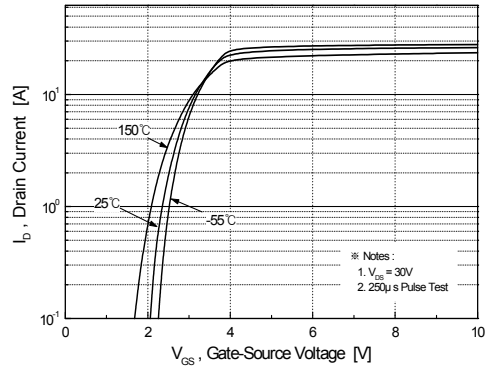


Figure 2. Transfer Characteristics

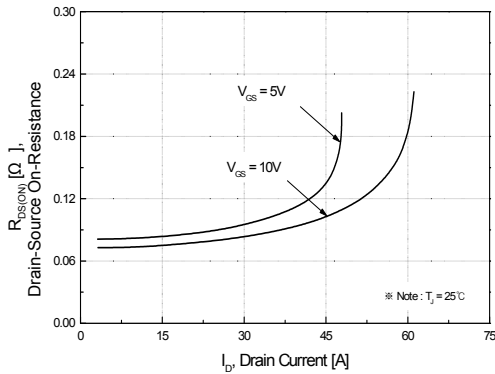


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

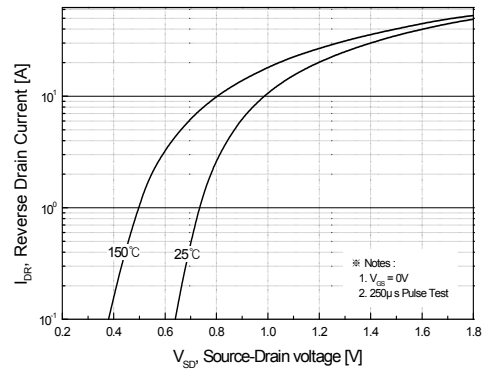


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

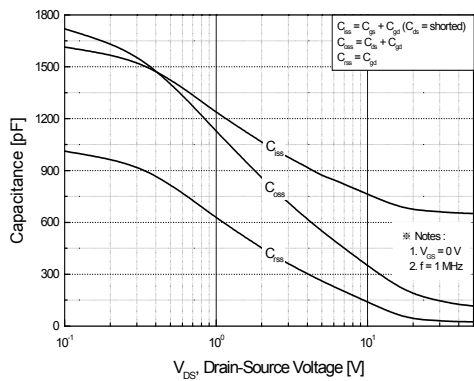


Figure 5. Capacitance Characteristics

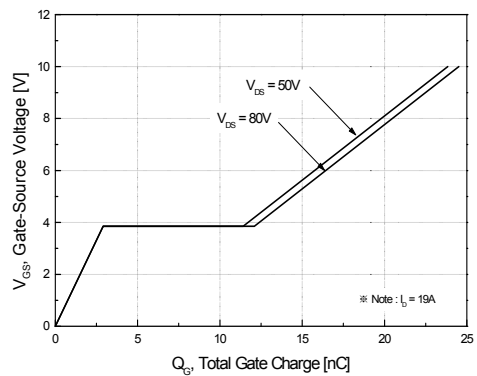


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

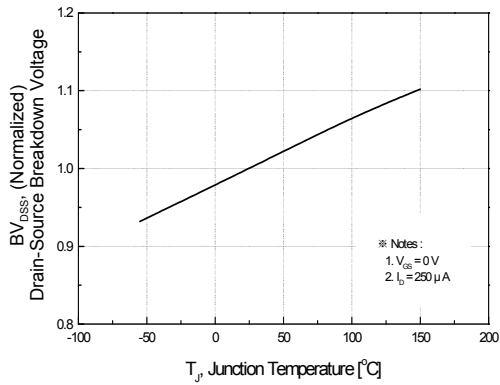


Figure 7. Breakdown Voltage Variation vs. Temperature

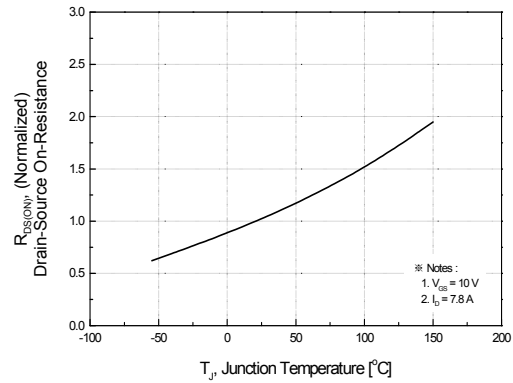


Figure 8. On-Resistance Variation vs. Temperature

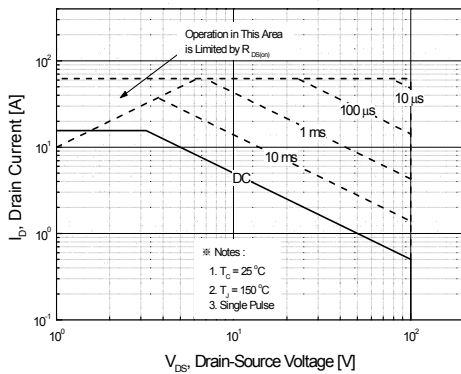


Figure 9. Maximum Safe Operating Area

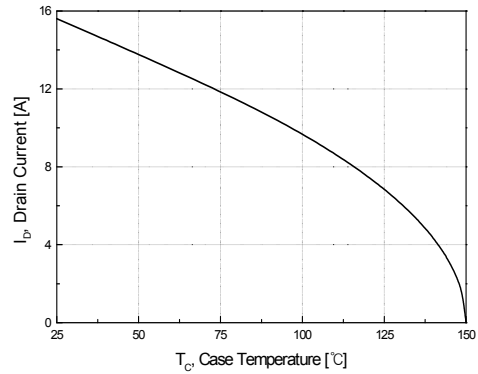


Figure 10. Maximum Drain Current vs. Case Temperature

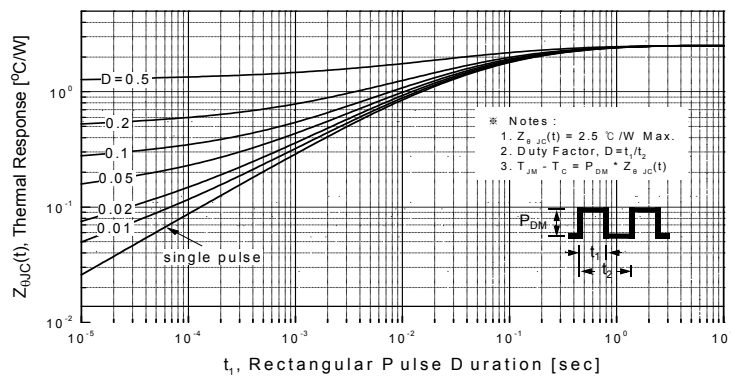
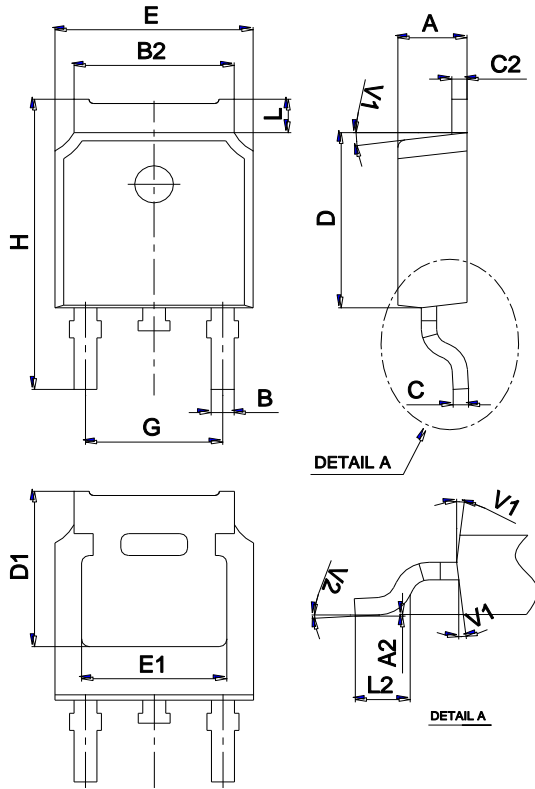


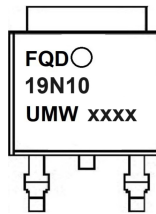
Figure 11. Transient Thermal Response Curve

Package Mechanical Data TO-252



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF			0.209REF		
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
H	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2	0°		6°	0°		6°

Marking



Ordering information

Order code	Package	Baseqty	Deliverymode
UMW FQD19N10LTM	TO-252	2500	Tape and reel

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[DMN2990UFB-7B](#) [SSM3K35CT,L3F](#) [IPLK60R1K0PFD7ATMA1](#) [2N7002W-G](#) [MCAC30N06Y-TP](#) [IPWS65R035CFD7AXKSA1](#)
[MCQ7328-TP](#) [SSM3J143TU,LXHF](#) [DMN12M3UCA6-7](#) [PJMF280N65E1_T0_00201](#) [PJMF380N65E1_T0_00201](#)
[PJMF280N60E1_T0_00201](#) [PJMF600N65E1_T0_00201](#) [PJMF900N65E1_T0_00201](#)