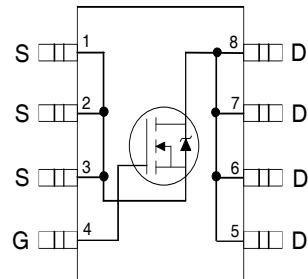


Description

The IRF8788TR has been optimized for parameters that are critical in synchronous buck operation including $R_{DS(on)}$ and gate charge to reduce both conduction and switching losses. The reduced total losses make this product ideal for high efficiency DC-DC converters that power the latest generation of processors for notebook and Netcom applications.



Top View

Benefits

- $V_{DS(V)} = 30V$
- $I_D = 24A$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 2.8m\Omega$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 3.8m\Omega$ ($V_{GS} = 4.5V$)
- Very Low Gate Charge
- Very Low $R_{DS(on)}$ at 4.5V V_{GS}
- Ultra-Low Gate Impedance
- Fully Characterized Avalanche Voltage and Current
- 20V V_{GS} Max. Gate Rating

Applications

- Synchronous MOSFET for Notebook Processor Power
- Synchronous Rectifier MOSFET for Isolated DC-DC Converters

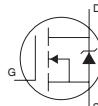
Absolute Maximum Ratings

	Parameter	Max.	Units
V_{DS}	Drain-to-Source Voltage	30	V
V_{GS}	Gate-to-Source Voltage	± 20	
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	24	
$I_D @ T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	19	A
I_{DM}	Pulsed Drain Current ①	190	
$P_D @ T_A = 25^\circ C$	Power Dissipation	2.5	
$P_D @ T_A = 70^\circ C$	Power Dissipation	1.6	W
	Linear Derating Factor	0.02	W/ $^\circ C$
T_J	Operating Junction and Storage Temperature Range	-55 to + 150	$^\circ C$
T_{STG}			

Thermal Resistance

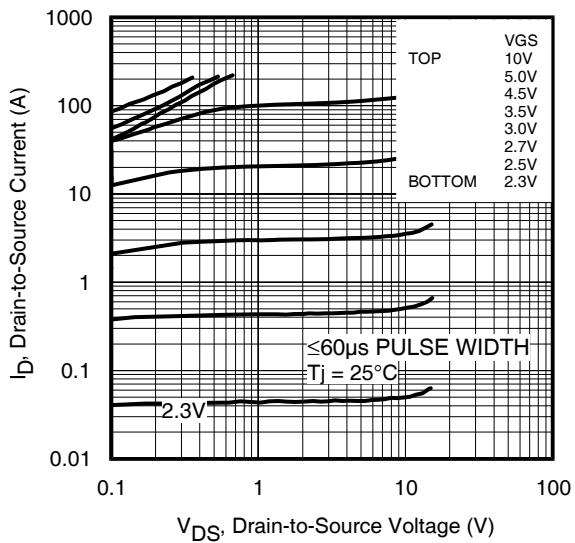
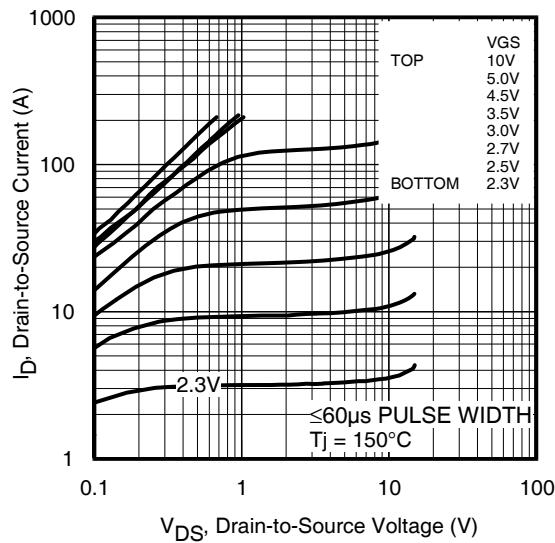
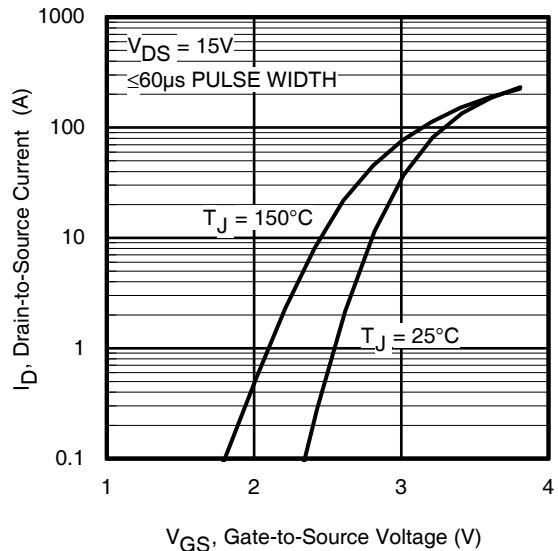
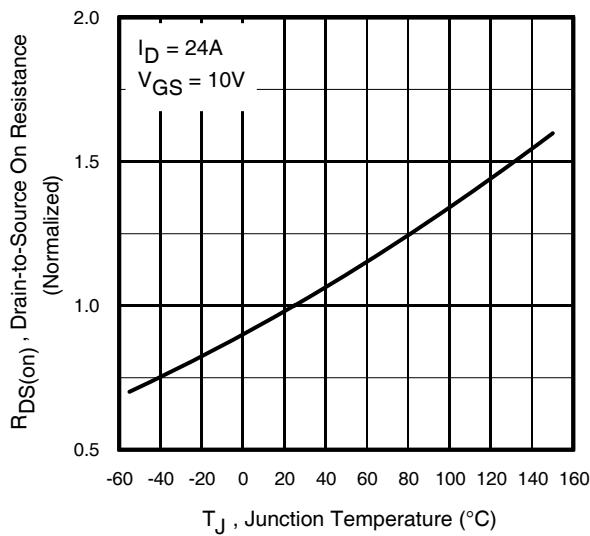
	Parameter	Typ.	Max.	Units
R_{0JL}	Junction-to-Drain Lead ⑤		20	$^\circ C/W$
R_{0JA}	Junction-to-Ambient ④⑤		50	

Static @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	30			V	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	Breakdown Voltage Temp. Coefficient		0.024		V/ $^\circ\text{C}$	Reference to $25^\circ\text{C}, I_D = 1\text{mA}$
$R_{\text{DS(on)}}$	Static Drain-to-Source On-Resistance	2.3	2.8		$\mu\Omega$	$V_{\text{GS}} = 10\text{V}, I_D = 24\text{A}$ ③
		3.04	3.8			$V_{\text{GS}} = 4.5\text{V}, I_D = 19\text{A}$ ③
$V_{\text{GS(th)}}$	Gate Threshold Voltage	1.35	1.80	2.35	V	$V_{\text{DS}} = V_{\text{GS}}, I_D = 100\mu\text{A}$
$\Delta V_{\text{GS(th)}}$	Gate Threshold Voltage Coefficient		-6.59		mV/ $^\circ\text{C}$	
I_{DSS}	Drain-to-Source Leakage Current		1.0		μA	$V_{\text{DS}} = 24\text{V}, V_{\text{GS}} = 0\text{V}$
			150			$V_{\text{DS}} = 24\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$
I_{GSS}	Gate-to-Source Forward Leakage		100		nA	$V_{\text{GS}} = 20\text{V}$
	Gate-to-Source Reverse Leakage		-100			$V_{\text{GS}} = -20\text{V}$
g_{fs}	Forward Transconductance	95			S	$V_{\text{DS}} = 15\text{V}, I_D = 19\text{A}$
Q_g	Total Gate Charge		44	66	nC	$V_{\text{DS}} = 15\text{V}$ $V_{\text{GS}} = 4.5\text{V}$ $I_D = 19\text{A}$ See Figs. 17a & 17b
$Q_{\text{gs}1}$	Pre-V _{th} Gate-to-Source Charge		12			
$Q_{\text{gs}2}$	Post-V _{th} Gate-to-Source Charge		4.7			
Q_{gd}	Gate-to-Drain Charge		14			
Q_{godr}	Gate Charge Overdrive		13.3			
Q_{sw}	Switch Charge ($Q_{\text{gs}2} + Q_{\text{gd}}$)		18.7			
Q_{oss}	Output Charge		22		nC	$V_{\text{DS}} = 16\text{V}, V_{\text{GS}} = 0\text{V}$
R_g	Gate Resistance		0.54	1.09	Ω	
$t_{\text{d(on)}}$	Turn-On Delay Time		23		ns	$V_{\text{DD}} = 15\text{V}, V_{\text{GS}} = 4.5\text{V}$ $I_D = 19\text{A}$ $R_G = 1.8\Omega$ See Fig. 15a & 15b
t_r	Rise Time		24			
$t_{\text{d(off)}}$	Turn-Off Delay Time		23			
t_f	Fall Time		11			
C_{iss}	Input Capacitance		5720		pF	$V_{\text{GS}} = 0\text{V}$ $V_{\text{DS}} = 15\text{V}$ $f = 1.0\text{MHz}$
C_{oss}	Output Capacitance		980			
C_{rss}	Reverse Transfer Capacitance		450			
I_S	Continuous Source Current (Body Diode)			3.1	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I_{SM}	Pulsed Source Current (Body Diode) ①			190	A	
V_{SD}	Diode Forward Voltage			1.0	V	$T_J = 25^\circ\text{C}, I_S = 19\text{A}, V_{\text{GS}} = 0\text{V}$ ③
				0.75	V	$T_J = 25^\circ\text{C}, I_S = 2.2\text{A}, V_{\text{GS}} = 0\text{V}$ ③
t_{rr}	Reverse Recovery Time		24	36	ns	$T_J = 25^\circ\text{C}, I_F = 19\text{A}, V_{\text{DD}} = 15\text{V}$ $dI/dt = 230\text{A}/\mu\text{s}$ ③
Q_{rr}	Reverse Recovery Charge		33	50	nC	
t_{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

Avalanche Characteristics

	Parameter	Typ.	Max.	Units
E_{AS}	Single Pulse Avalanche Energy ②		230	mJ
I_{AR}	Avalanche Current ①		19	A

**Fig 1.** Typical Output Characteristics**Fig 2.** Typical Output Characteristics**Fig 3.** Typical Transfer Characteristics**Fig 4.** Normalized On-Resistance vs. Temperature

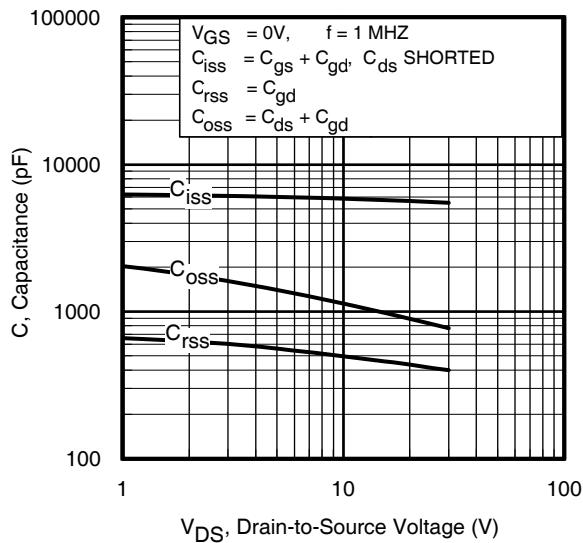


Fig 5. Typical Capacitance vs.
Drain-to-Source Voltage

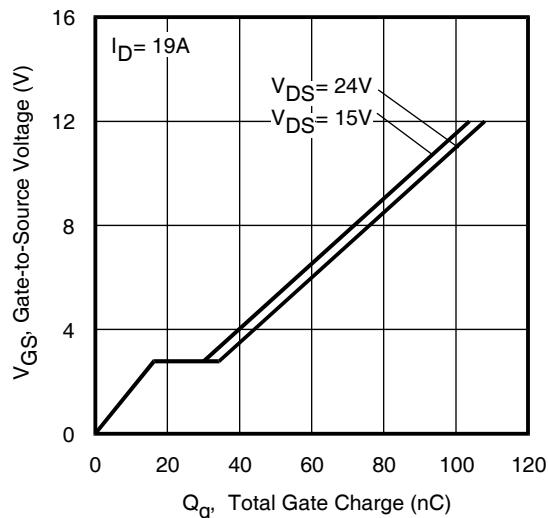


Fig 6. Typical Gate Charge vs.
Gate-to-Source Voltage

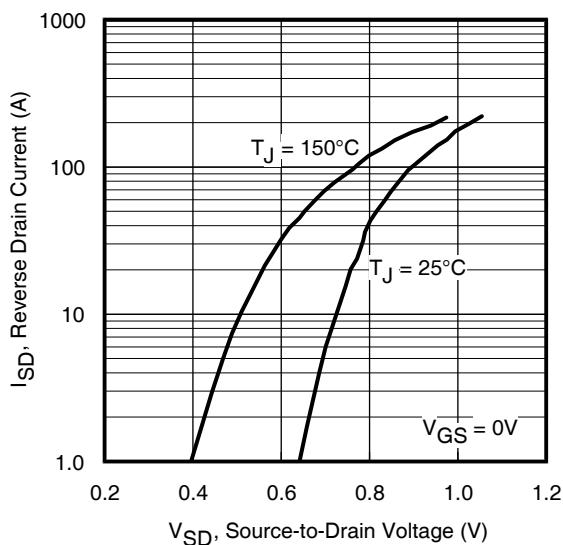


Fig 7. Typical Source-Drain Diode
Forward Voltage

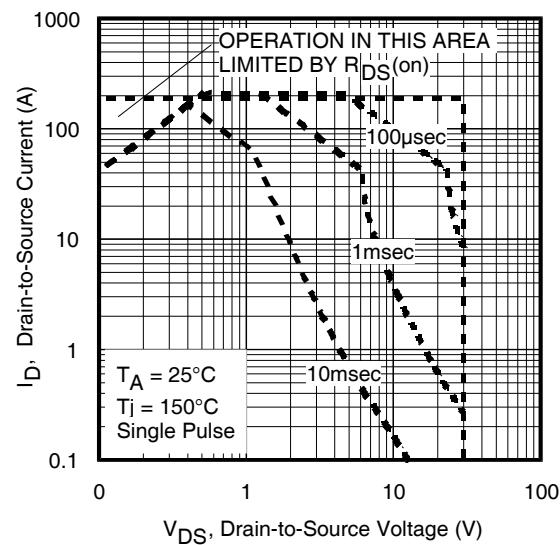


Fig 8. Maximum Safe Operating Area

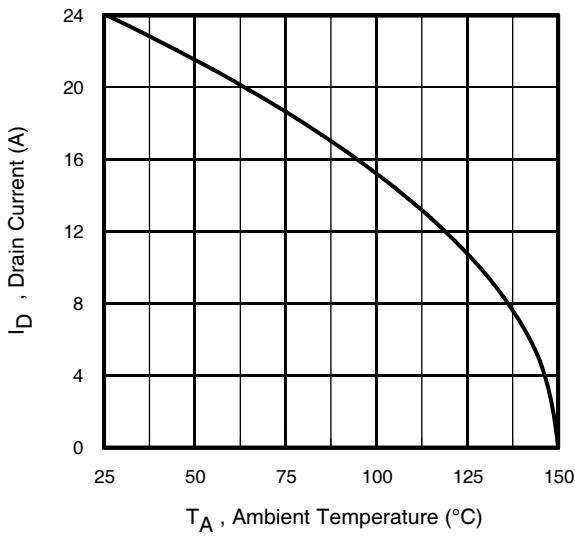


Fig 9. Maximum Drain Current vs.
Ambient Temperature

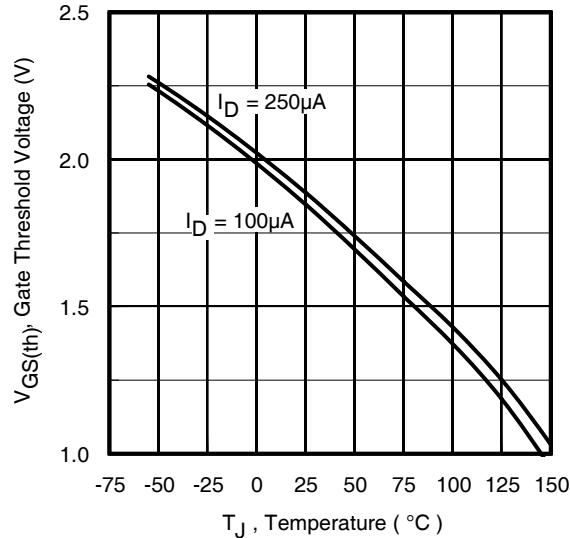


Fig 10. Threshold Voltage vs. Temperature

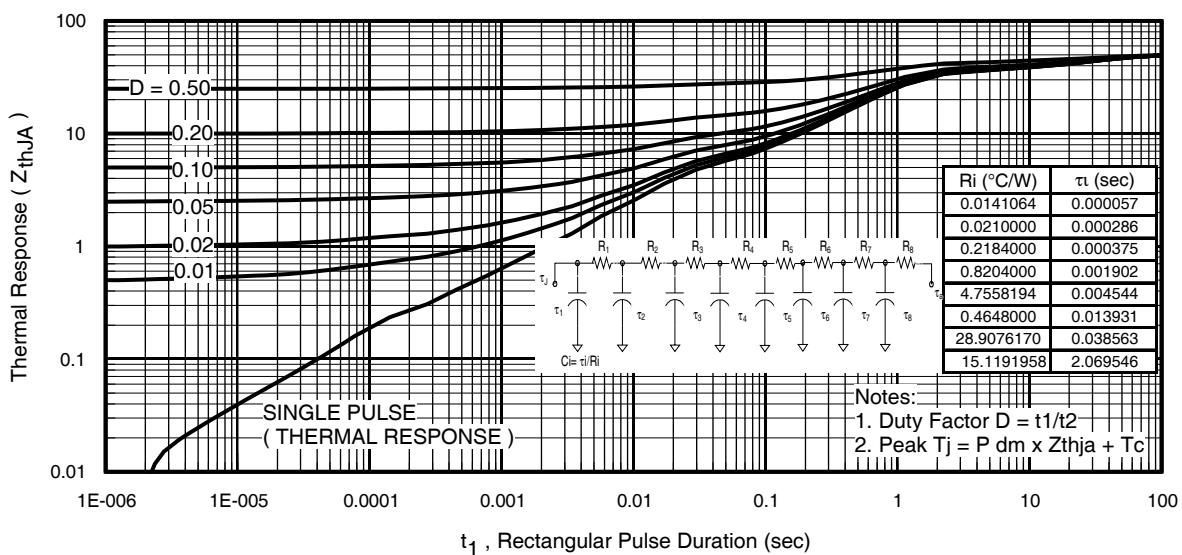


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

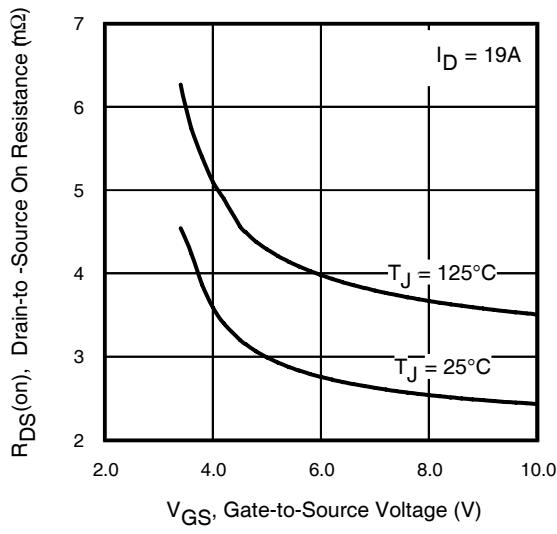


Fig 12. On-Resistance vs. Gate Voltage

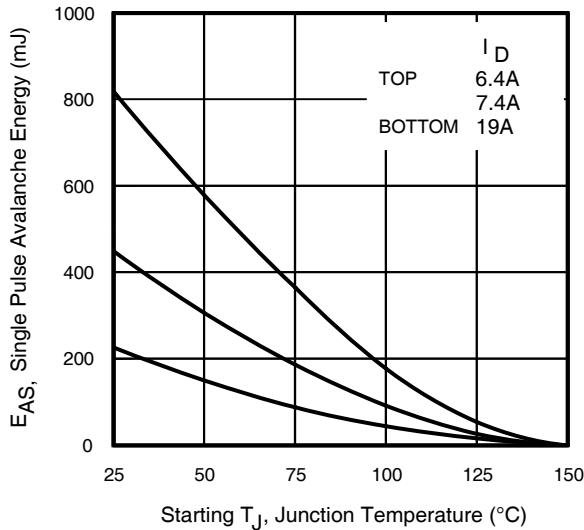
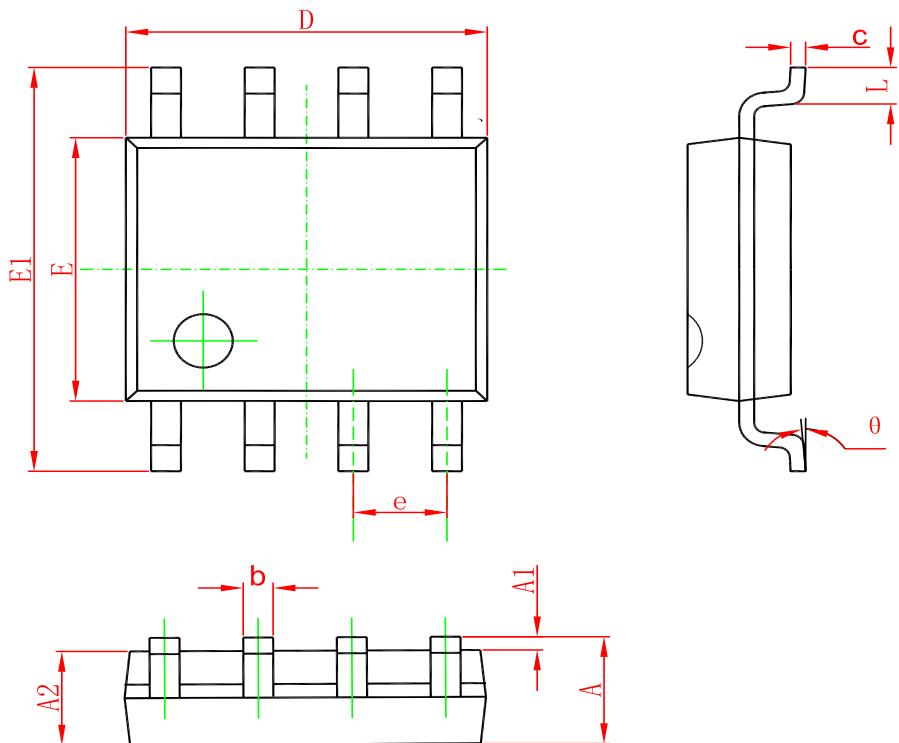


Fig 13. Maximum Avalanche Energy vs. Drain Current

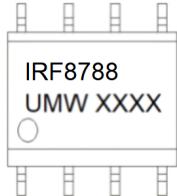
PACKAGE OUTLINE DIMENSIONS

SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

Marking



Ordering information

Order code	Package	Baseqty	Deliverymode
UMW IRF8788TR	SOP-8	3000	Tape and reel

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