

### Description

The TMP709 is a fully integrated, resistor-programmable temperature switch with a temperature threshold that is set by just one external resistor within the entire operating range. The TMP709 provides an open-drain, active-low output and has a 2.7V to 5.5V supply-voltage range. °C

The temperature threshold accuracy is typically  $\pm 0.5^{\circ}\text{C}$ , with a maximum of  $\pm 3^{\circ}\text{C}$  (60°C to 100°C). The quiescent current consumption is typically 33  $\mu\text{A}$ . Hysteresis is pin-selectable to 2°C or 10°C.

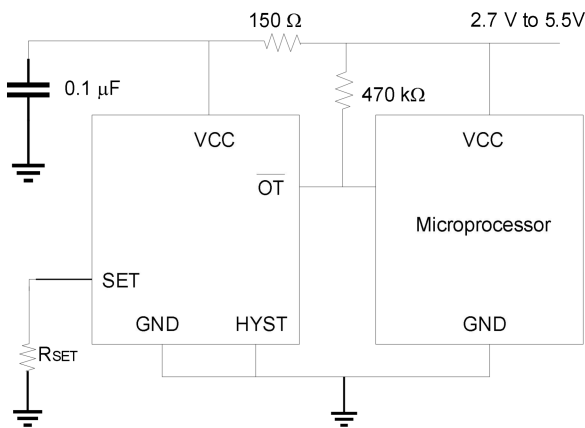
The TMP709 is available in a 5-pin SOT-23 and small DFN 6 package.

### Features

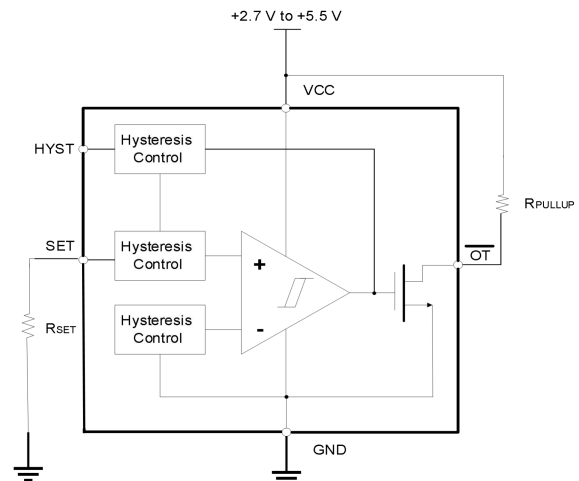
- Threshold accuracy:
  - $\pm 0.5^{\circ}\text{C}$  Typical
  - $\pm 3^{\circ}\text{C}$  Maximum (+ 60°C to 100°C)
- Temperature threshold set by 1% external resistor
- Low quiescent current: 33 $\mu\text{A}$  typical
- Open-drain, active-low output stage
- Pin-selectable 2°C or 10°C hysteresis
- Reset operation specified at VCC = 0.8V
- Power range: 2.7V to 5.5V
- Packaging: 5-pin SOT23, 6-pin DFN package

### Applications

- Computers (laptops and desktops)
- servers
- Industrial and medical equipment
- Storage Area Networks
- Automobiles

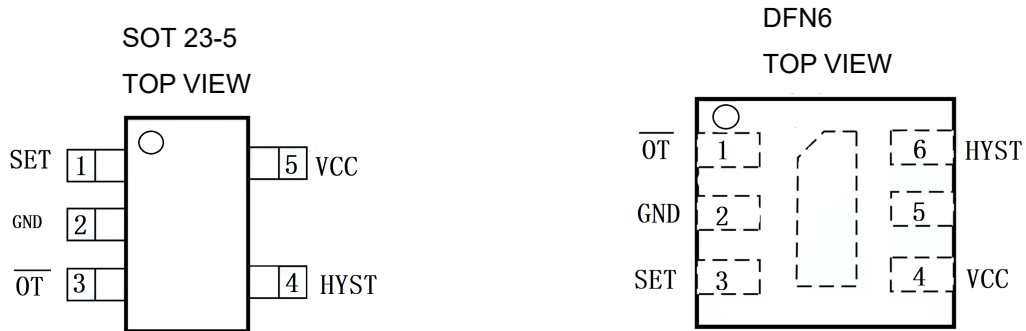


Typical Application



Chip Internal Structure Diagram

**Pin Configuration and Functions**



**Pin Functions**

PIN			DESCRIPTION
SOT 23-5	DFN6	NAME	
1	3	SET	Temperature set point. Connect an external 1% resistor between SET and GND.
2	2	GND	Device ground.
3	1	OT	Open-drain, active-low output.
4	6	HYST	Hysteresis selection. For 10°C, HYST = VCC; for 2°C, HYST = GND.
5	4	VCC	Supply voltage pin. The voltage range is 2.7V to 5.5V.
	5	NC	No connection.

**Absolute Maximum Ratings**

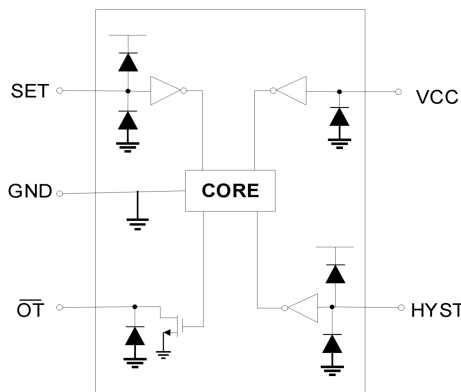
	MIN	MAX	UNIT
Supply Voltage VCC	-0.3	6	V
$\overline{\text{OT}}$ Pin Voltage	-0.3	6	V
HYST and SET Pin Voltage	-0.3	(Vcc+0.3)	V
Operating Temperature	-40	125	°C
Junction Temperature		150	°C
Storage Temperature	-65	150	°C

Unless otherwise noted, the specifications in the above table apply within the atmospheric temperature range.

Stresses beyond the range may cause permanent damage to the device.

**Electrostatic Protection**

		Value	UNIT
Electrostatic Discharge Voltage V <sub>ESD</sub>	Human-body Model (HBM)	5 000	V
	Charged-device Model (CDM)	2000	V
	Latch up Test	100	mA
	Machine Model (MM)	200	V



**TMP709 Internal ESD Equivalent Circuit**

**Recommended Operating Conditions**

	MIN	NOM	MAX	UNIT
Supply Voltage VCC	2.7	3.3	5.5	V
Operating Temperature T <sub>A</sub>	0		125	°C

Unless otherwise stated, the specifications in the above table apply within the atmospheric temperature range.

### Electrical Characteristics

Unless otherwise specified, electrical characteristics of devices at  $T_A = 0^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$  and  $V_{CC} = 2.7\text{V} \sim 5.5\text{V}$ .

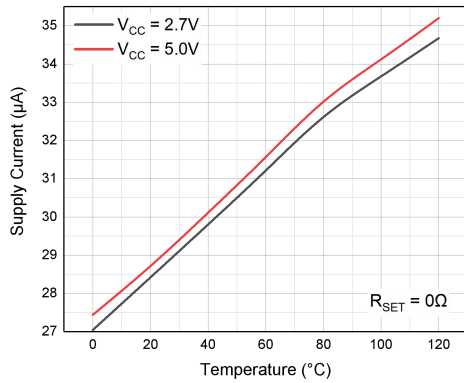
PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Power Supply					
$V_{CC}$ Supply voltage range		2.7		5.5	V
$I_{CC}$ Supply current	$V_{CC} = 5\text{V}$		3.3	55	$\mu\text{A}$
	$V_{CC} = 2.7\text{V}$		3.3	55	$\mu\text{A}$
Temperature					
$T_E$ Temperature error	$T_A = +6.0^{\circ}\text{C}$ to $+100^{\circ}\text{C}$		$\pm 0.5$	$\pm 3$	$^{\circ}\text{C}$
Digital input (HYST)					
$V_{IH}$ High level input voltage		$0.7 \times V_{CC}$			V
$V_{IL}$ Low level input voltage				$0.3 \times V_{CC}$	V
$C_{IN}$ Input capacitance			10		pF
Analog input (SET)					
$V_{IN}$ Input voltage range		0		$V_{CC}$	V
$I_{lkg\_in}$ Input leakage current			1		$\mu\text{A}$
Digital open-drain output (OT)					
$I_{(OT\_SINK)}$ Output sink current	$V_{OT} = 0.3\text{V}$	5	12		mA
$I_{lkg(OT)}$ Output leakage current	$V_{OT} = V_{CC}$		1		$\mu\text{A}$

### Thermal Information

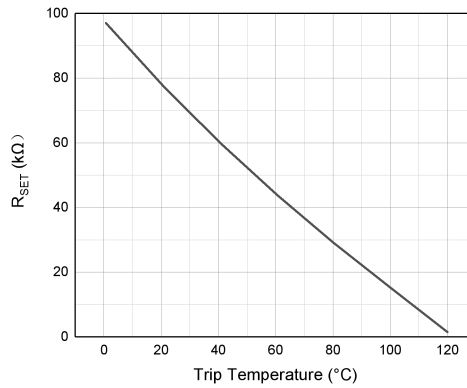
THERMAL METRIC		TMP709	UNIT
		DBV(SOT23)	
		5 PINS	
$\theta_{JA}$	Junction to ambient thermal resistance	217.9	$^{\circ}\text{C}/\text{W}$
$\theta_{JCtop}$	Junction to chip case (top) thermal resistance	86.3	
$\theta_{JB}$	Junction to board thermal resistance	44.6	
$\psi_{JT}$	Junction to top characterization parameters	4.4	
$\psi_{JB}$	Junction to circuit board characterization parameters	43.8	
$\theta_{JCbo}$	Junction to chip case (bottom) thermal resistance	unavailable	

### Typical Characteristics

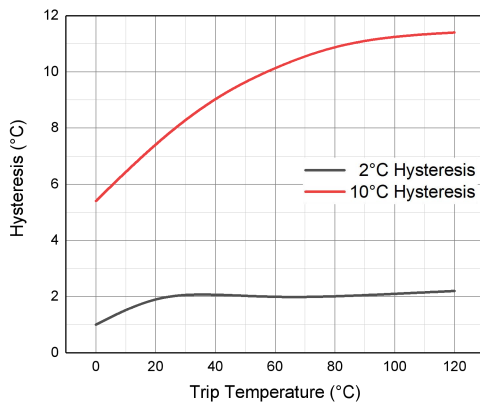
At  $T_A = +25^\circ\text{C}$  and  $V_{CC} = 2.7\text{ V to } 5.5\text{ V}$  (unless otherwise noted).



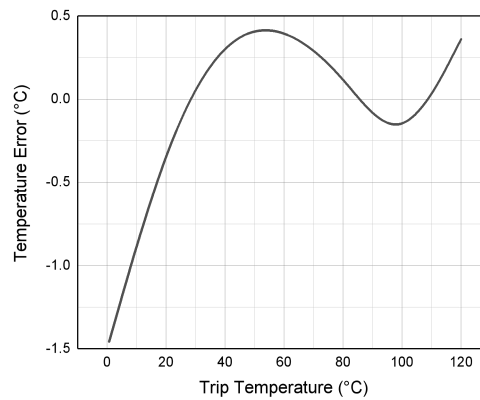
Supply Current vs Temperature



Rset vs Trip Temperature



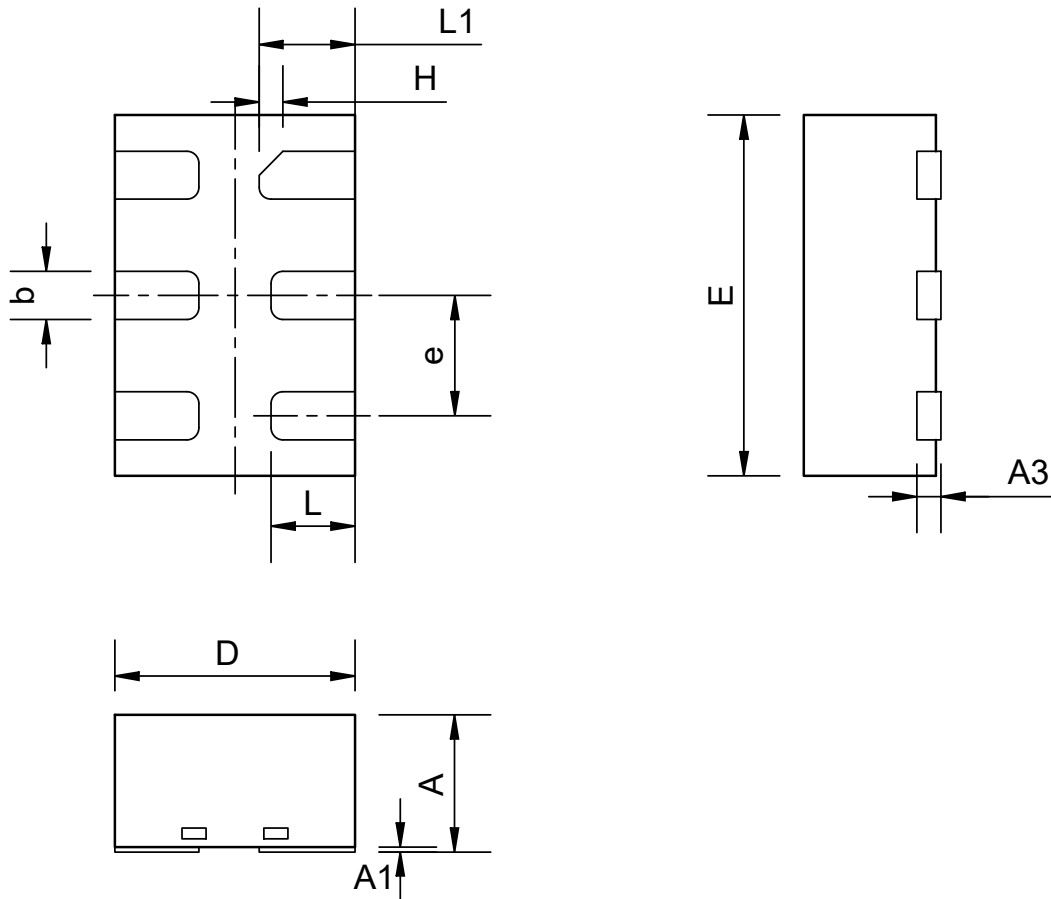
Hysteresis vs. Trip Temperature



Temperature Error vs Trip Temperature



DFN6(1.0×1.5)



COMMON DIMENSIONS  
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	0.50	--	0.60
A1	0	0.02	0.05
A3	0.10REF		
b	0.15	0.20	0.25
D	0.90	1.00	1.10
E	1.40	1.50	1.60
e	0.40	0.50	0.60
H	0.10REF		
L	0.30	0.35	0.40
L1	0.35	0.40	0.45

**Ordering information**

Order code	Package	Baseqty	Deliverymode	Marking
UMW TMP709AIDBVR	SOT23-5	3000	Tape and reel	SBJ U
UMW TMP709D	DFN6	5000	Tape and reel	SBJ U



## X-ON Electronics

Largest Supplier of Electrical and Electronic Components

*Click to view similar products for [Thermal Cut-offs](#) category:*

*Click to view products by [Youtai](#) manufacturer:*

Other Similar products are found below :

[CR-200W](#) [D070-002](#) [HC77AY-B](#) [SDF DF192S](#) [SDF DF240S](#) [SDF DF152S](#) [TT125](#) [TT86](#) [TMP709AIDBVR\(UMW\)](#) [X2](#) [Y1](#) [F18](#) [Q136](#) [C7](#)  
[RS257](#) [H18](#) [R7](#) [TT102](#) [B13](#) [X3](#) [C32](#) [S136](#) [X8](#) [RS104](#) [RS117](#) [RT121](#) [U2](#) [RT167](#) [SDF DF144S](#) [HP136](#) [K5](#) [RT117](#) [H7](#) [RS128](#) [H5](#) [RT99](#)  
[RT128](#) [X32](#) [RS72](#) [R3](#) [H6](#) [RS121](#) [RS152](#) [R6](#) [S150](#) [TC130](#) [K1](#) [F1](#) [S102](#) [K3](#)