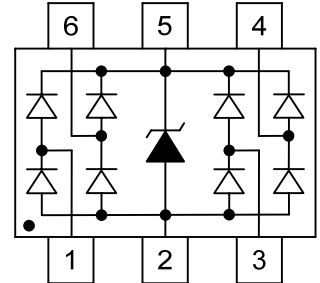


Description

UDT26A05L05 is surge rated diode arrays designed to protect high speed data interfaces. It has been specifically designed to protect sensitive components which is connected to data and transmission lines from overvoltage caused by electrostatic discharge (ESD), electrical fast transients (EFT), and lightning.

The unique design of the device incorporates one surge rated, and four data lines. Low capacitance steering diodes and a TVS diode in a single package. The low capacitance array configuration allows the user to protect four high speed data or transmission lines.



Features

- IEC61000-4-2 ESD 15KV Air, 8KV contact compliance
- SOT23-6L surface mount package
- Protects four high-speed data lines and one power line
- Array of surge rated, low capacitance diodes
- Working voltage: 5V
- Low leakage current
- Low clamping voltage
- Solder reflow temperature: Pure Tin-Sn, 260~270°C

Applications

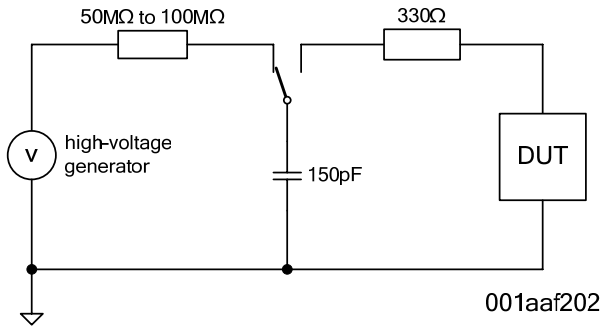
- USB power and data line protection
- 10/100/1000 Ethernet
- Video line protection
- I²C bus protection
- WAN/LAN equipment
- ISDN S/T interface
- Microcontroller input protection
- Portable electronics

Maximum Ratings

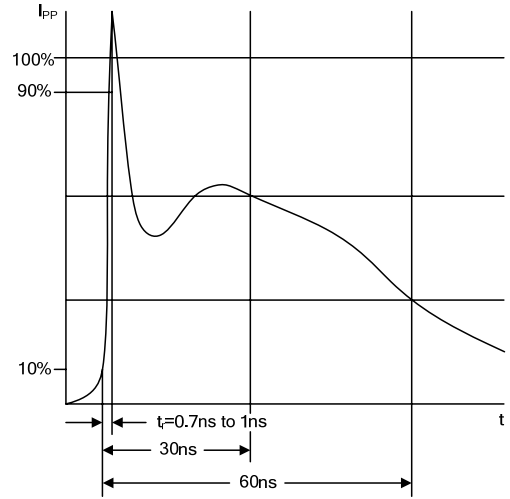
Rating	Symbol	Value	Unit
ESD voltage (Contact discharge)	V _{ESD}	±8	kV
ESD voltage (Air discharge)		±15	
Storage & operating temperature range	T _{STG} , T _J	-55~+150	°C

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse stand-off voltage	V_{RWM}				5	V
Reverse breakdown voltage	V_{BR}	$I_{BR}=1mA$	6			V
Reverse leakage current	I_R	$V_R=5V$			5	μA
Clamping voltage ($t_p=8/20\mu s$)	V_C	$I_{PP}=1A$			9.8	V
Clamping voltage ($t_p=8/20\mu s$)	V_C	$I_{PP}=2A$			16	V
Off state junction capacitance	C_J	0Vdc, f=1MHz Between I/O pins and GND		2.5		pF

IEC61000-4-2



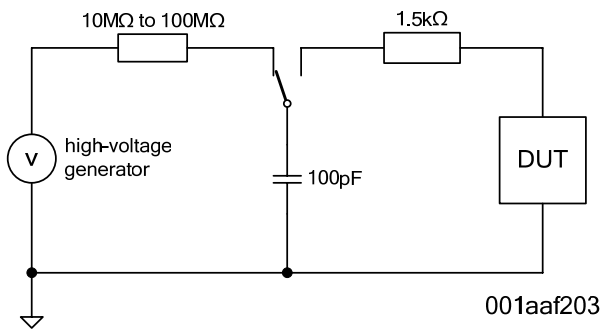
Test circuit according IEC61000-4-2



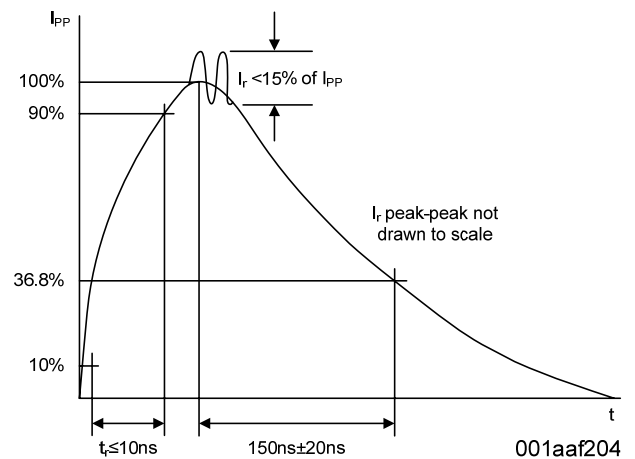
ESD surge according IEC61000- -

Human Body Model (HBM, MIL-883E method 3015.7)

The HBM standard simulates an ESD surge generated by human contact to electronic components.

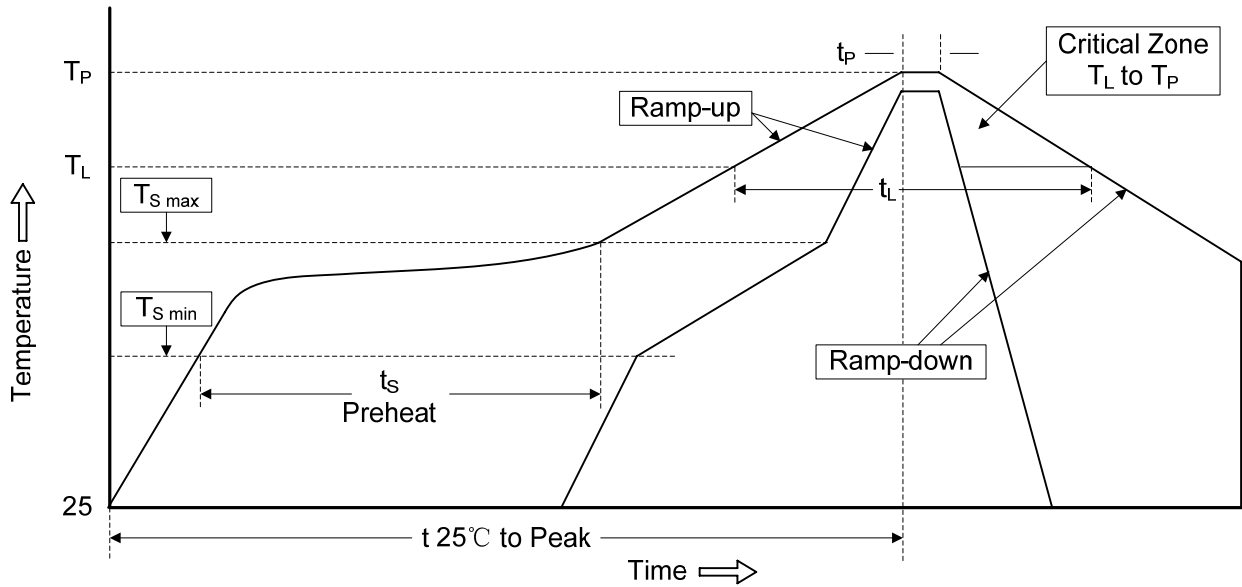


Test circuit according to MIL-883E method 3015.7



ESD surge according to MIL-883E method 3015.7

Reflow Soldering



Recommended Conditions

Profile Feature	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	3°C/second max.
Preheat -Temperature Min ($T_{S\ min}$) -Temperature Max ($T_{S\ max}$) -Time (min to max) (t_s)	150°C 200°C 60-180 seconds
$T_{S\ max}$ to T_L -Ramp-up Rate	3°C/second max.
Time maintained above: -Temperature (T_L) -Time (t_L)	217°C 60-150 seconds
Peak Temperature (T_P)	260°C
Time within 5°C of actual Peak Temperature (t_p)	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

SOT23-6

Marking

Ordering information

Order code	Package	Base qty	Delivery mode
UMW UDT26A05L05UL	SOT23-6	3000	Tape and reel

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[P6KE13CA](#) [P6KE43CA](#) [P6KE6.8CA](#) [P6KE8.2](#) [P6SMBJ20CA](#) [JANTX1N6072A](#) [SR2835ESKG](#) [SA90CA](#)