

TIP127

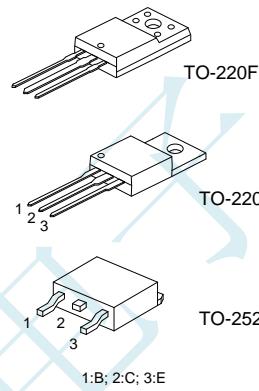
达林顿三极管(PNP)

PNP EPITAXIAL DARLINGTON TRANSISTOR

特性 FEATURES

与TIP122互补
Complementary to TIP122

应用于中功率线性开关
Medium Power Linear Switching Applications



产品订购信息

产品名称 Ordering Number	封装形式 Package	打印名称 Print Number	环保等级 Free	包装 Packing
TIP127-TA3-L-T	TO-220	TIP127	RoHS	料管
TIP127-TNB-L-R	TO-252	TIP127	RoHS	编带
TIP127-TF3-L-T	TO-220F	TIP127	RoHS	料管

TIP127- TA3 - L - R

包装类型: 料管(Tube)T, 卷盘编带(Tape Reel)R,
防静电袋(Bulk) K, T092编带(Tape Box)B
绿色包装: 如无卤无铅G, 无铅L
封装外形: TA3:TO-220; TNB:TO-252;
TF3:TO-220F
产品型号



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绝对最大额定值 MAXIMUM RATINGS (Ta=25°C unless otherwise noted)

项目 Parameter	符号 Symbol	数值 Rating	单位 Unit
集电极、基极击穿电压 Collector-Base Voltage	V _{CBO}	100	V
集电极、发射极击穿电压 Collector-Emitter Voltage	V _{CEO}	100	V
发射极、基极击穿电压 Emitter-Base Voltage	V _{EBO}	5	V
集电极额定电流 Collector Current -Continuous	I _C	5	A
集电极耗散功率 Collector Power Dissipation	P _C	2	W
结到外壳的热阻 (TO-220) Thermal Resistance From Junction To Ambient	R _{θ JA}	62.5	°C/W
结到管壳的热阻 (TO-220) Thermal Resistance Junction to Case	R _{θ JA}	1.92	°C/W
最高结温 Junction Temperature	T _J	150	°C
贮存温度 Storage Temperature Range	T _{stg}	-55to+150	°C

电特性 ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

项目 Parameter	符号 Symbol	测试条件 Test conditions	最小值 Min	最大值 Max.	单位 Unit
集电极、基极击穿电压 Collector-base breakdown voltage	V _{(BR)CBO}	I _C =1mA,I _E =0	100		V
集电极、发射极击穿电压 Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C =30mA,I _B =0	100		V
集电极漏电流 Collector cut-off current	I _{CBO}	V _{CB} =100V,I _E =0		0.2	mA
集电极、发射极漏电流 Collector cut-off current	I _{CEO}	V _{CE} =50V,I _E =0		0.5	mA
发射极漏电流 Emitter cut-off current	I _{EBO}	V _{EB} =5V,I _C =0		2	mA
直流电流增益 DC current gain	hFE(1)	V _{CE} =3V,I _C =0.5A	1000		
	hFE(2)	V _{CE} =3V,I _C =3A	1000		
集电极、发射极饱和压降 Collector-emitter saturation voltage	V _{CE(sat)}	I _C =3A,I _B =12mA I _C =5A,I _B =20mA		2 4	V
基极、发射极开启电压 Base-emitter voltage	V _{BE}	V _{CE} =3V,I _C =3A		2.5	V
集电极输出电容 Collector output capacitance	C _{ob}	V _{CB} =10V,I _C =0,f=0.1MHz		300	PF



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电参数曲线图 Electrical Characteristic Curve

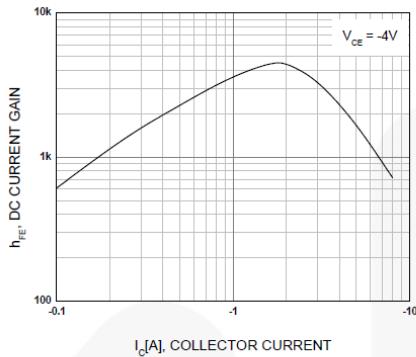


Figure 1. DC Current Gain

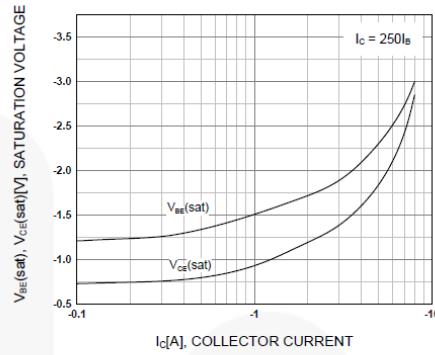


Figure 2. Base-Emitter Saturation Voltage and Collector-Emitter Saturation Voltage

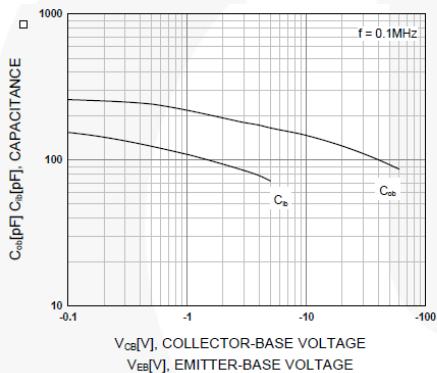


Figure 3. Output and Input Capacitance vs. Reverse Voltage

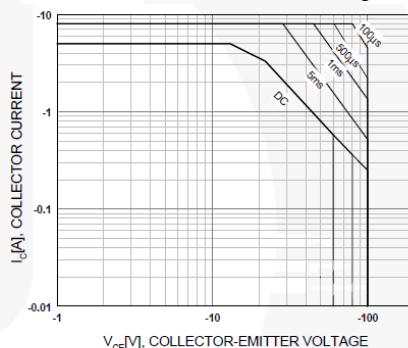


Figure 4. Safe Operating Area

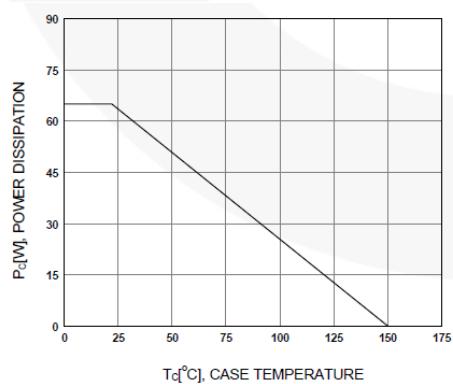


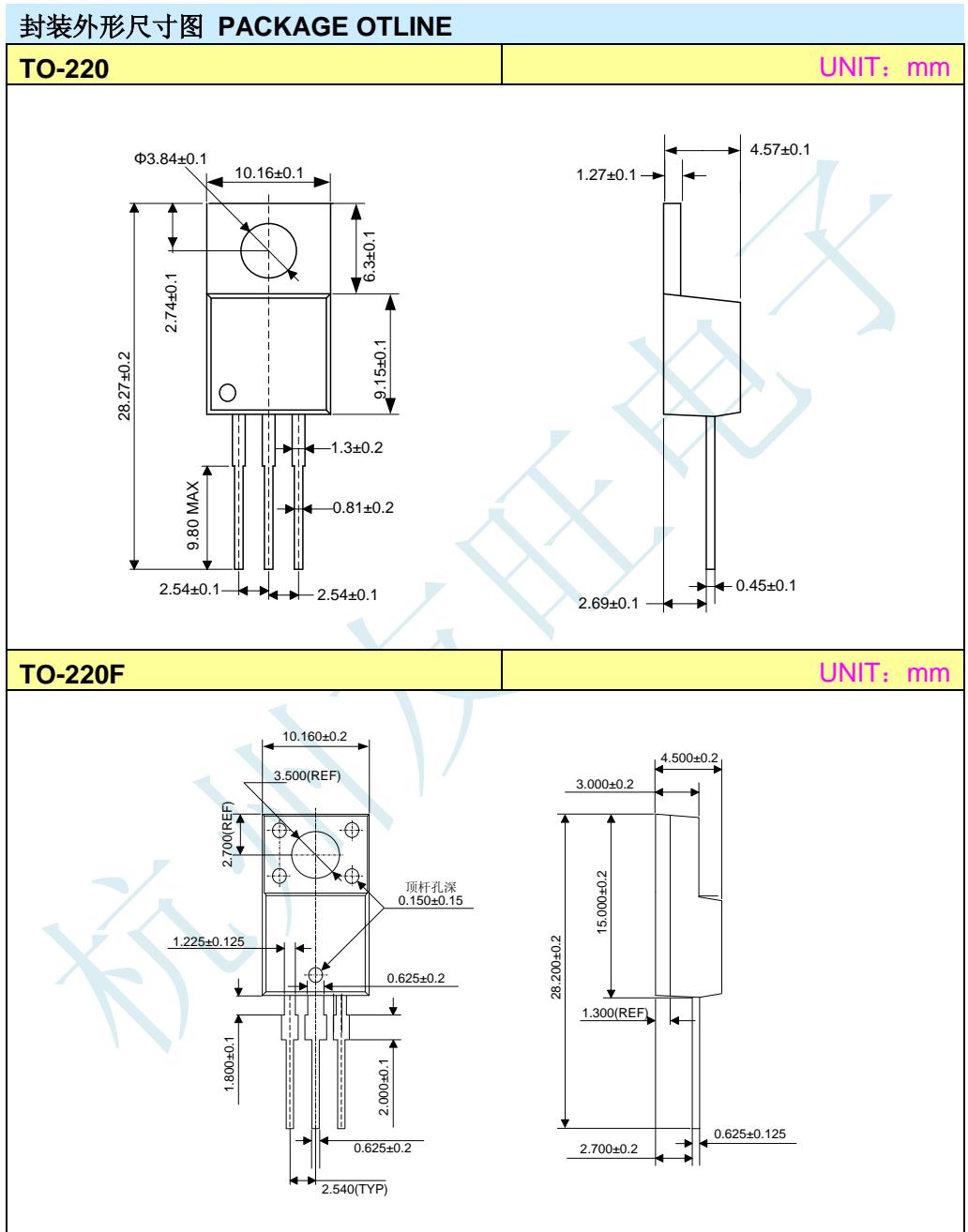
Figure 5. Power Derating



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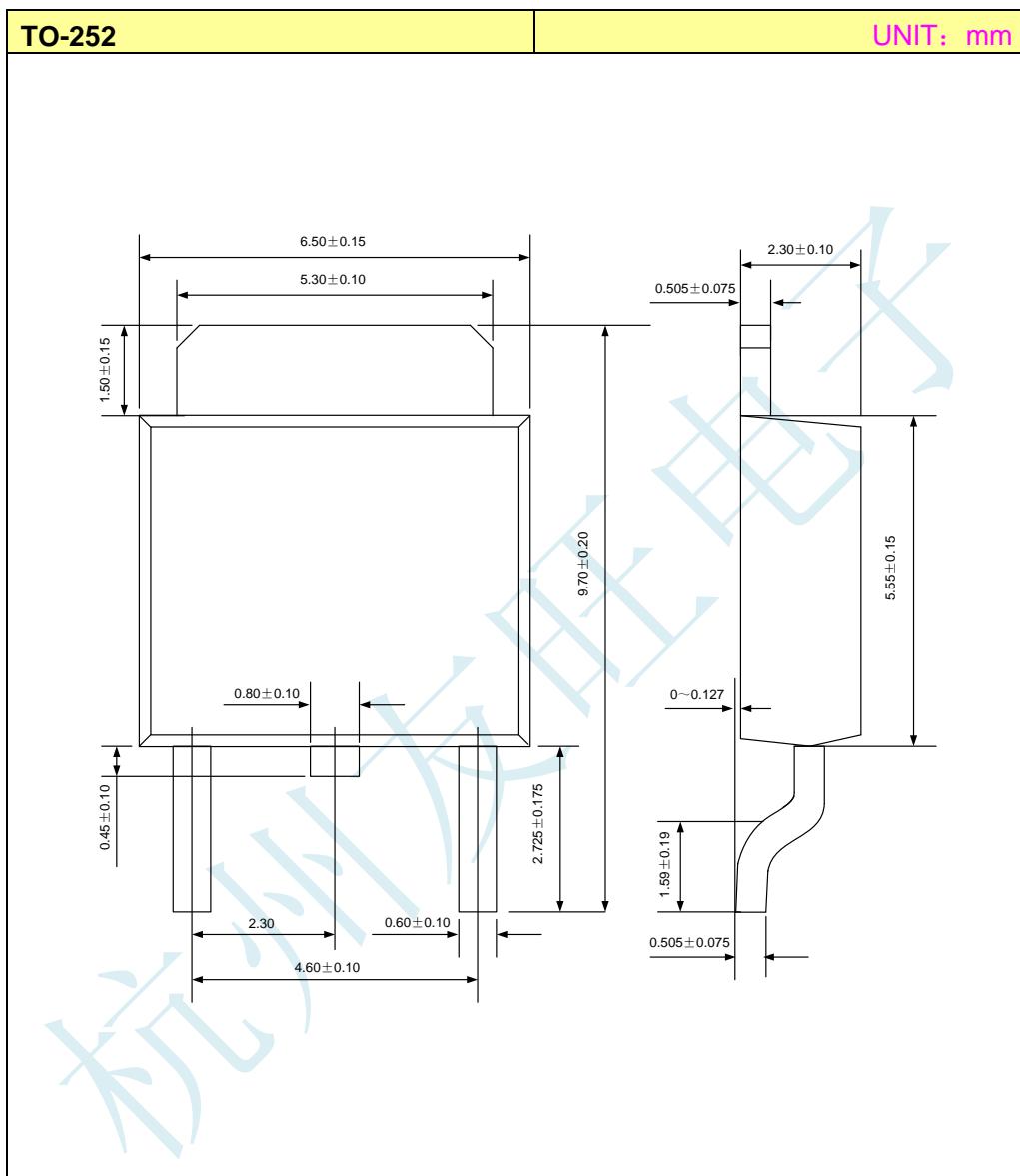
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防静电注意事项 ELECTROSTATIC DISCHARGE CAUTION



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage handling to prevent electrostatic damage to the device.

产品的内置ESD防护有限。产品存储时，应做好静电防护，以防静电对电路造成损坏。

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