

DUAL OPERATIONAL AMPLIFIERS

DESCRIPTION

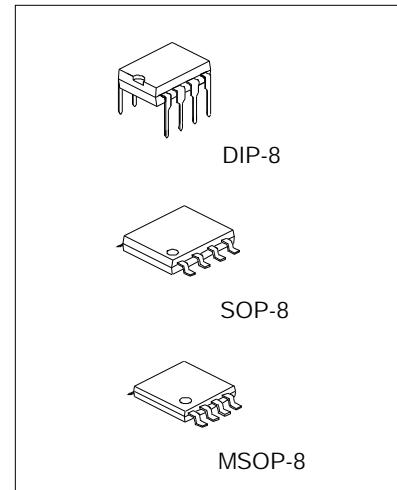
The UTC358 consists of dual independent, high gain internally frequency compensated operational amplifiers which were designed specifically to operate from a single power supply over a wide voltage range.

Operation from split power supplies is also possible so long as the difference between the two supplies 3 volts to 32 volts.

Application areas include transducer amplifier, DC gain blocks and all the conventional OP amp circuits which now can be easily implemented in single power supply system.

FEATURES

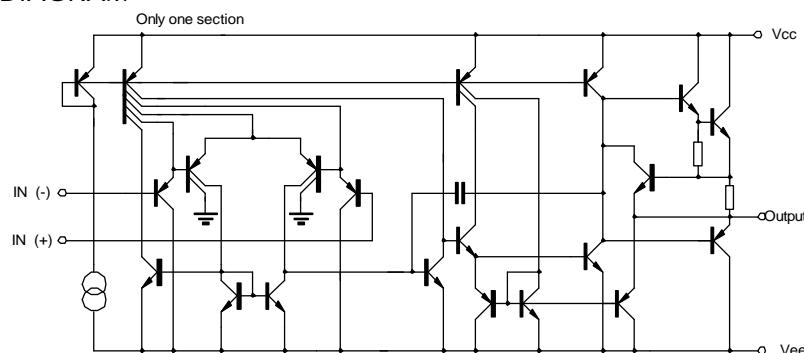
- *Internally frequency compensated for unity gain
- *Large DC voltage gain :100dB
- *Wide operating supply range($V_{cc}=3V\sim 32V$)
- *Input common-mode voltage includes ground
- *Large output voltage swing:From 0V to $V_{cc}-1.5V$
- *Power drain suitable for battery operation



ORDERING INFORMATION

Device	Package
UTC358D	DIP-8-300-2.54
UTC358	SOP-8-225-1.27
UTC358N	MSOP-8-170-0.65

BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

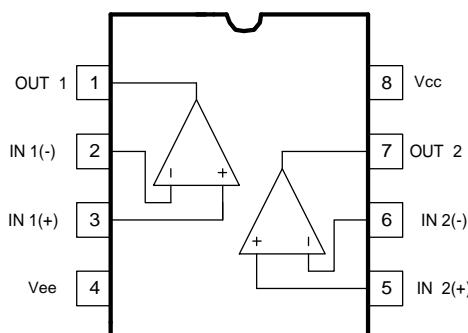
Characteristic	Symbol	Value	Unit
Supply Voltage	Vcc	± 18	V
Differential input voltage	Vi(DIFF)	32	V
Input Voltage	Vi	-0.3~32V	V
Power Dissipation	Pd	570	mW
Operating Temperature	Topr	0 to +70	°C
Storage Temperature	Tstg	-65 to 150	°C

ELECTRICAL CHARACTERISTICS($T_a=25^\circ\text{C}$)

(Vcc=5.0V, All voltage referenced to GND unless otherwise specified)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Input offset voltage	Vi _o	$V_{CM}=0$ to $V_{CC}-1.5$ $V_{O(p)}=1.4\text{V}, R_s=0$		2.9	7.0	mV
Input offset current	I _{i0}			5.0	50	nA
Input Bias current	I _b			45	250	nA
Input Common-mode voltage range	Vi(R)	$V_{CC}=30\text{V}$	0	$V_{CC}-1.5$		V
Supply Current	I _{cc}	$R_L=\infty, V_{CC}=30\text{V}$ $V_{CC}=5\text{V}$		0.8	2.0	mA
				0.5	1.2	mA
Large signal Voltage Gain	G _v	$V_{CC}=15\text{V}, R_L>2\text{k}\Omega$ $V_{O(p)}=1\text{V}$ to 11V	25	100		V/mV
Output voltage Swing	V(OH)	$V_{CC}=30\text{V}, R_L=2\text{k}\Omega$	26			V
		$V_{CC}=30\text{V}, R_L=10\text{k}\Omega$	27	28		V
V(OL)		$V_{CC}=5, R_L>10\text{k}\Omega$		5	20	mV
Common-mode rejection Ratio	CMRR		65	75		dB
Power supply rejection Ratio	PSRR		65	100		dB
Chanel Separation	CS	f=1kHz to 20kHz		5	20	mV
Short circuit to GND	I _{sc}			40	60	mA
Output current	I _{source}	$V_{I(+)}=1\text{V}, V_{I(-)}=0$ $V_{CC}=15\text{V}, V_{O(p)}=2\text{V}$	20	40		mA
	I _{sink}	$V_{I(+)}=0\text{V}, V_{I(-)}=1\text{V}$ $V_{CC}=15\text{V}, V_{O(p)}=2\text{V}$	10	13		mA
		$V_{I(+)}=1\text{V}, V_{I(-)}=0$ $V_{CC}=15\text{V}, V_{O(p)}=200\text{V}$	12	45		μA
Differential input voltage	Vi(DIFF)				Vcc	V

PIN CONFIGURATION



Any Class-1 or Class-2 Ozone depleting substances are not used in this component or its manufacturing. (As per defined under section 611 of the clean air act)

TYPICAL CHARACTERISTICS PERFORMANCE

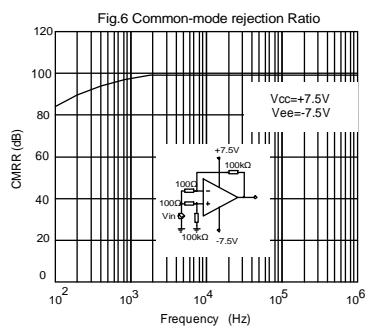
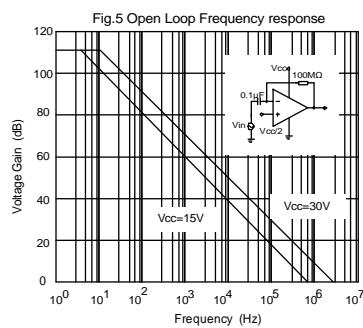
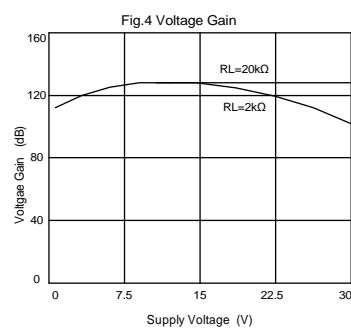
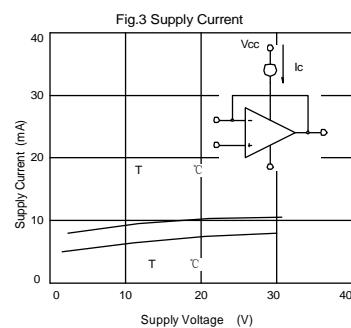
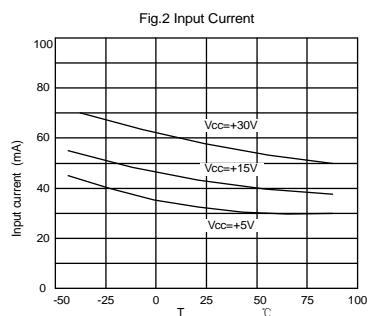
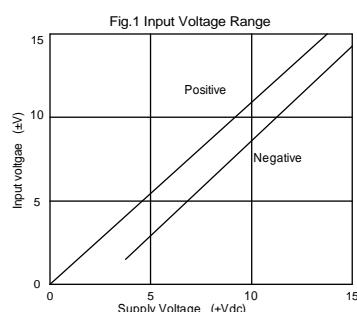


Fig.7

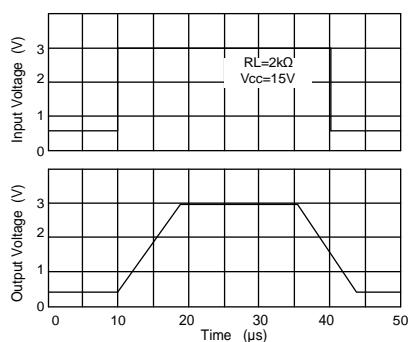


Fig.8 voltage Follower pulse response (small signal)

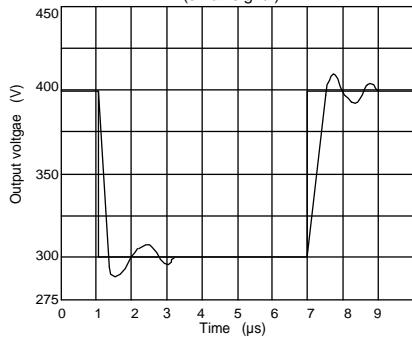


Fig.9 Large signal Frequency Response

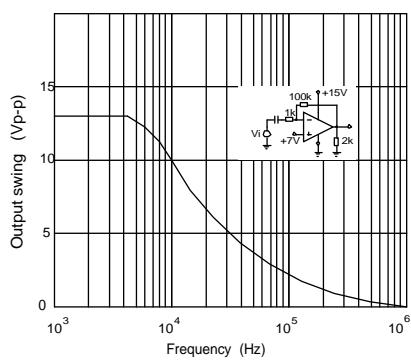


Fig.10 Output Characteristics current sourcing

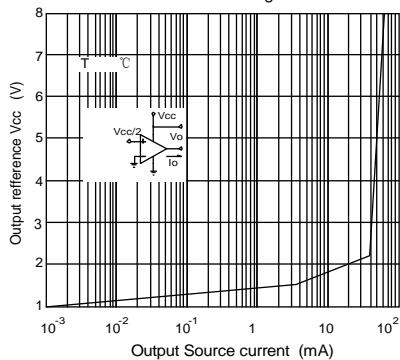


Fig.11 Output Characteristics Current sinking

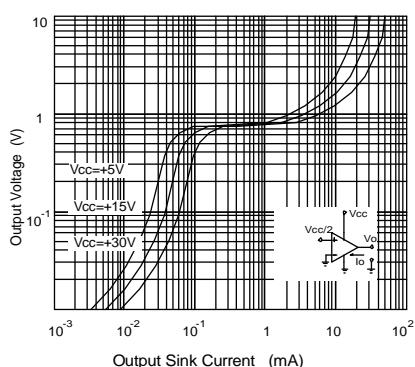
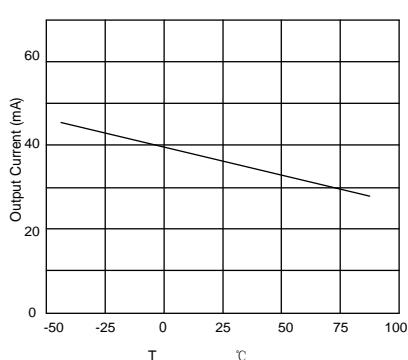


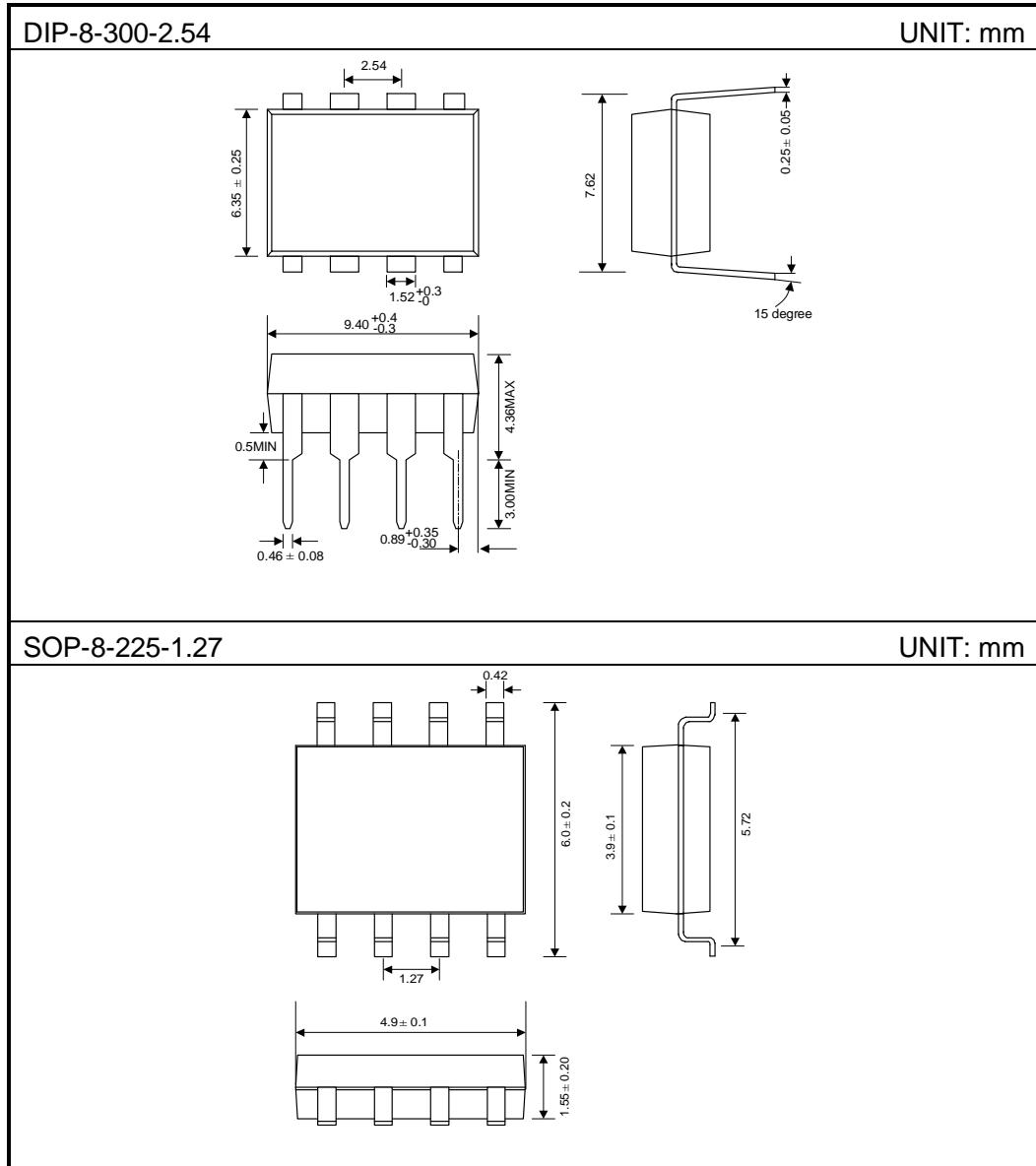
Fig.12 Current Limiting



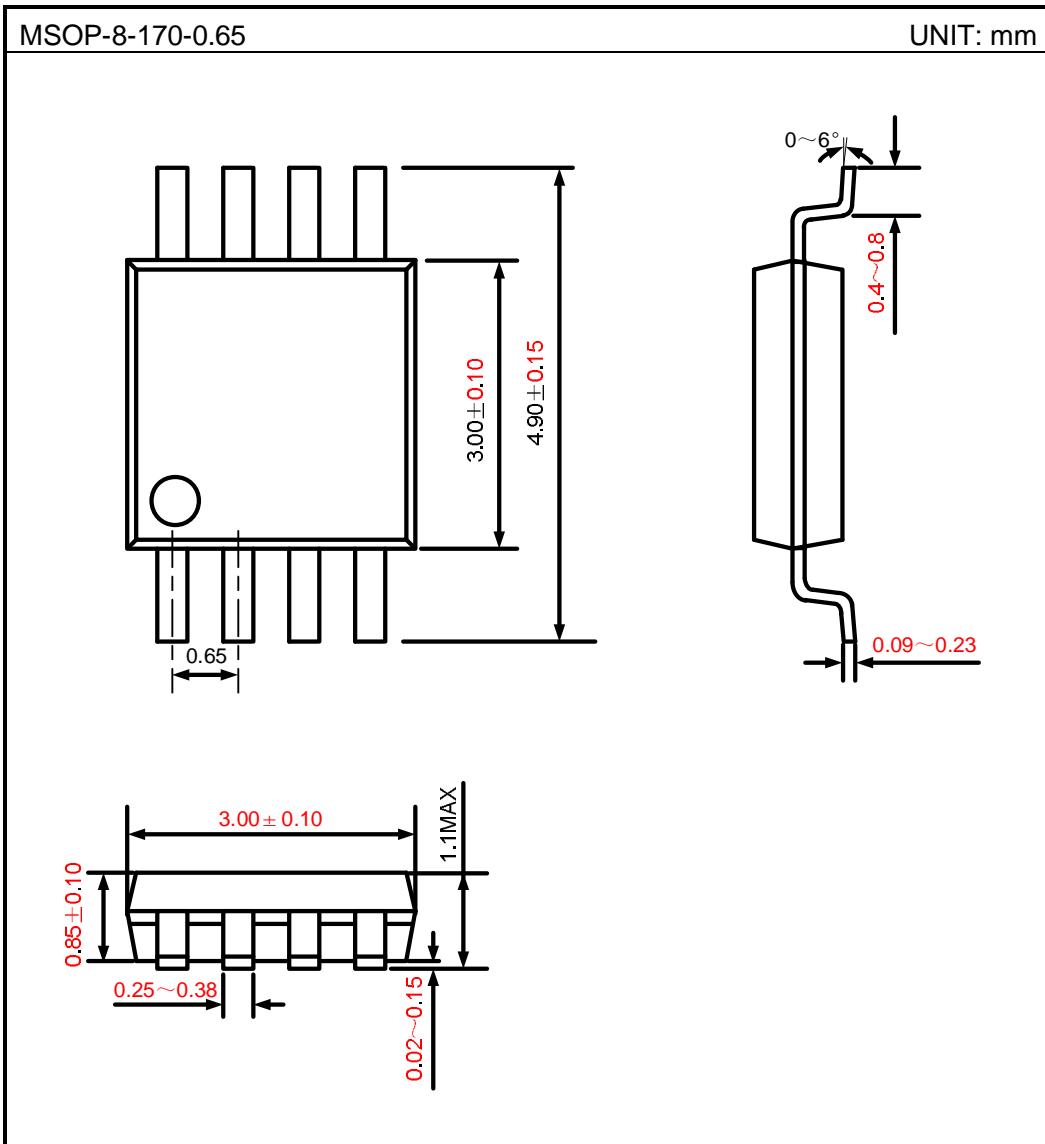
UTC358

LINEAR INTEGRATED CIRCUIT

PACKAGE OUTLINE



PACKAGE OUTLINE



Attach

Revision History

Data	REV	Description	Page
	1.0	Original	
2006.03.27	1.1	Add "Ordering Information"	1
2007.02.27	1.2	Add Package Outline "MSOP-8-0.65"	5

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