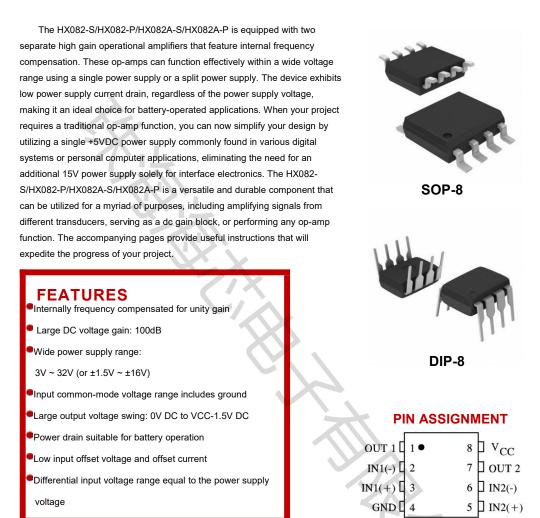
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HX082-S/HX082-P/HX082A-S/HX082A-P JFET Low Power Dual Operational Amplifier



Product Information						
	Package Information	temperature	Orchestration	quantity		
HX082-S	SOP-8	0°C~70°C	Taping	2500		
HX082-P	DIP-8	0°C~70°C	Taping	1000		
HX082A-S	SOP-8	-40°C~85°C	Taping	2500		
HX082A-P	DIP-8	-40°C~85°C	Taping	1000		

RECOMMENDED OPERATING CONDITIONS						
Symbol	Parameter	Min	Max	Unit		
V _{CC}	DC Supply Voltage	±2.5 or 5.0	±15 or 30	V		
T _A	Operating Temperature, All Package Types	0	+70	°C		

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MAXIMUM RATINGS					
Symbol	Parameter	Value	Unit		
V _{CC}	Power Supply Voltages	32 ±16	V		
VIDR	Input Differential Voltage Range a	±32	V		
VICR	Input Common Mode Voltage Range	-0.3 to 32	V		
I _{SC}	Output Short Circuit Duration	Continuous			
TJ	Junction Temperature	150	°C		
Tstg	Storage Temperature	-55 to +125	°C		
I _{IN}	Input Current, per pin b	50	mA		
TL	Lead Temperature, 1mm from Case for 10 Seconds	260	°C		

Notes

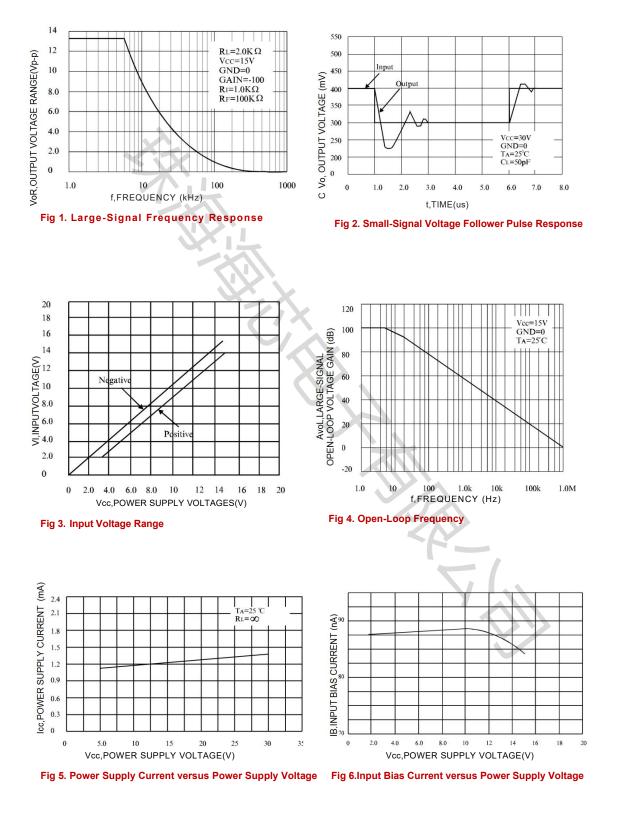
a. Split Power Supplies.b. VIN<-0.3V. This input current will only exist when voltage at any of the input leads is driven negative.

DC EL	ECTRICAL CHARA	CTERISTICS(TA=0 to +70°	C)			
o			Guaranteed Limit			
Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
V _{IO}	Maximum Input Offset Voltage	$\begin{array}{c} V_O = 1.4 V \; V_{CC} = 5.030 V; R_S = 0 \; \Omega \\ V_{ICM} = 0 \; V \; to \; V_{CC} 1.7 V \end{array}$			9.0	mV
$\Delta V_{IO} / \Delta T$	Input Offset Voltage Drift	R _S =0Ω, V _{CC} =30V		7.0		µV/。 C
I _{IO}	Maximum Input Offset Current	V _{CC} =5.0V			150	nA
$\Delta I_{IO} / \Delta T$	Input Offset Current Drift	R _S =0Ω, V _{CC} =30V		10		pAV。C
I _{IB}	Maximum Input Bias Current	V _{CC} =5.0V			-500	nA
V _{ICR}	Input Common Mode Voltage Range	V _{CC} =30V	0		28	V
Icc	Maximum Power Supply Current	$R_{L}=,V_{CC}=30V,V_{0}=0V$			3	mA
A _{VOL}	Minimum Large Signal Open-Loop Voltage Gain	V_{CC} =15V, $R_L \ge 2K\Omega$	15			V/mV
V _{OH}	Minimum Output High- Level Voltage Swing	V _{CC} =30V,R _L =2KΩ V _{CC} =30V, R _L =10KO	26 27			V
V _{OL}	Maximum Output Low- Level Voltage Swing	V_{CC} =5V,RL=10K Ω			20	mV
CMR	Common Mode Rejection	V _{CC} =30V, R _S =10KΩ	65a			dB
PSR	Power Supply Rejection	V _{CC} =30V	65			dB
CS	Channel Separation	f=1KHz to 20KHz,V _{CC} =30V	-120a			dB
I _{SC}	Maximum Output Short Circuit to GND	V _{CC} =5.0V			60*	mA
I _{source}	Minimum Source Output Current	$V_{IN+}=1V, V_{IN-}=0V, V_{CC}=15V, V_{0}=0V$	10			mA
sink	Current	V _{IN+} =0V, V _{IN-} =1V, V _{CC} =15V,V ₀ =15V V _{IN+} =0V, V _{IN-} =1V, V _{CC} =15V, V ₀ =0.2V	5 12₄			mΑ μA
VIDR		All V _{IN} ≥GND or V-Supply (if used)			Vcca	V

Notes a. =@25°C

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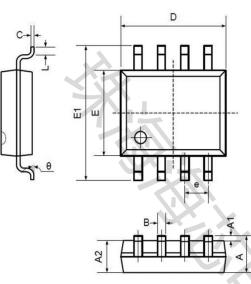
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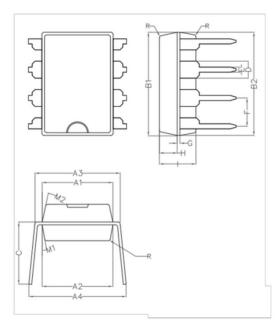
Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
A	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
В	0.330	0.510	0.013	0.020	
С	0.190	0.250	0.007	0.010	
D	4.780	5.000	0.188	0.197	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.300	0.228	0.248	
е	1.270TYP		0.050	TYP	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	

SOP8 (Package Outline Dimensions)

DIP8 (Package Outline Dimensions)



Symbol	Min	Non	Max
A1	6.28	6.33	6.38
A2	6.33	6.38	6.43
A3	7.52	7.62	7.72
A4	7.80	8.40	9.00
B1	9.15	9.20	9.25
B2	9.20	9.25	9.30
С		5.57	
D		1.52	
E	0.43	0.45	0.47
F		2.54	
G		0.25	
Н	1.54	1.59	1.64
I	3.22	3.27	3.32
R		0.20	
M1	9°	10°	11°
M2	11°	12°	13°

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