HX4428-S/HX4428-P

Dual 1.5A-Peak Low-Side MOSFET Drivers

General Description

The HX4426/4427/4428 series of dual low-side MOSFET drivers are manufactured on a BiCMOS/DMOS process, providing efficient power usage and high reliability. These drivers translate input logic levels from TTL or CMOS to output voltage levels that swing within 25 mV of the positive supply or ground. This is in contrast to bipolar devices, which can only swing to within 1V of the supply. The HX4426/4427/4428 drivers are available in three configurations: dual inverting, dual non-inverting, and one inverting plus one non-inverting output. They are designed replace the HX426/427/428 HX1426/1427/1428, offering improved electrical performance and ruggedness.

The HX4426/4427/4428 drivers are highly durable, capable of withstanding up to 500 mA of reverse current (either polarity) without latching and up to 5V noise spikes (either polarity) on the ground pins. These drivers are primarily intended for driving power MOSFETs, but are also suitable driving other loads that require low-impedance, high peak current, and fast switching time. Other potential applications include driving heavily loaded clock lines, coaxial cables, or piezoelectric transducers. It is important to note that the total driver power dissipation must not exceed the package's limits. For high power and narrow pulse applications, see HX4126/4127/4128.

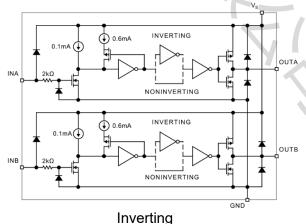
Features

- 1.5A-Peak Output Current
- Latch-Up Protected to >500 mA Reverse Current
- 4.5V to 18V Operating Range
- Switches 1000 pF in 25 ns
- Low Quiescent Supply Current
 - 4 mA at Logic 1 Input
 - 400 μA at Logic 0 Input
- Matched Rise and Fall Times
- 7Ω Output Impedance
- <40 ns Typical Delay
- Logic-Input Protection to –5V
- Logic-Input Threshold Independent of Supply Voltage
- 6 pF Typical Equivalent Input Capacitance
- 25 mV Max. Output Offset from Supply or Ground

Applications

- MOSFET Driver
- Piezoelectric Transducer Driver
- Coax Cable Driver
- Clock Line Driver

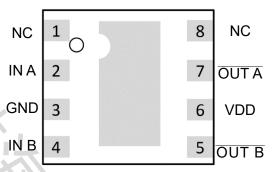
Pin Configuration and Functions



Functional Block Diagram

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Dual Inverting

Pin Diagram

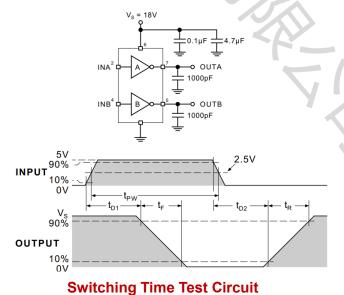
ELECTRICAL CHARACTERISTICS								
Parameter	Sym.	Min.	Тур.	Max.	Units			
Input								
Logic 1 Input Voltage		2.4	1.4	_	V			
	VIH	2.4	1.5	_	V			
Logic 0 Input Voltage			1.1	0.8				
	VIL		1.0	0.8	V			
Input Current	IIN	-1	_	1	μΑ			
Output								
High Output Voltage	VOH	V _s -		_	V			
Low Output Voltage	VOL	27	12	0.025	V			
Outrot Basistana		_ /	6	10	0			
Output Resistance	Ro	_	8	12	Ω			
Peak Output Current	IPK	_	1.5		Α			
Latch-Up Protection	I	>500			mA			
Switching Time								
Rise Time		_	18	30				
	t _r	_	20	40	ns			
Fall Time		_	15	20				
	t_f	_	29	40	ns			
Delay Time		_	17	30	-			
	tD1	_	19	40	ns			
Delay Time	tD2	_	23	50	ns			

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ELECTRICAL CHARACTERISTICS (CONTINUED)									
Parameter	Sym.	Min.	Тур.	Max.	Units				
Pulse Width	tPW	400	_		ns				
Power Supply									
Power Supply Current		0.6	1.4	4.5	mA				
	IS	_	1.5	8					
Power Supply Current		_	0.18	0.4	mA				
	IS	_	0.19	0.6	IIIA				

TEMPERATURE SPECIFICATIONS								
Temperature Ranges								
Maximum Junction Temperature	T _J	_	1	+150	°C			
Storage Temperature Range	Ts	-65	1	+150	°C			
Lead Temperature	_	_	1	+300	°C			
Junction Operating Temperature	T _J	0	1	+70	°C			
Junction Operating Temperature	TJ	-40	1	+85	°C			
Package Thermal Resistances								
Thermal Resistance, PDIP 8-Ld	θЈА	_	130	1	°C/W			
Thermal Resistance, PDIP 8-Ld	θЈС	_	42	1	°C/W			
Thermal Resistance, SOIC 8-Ld	θЈА	_	120		°C/W			
Thermal Resistance, SOIC 8-Ld	θЈС	7	75	_	°C/W			
Thermal Resistance, MSOP 8-Ld	θЈА		250	_	°C/W			

Aplication Informa



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