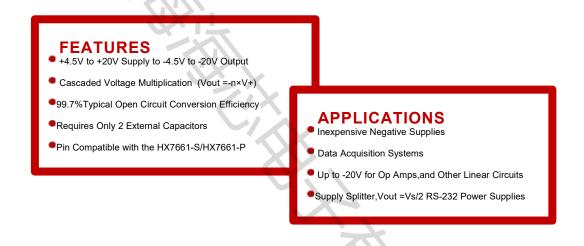
HX7662-S/HX7662-P CMOS Voltage Converters

The HX7662-S/HX7662-P is a monolithic charge pump voltage inverter designed to convert a positive voltage in the range of +4.5V to +20V into the corresponding negative voltage of -4.5V to -20V. Compared to previous implementations of charge pump voltage inverters, the HX7662-S/HX7662-P offers superior performance by combining low quiescent current with high efficiency. It integrates an oscillator, control circuitry, and 4 power MOS switches on-chip, requiring only two low-cost capacitors as external components.





Version 1.1 Date: Oct. 2023

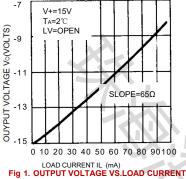
Symbol	Limit	Unit	
V+TO GND	-0.3 +22	V	
Oscillator Input to GND(Note 1)			
V-<12V	-0.3 V++0.3	V	
V+>12V	V+-12.3.V++0.3V	V	
Power Dissipation (Note 2)			
Plastic DIP	300	mW	
SO	500	mW	
TO-99	500	mW	
CERDIP	500	mW	
Operating Temperature Ranges			
Commercial	0 to +70	$^{\circ}\!\mathbb{C}$	
Extended	-40 to +85	$^{\circ}\!\mathbb{C}$	
Military	-55 to +125	$^{\circ}$	
Storage Temperature	-65 to +160	$^{\circ}\!\mathbb{C}$	
ead Temperature	+300	$^{\circ}\mathbb{C}$	

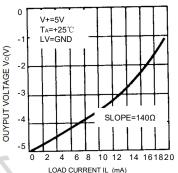
ELECTRICAL CHA	RACTE	RISTICS						
PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS	
Supply Voltage Range-Lo	V+L	RL=10k2,LV=GND	55℃ <ta<+125℃< td=""><td>4.5</td><td></td><td>11</td><td></td></ta<+125℃<>	4.5		11		
Supply Voltage Range-Hi	V+H	RL=10k2,LV=Open	40℃ <t <+85℃<="" td=""><td>9</td><td></td><td>20</td><td rowspan="2">V</td></t>	9		20	V	
Supply Voltage Hallge-III	V +11		55℃< +125℃	9		16.5		
			TA=+25℃		0.25	0.60	mA	
Supply Current	l+	RL=o,LV=Open	L=o,LV=Open 0°C <ta<+70℃< td=""><td></td><td>0.30</td><td>0.85</td></ta<+70℃<>		0.30	0.85		
			55℃ <ta<+125c< td=""><td></td><td>0.40</td><td>1.0</td></ta<+125c<>		0.40	1.0		
			TA=+25℃	l.	60	100	Ω	
Output Source Resistance	Ro	lo =20mA,LV =Open	0°C <ta<+70°c< td=""><td></td><td>70</td><td>120</td></ta<+70°c<>		70	120		
			55°℃ <ta<+125℃< td=""><td></td><td>90</td><td>150</td></ta<+125℃<>		90	150		
	l+	V+=5V, RL=oo,LV=GND	TA=+25℃		20	150	μА	
Supply Current			0℃ <ta<+70℃< td=""><td></td><td>25</td><td>200</td></ta<+70℃<>		25	200		
			-55℃ <ta<+125c< td=""><td></td><td>30</td><td>250</td></ta<+125c<>		30	250		
	Ro	V+=5V, lo =3mA, LV =GND	TA=+25℃		125	200	Ω	
Output Source Resistance			0℃ <ta<+70℃< td=""><td></td><td>150</td><td>250</td></ta<+70℃<>		150	250		
			-55℃ <ta<+125℃< td=""><td></td><td>200</td><td>350</td><td colspan="2"></td></ta<+125℃<>		200	350		
Oscillator Frequency	fosc				10		kHz	
Power Efficiency	Peff	RL=2kQ	TA=+25℃	93	96		- %	
	1 011	TL-ZNQ	Min <ta<max< td=""><td>90</td><td>95</td><td></td></ta<max<>	90	95			
Voltage Conversion	VoEf	RL=∞	Min <ta<max< td=""><td>97</td><td>99.9</td><td></td><td>%</td></ta<max<>	97	99.9		%	
Oscillator Sink or Source	losc	V+=5V(Vosc	=OV to +5V)		0.5		μА	
Current	1030	V+=15V(Vosc	=+5V to+15V)		4.0			

Notes

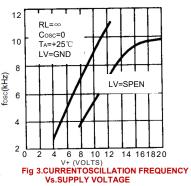
Version 1.1 Date: Oct. 2023 2

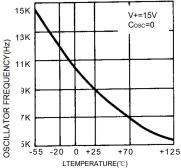
a. Connecting any terminal to voltages greater than V+or less than ground may cause destructive latchup.It is recommended that no input from sources operating from external supplies be applied prior to power-up of the HX7661-S/HX7661-P.
b. Derate linearly above +50°C by 5.5mW/°C c.Pin 1 is a test pin and is not connected in normal use.



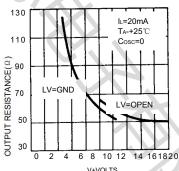


LOAD CURRENT IL (mA)
Fig 2. OUTPUT VOLTAGE VS.LOAD CURRENT





4. UNOADED OSCILLATOR FREQUENCY vs. TEMPERATURE



V+VOLTS
Fig 5. OUTPUT SOURCE RESLSTANCE vs.
SUPPLY VOLTAGE

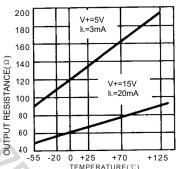


Fig 6.CURRENTO SPURCE RESISTANCE vs. TEMPERATURE

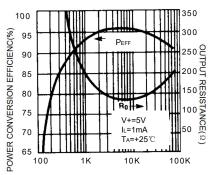
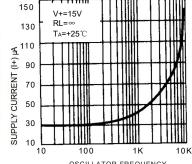
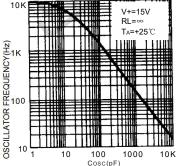


Fig 1. POWER CONVERSION FREQUENCY AND OUTPUT RESISTANCE vs. OSCILLATOR FREQUENCY



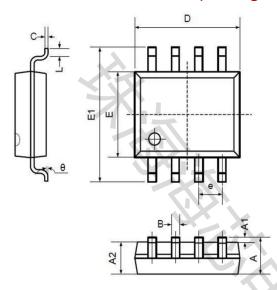
OSCILLATOR FREQUENCY
Fig 8. SUPPLY CURRENT vs. OSCILLATOR
FREQUENCY



Cosc(pF)
Fig 9.OSCILLATION FREQUENCY vs. EXTERNAL OSCILLATOR CAPACITANCE

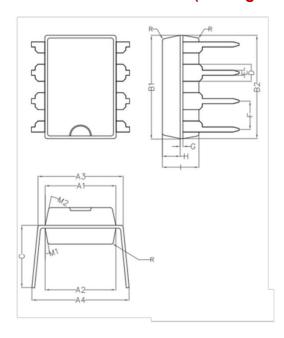
Package Information





Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
Α	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
В	0.330	0.510	0.013	0.020
С	0.190	0.250	0.007	0.010
D	4.780	5.000	0.188	0.197
E	3.800	4.000	0.150	0.157
E1	5.800	6.300	0.228	0.248
е	1.270TYP		0.050TYP	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

DIP8 (Package Outline Dimensions)



Symbol	Min	Non	Max	
A1	6.28	6.33	6.38	
A2	6.33	6.38	6.43	
A3	7.52	7.62	7.72	
A4	7.80	8.40	9.00	
B1	9.15	9.20	9.25	
B2	9.20	9.25	9.30	
С		5.57		
D		1.52		
E	0.43	0.45	0.47	
F		2.54		
G		0.25		
Н	1.54	1.59	1.64	
1	3.22	3.27	3.32	
R		0.20		
M1	9°	10°	11°	
M2	11°	12°	13°	

Version 1.1 4 Date: Oct. 2023

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Version 1.1 5 Date: Oct. 2023

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